

EKI-2728-AE

QA Test Report

8 10/100/1000T Industrial Switch

- **Hardware Version: EASM007740R V1.0**

Abstract:

This document describes the testing environment; configurations; procedures and schedules that QA LAB uses to test EKI-2728-AE.

The document Provides an overview of the kind testing which will be performed against this product.

This document consists of the sections described below:

section 1 : Product Features

section 2 : Testing Environment

section 3 : Capability Testing Item & Result

section 4 : Reliability Testing Item & Result

Test Period	2007/11/01~2007/11/14
Tested by	Bill S.W. Lai
QA Lab	Jim Chou
Project Leader	Gene
Final Approval	Gary Tsai

1. Product Features

Hardware Specification

Switch Architecture	Back-plane (Switching Fabric): 16Gbps Packet throughput ability (Full-Duplex): 23.8Mpps @64bytes
Transfer Rate	14,880pps for Ethernet port 148,800pps for Fast Ethernet port 1,488,000pps for Gigabit Fiber Ethernet port
Memory Buffer	136Kbits
MAC Address	8K MAC address table
Jumbo Frame	9Kbytes
Connector	10/100/1000T: 8 x RJ-45
Network Cable	10Base-T: 2-pair UTP/STP Cat. 3, 4, 5 cable EIA/TIA-568 100-ohm (100m) 100Base-TX: 2-pair UTP/STP Cat. 5 cable EIA/TIA-568 100-ohm (100m)
Protocol	CSMA/CD
LED	Per unit: Power 1 (Green), Power 2 (Green), Fault(Red) Per port: Link/Activity (Green), Speed (1000Mbps Green)
Reserve polarity protection	Present
Overload current protection	Present
Power Supply	12~48 VDC, Redundant power with polarity reverse protect function and removable terminal block
Power Consumption	7.79 Watts
Operating Humidity	5%~95% (Non-condensing)
Operating Temperature	Standard: -10℃~60℃ ; Wide Operating Temperature (-40℃~75℃)
Storage Temperature	-40℃~85℃
Case Dimension	Metal case. IP-30, 30mm (W) x 95mm (D) x 140mm (H)
Installation	DIN Rail and Wall Mount Design
EMI	FCC Class A, CE EN61000-4-2 (ESD), CE EN61000-4-3 (RS), CE EN-61000-4-4 (EFT), CE EN61000-4-5 (Surge), CE EN61000-4-6 (CS), CE EN61000-4-8, CE EN61000-4-11, CE EN61000-4-12, CE EN61000-6-2, CE EN61000-6-4
Safety	UL, cUL, CE/EN60950-1
Stability Testing	IEC60068-2-32 (Free fall), IEC60068-2-27 (Shock), IEC60068-2-6 (Vibration)

V1.03 12-Oct-2007 Issued By Product Dept.

2. Testing Environment

2.1 H/W REQUIREMENT

- Server
 - Netware 3.12 :
 - CPU = Pentium MMX 166 DRAM = 32MB
 - Window NT4.0 :
 - CPU = Pentium MMX 166 DRAM = 48MB
- Client3A-3D :
 - CPU = Pentium - III 700 MHZ DRAM = 64MB
- Client 3E -3H :
 - CPU = Pentium - III 700 MH DRAM = 64MB
- Client 5A-5B :
 - CPU = PENTIUMIV 1.5GHz DRAM = 256MB
- Client 5C – 5D :
 - CPU = PENTIUM III 933MHz DRAM = 128MB
- Client 5E-5H :
 - CPU = AMD A462 1GHz DRAM = 256MB
- Switch HUB :

3Com Switch 3300	FE-1601FM
VIP Switch 2400	Cisco System Catalyst 2900XL
Max Switch II m	Base Switch 2401F
GFA2402M	Flex Switch Hub 2400
GF-24E2M	FE-B2401F
GF2402M	HS-312M

- Converter :

Fast Fiber Converter/SC	CM-A1000L
Module Fast Fiber Converter	
CM-B100L	

- ZyXEL PRESTIGE 600 series ADSL ROUTER
- IXIA 1600T Traffic Generator/Performance Analyzer
- WebCom EZTester-II
- Cables: Unshielded twisted-pair cable (Cat. 5E: 3m, 100m, 120m)
Fiber cable multi mode : 3m,10m and 15m

2.2 S/W REQUIREMENT

- Novell NetWare Version 3.12
- LANtest program Version 1.61
- IxExplorer 4.10.250 Build 29
- IxScriptMate 5.20.SP3

2.3 TOPOLOGY

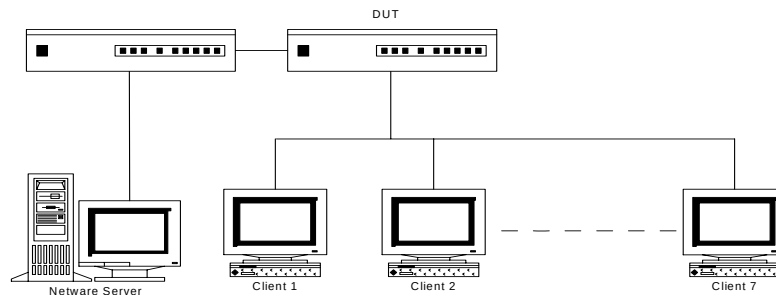


Fig 1. Compatibility and Uplink Test

[Uplink Cable can use Normal cable or Crossover cable.]

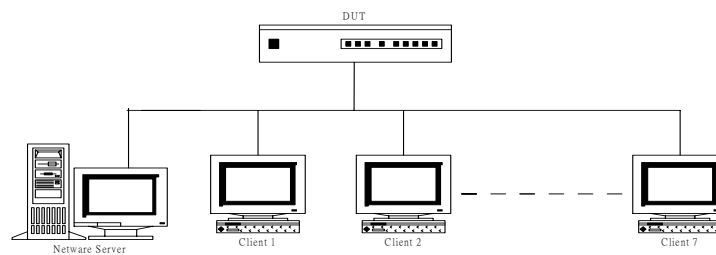


Fig 2. Performance Test

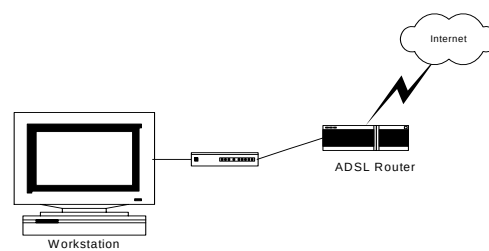


Fig 3. ADSL Test

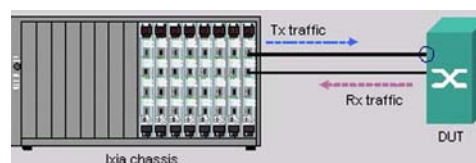


Fig 4. IXIA Test Environment

3. Capability Testing Item & Result

3.1 Test Item

◆ EKI-2728-AE Capability Test

- Basic Function Test
 - UTP PORT CONNECTION TEST
 - LED STATUS TEST
 - ◆ PWR1 ◆ PWR2 ◆ FAULT ◆ LNK/ACT
 - 10/100M/1000M with Full/Half Duplex Test
 - Uplink Test
 - Power Redundant Function and Polarity Reverse Protection Test
 - Throughput Test
 - Frame Loss Test
 - Per Port TX/RX Function Test
 - Architecture Verify
 - Package/Panel Verify
 - Manual Verify
 - Eye Pattern
 - Transmit Jitter
 - Rise and Fall time
 - Differential Output Voltage
 - Clock Frequency Counter
 - IC Input Voltage
 - Peak Differential Output Voltage
 - Maximum Output Droop
-

3.2 Testing Result:

Capability Test List (1)

Case	Describe	Item	Result		Comment
			OK	NG	
1	Basic Function Test	1. UTP PORT CONNECTION TEST	✓		
		2. LED STATUS TEST			
		● PWR1	✓		
		● PWR2	✓		
		● FAULT	✓		
		● LNK/ACT	✓		
		3. 10/100M/1000M with Full/Half Duplex Test	✓		
		4. UPLINK TEST	✓		
		5. Power Redundant Function and Polarity Reverse Protection	✓		
2	Throughput Test	Find the maximum rate of the DUT's forwarding packet.	✓		Appendix 1A
3	Frame Loss Test	Find percentage of frames not forwarding during a constant load.	✓		Appendix 1B
4	Per Port Tx/Rx Function Test	Test different Media type, to check any CRC and ERROR packet or not.	✓		Appendix 1C I Appendix 1L

Capability Test List (2)

Case	Describe	Item	Result		Comment
			OK	NG	
5	Architecture Verify	Check the assemblage of parts of the DUT.	✓		
6	Package/Panel Verify	Follow Manual's descriptions to check the DUT's attachments, package and panel.	✓		
7	Manual Verify	See manual to check it right or not.	✓		
8	Eye Pattern	The Red Line cannot locate on the hexagon zone more than 100 hits during 6500 waveform counts	✓		To measure the PHY' signal whether follow IEEE 802.3 standard
9	Transmit Jitter	The peak-to-peak transmit jitter shall not exceed 1.4 ns	✓		
10	Rise and Fall time	Rise and Fall Time Range: 3 ns ~ 5 ns, where peak-to-peak time ≤ 0.5 ns	✓		
11	Differential Output Voltage	Vout+: 950 ~ 1050 mV ; Vout-: -1050 ~ -950 mV	✓		
12	Clock Frequency Counter	To measure the Clock Frequency in order to meet the standard of ± 50 ppm	✓		
13	IC Input Voltage	To observe the ripple of IC Input Voltage	✓		
14	Peak Differential Output Voltage	To measure the Peak differential output voltage at the transmit pins of the RJ-45, on all four transmits channels.	✓		
15	Maximum Output Droop	To verify that the transmitter output level does not decay faster than the maximum specified rate	✓		

Appendix 1A

Tested by : IXIA - ixos 4.10.250.29 GA
 IxRouter 4.10.10.3
 IxScriptMate 5.20.SP3
 Copyright ?1997 - 2005 IXIA
 Product Name : EKI-2728-AE
 Version Number : EASM007740 V1.0
 Serial Number : 9626
 User Name : BILL
 Date & Time : 10/31/07 04:49:25 PM
 Test Name/Type : RFC 2544 Throughput Test - Per Port Binary Search
 Protocol : MAC(Ethernet Type 08 00)
 Number of Trials : 1
 Duration of each trial : 00:00:20 sec
 Estimated Run Time : 00:00:44 sec
 Chassis Chain : loopback
 Chassis Id(s) : 1
 Theoretical maximum Frame Rates (frames/second) for different frame sizes (bytes):

Speed	64	128	256	512	1024	1280	1518
10 Mbps	14881	8446	4529	2350	1198	962	813
100 Mbps	148810	84460	45290	23497	11973	9616	8128
1000 Mbps	1488096	844595	452899	234963	119732	96154	81275
10000 Mbps	14880952	8445946	4528986	2349625	1197318	961539	812744
155 Mbps (Oc-3)	288000	145116	72840	36491	18263	14614	12324
622 Mbps (Oc-12)	1152000	580465	291362	145965	73054	58454	49296
2488 Mbps (Oc-48)	4608000	2321860	1165447	583860	292215	233817	197182
9952 Mbps (Oc-192)	18432000	9287442	4661790	2335439	1168859	935270	788730

 =====
 - Port Configuration - One-to-One
 =====

Name	TXchassis.card.port	Speed	Duplex	RXchassis.card.port	Speed	Duplex
Pair1	1.1.1	1000	FULL	1.1.2	1000	FULL
Pair2	1.1.2	1000	FULL	1.1.1	1000	FULL
Pair3	1.1.3	1000	FULL	1.1.4	1000	FULL
Pair4	1.1.4	1000	FULL	1.1.3	1000	FULL
Pair5	1.2.1	1000	FULL	1.2.2	1000	FULL
Pair6	1.2.2	1000	FULL	1.2.1	1000	FULL
Pair7	1.2.3	1000	FULL	1.2.4	1000	FULL
Pair8	1.2.4	1000	FULL	1.2.3	1000	FULL

Appendix 1B

Tested by : IXIA - ixos 4.10.250.29 GA
 IxRouter 4.10.10.3
 IxScriptMate 5.20.SP3
 Copyright ?1997 - 2005 IXIA
 Product Name : EKI-2728-AE
 Version Number : EASM007740 V1.0
 Serial Number : 9626
 User Name : BILL
 Date & Time : 10/31/07 05:37:52 PM
 Test Name/Type : RFC 2544 Frameloss Test with coarse granularity
 Protocol : MAC(Ethernet Type 08 00)
 Number of Trials : 1
 Estimated Run Time : 00:00:32 sec
 Chassis Chain : loopback
 Chassis Id(s) : 1
 Theoretical maximum Frame Rates (frames/second) for different frame sizes (bytes):

Speed	64	128	256	512	1024	1280	1518
10 Mbps	14881	8446	4529	2350	1198	962	813
100 Mbps	148810	84460	45290	23497	11973	9616	8128
1000 Mbps	1488096	844595	452899	234963	119732	96154	81275
10000 Mbps	14880952	8445946	4528986	2349625	1197318	961539	812744
155 Mbps (Oc-3)	288000	145116	72840	36491	18263	14614	12324
622 Mbps (Oc-12)	1152000	580465	291362	145965	73054	58454	49296
2488 Mbps (Oc-48)	4608000	2321860	1165447	583860	292215	233817	197182
9952 Mbps (Oc-192)	18432000	9287442	4661790	2335439	1168859	935270	788730

 - Port Configuration - One-to-One
 =====

Name	TXchassis.card.port	Speed	Duplex	RXchassis.card.port	Speed	Duplex
Pair1	1.1.1	1000	FULL	1.1.2	1000	FULL
Pair2	1.1.2	1000	FULL	1.1.1	1000	FULL
Pair3	1.1.3	1000	FULL	1.1.4	1000	FULL
Pair4	1.1.4	1000	FULL	1.1.3	1000	FULL
Pair5	1.2.1	1000	FULL	1.2.2	1000	FULL
Pair6	1.2.2	1000	FULL	1.2.1	1000	FULL
Pair7	1.2.3	1000	FULL	1.2.4	1000	FULL
Pair8	1.2.4	1000	FULL	1.2.3	1000	FULL

Speed: 1000M Full

Frame Size	64	128	256	512	1024	1280	1518
%Loss	0.000	0.000	0.000	0.000	0.000	0.000	0.000

Speed: 100M Full

Frame Size	64	128	256	512	1024	1280	1518
%Loss	0.000	0.000	0.000	0.000	0.000	0.000	0.000

Speed: 10M Full

Frame Size	64	128	256	512	1024	1280	1518
%Loss	0.000	0.000	0.000	0.000	0.000	0.000	0.000

Speed: 100M Half

Frame Size	64	128	256	512	1024	1280	1518
%Loss	0.000	0.000	0.000	0.000	0.000	0.000	0.000

Speed: 10M Half

Frame Size	64	128	256	512	1024	1280	1518
%Loss	0.000	0.000	0.000	0.000	0.000	0.000	0.000

Data Length:64 byte

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, \dots, 8 \leftrightarrow 1,$

[illegible]

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, 3 \leftrightarrow 4, \dots, 8 \leftrightarrow 1$

[illegible]

Data Length:64 byte

Test pairs: 1<->2, 2<->3....., 8<>1

[illegible]

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, \dots, 8 \leftrightarrow 1$

[illegible]

Data Length:64 byte

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, \dots, 8 \leftrightarrow 1$

[illegible]

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, \dots, 8 \leftrightarrow 1$

[illegible]

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, \dots, 8 \leftrightarrow 1$

[illegible]

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, \dots, 8 \leftrightarrow 1$

[illegible]

Test pairs: $1 \leftrightarrow 2, 2 \leftrightarrow 3, \dots, 8 \leftrightarrow 1$

[illegible]

Appendix 1L

Backbone test 1000M Full Duplex Tx/Rx function test (DUT1 port 8 uplinked with DUT2 port 8)

Transmit time 48 HR

Data Length: Random byte

Test pairs:1<->2,2<->3.....,14<->1

	10.0.0.1:03.01	10.0.0.1:03.02	10.0.0.1:03.03	10.0.0.1:03.04	10.0.0.1:04.01	10.0.0.1:04.02
Link State	Link Up	Link Up	Link Up	Link Up	Link Up	Link Up
Line Speed	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps
Duplex Mode	Full	Full	Full	Full	Full	Full
Frames Sent	3804950891	3804947512	3804952168	3804948528	3804949583	3804945384
Frames Sent Rate	0	0	0	0	0	0
Valid Frames Received	7414197889	7415047020	7414016441	7413673362	7415728489	7414033622
Valid Frames Received Rate	0	0	0	0	0	0
Bytes Sent	3.00971E+12	3.00974E+12	3.00973E+12	3.00975E+12	3.00971E+12	3.0097E+12
Bytes Sent Rate	0	0	0	0	0	0
Bytes Received	3.24073E+12	3.24076E+12	3.24072E+12	3.24069E+12	3.24084E+12	3.24069E+12
Bytes Received Rate	0	0	0	0	0	0
Fragments	0	0	0	0	0	0
Undersize	0	0	0	0	0	0
Oversize	0	0	0	0	0	0
CRC Errors	0	0	0	0	0	0
Vlan Tagged Frames	0	0	0	0	0	0
Flow Control Frames	3609262488	3610096129	3609068929	3608721194	3610779961	3609084039
Alignment Errors	0	0	0	0	0	0
Dribble Errors	0	0	0	0	0	0
Collisions	0	0	0	0	0	0
Late Collisions	0	0	0	0	0	0
Collision Frames	0	0	0	0	0	0
Excessive Collision Frames	0	0	0	0	0	0
Oversize and CRC Errors	0	0	0	0	0	0
User Defined Stat 1	0	0	0	0	0	0
User Defined Stat 2	0	0	0	0	0	0
Capture Trigger (UDS 3)	7414197889	7415047020	7414016441	7413673362	7415728489	7414033622
Capture Filter (UDS 4)	7414197889	7415047020	7414016441	7413673362	7415728489	7414033622
ProtocolServer Transmit	0	0	0	0	0	0
ProtocolServer Receive	0	0	0	0	0	0
Transmit Arp Reply	0	0	0	0	0	0
Transmit Arp Request	0	0	0	0	0	0
Transmit Ping Reply	0	0	0	0	0	0
Transmit Ping Request	0	0	0	0	0	0
Receive Arp Reply	0	0	0	0	0	0
Receive Arp Request	0	0	0	0	0	0
Receive Ping Reply	0	0	0	0	0	0
Receive Ping Request	0	0	0	0	0	0
Transmit Duration(Cleared on Start Tx)	00:00.6	00:00.6	00:00.6	00:00.6	00:00.5	00:00.5
Protocol Server Vlan Dropped Frames	0	0	0	0	0	0
Scheduled Frames Sent	3804950891	3804947512	3804952168	3804948528	3804949583	3804945384
Asynchronous Frames Sent	0	0	0	0	0	0
Port CPU Frames Sent	0	0	0	0	0	0
Scheduled Transmit Duration	00:00.0	00:00.0	00:00.0	00:00.0	00:00.0	00:00.0
Bytes Sent / Transmit Duration	17417261	17417400	17417369	17417470	17417253	17417190
Bits Sent	2.40777E+13	2.40779E+13	2.40779E+13	2.4078E+13	2.40777E+13	2.40776E+13
Bits Sent Rate	0	0	0	0	0	0
Bits Received	2.59258E+13	2.59261E+13	2.59257E+13	2.59255E+13	2.59267E+13	2.59255E+13
Bits Received Rate	0	0	0	0	0	0
Port CPU Status	Ready	Ready	Ready	Ready	Ready	Ready
Port CPU DoD Status	Ready	Ready	Ready	Ready	Ready	Ready

[illegible]

4 Reliability Testing Item & Result

4.1 Test Item

- Thermal/Humidity Cycle Test
 - Cold Start Test
 - Component Temperature Test
-

Reliability Test List (1)

Case	Describe	Item	Result		Comment
			OK	NG	
1.	Thermal/Humidity Test	Temperature & humidity cycle testing (scope: Temperature -40 °C ~ 80 °C , humidity 0%~95%RH)	✓		Appendix 2A
2.	Cold Start Test	Test Condition: Power off Test temperature: -40°C Test Duration: 4 hours (after 3 hours then power on)	✓		Appendix 2B
3.	Component Temperature Test	Keep DUT with full loading in room temperature environment for 2Hrs, then measure the surface temperature of main components within DUT	✓		Appendix 2C

● Thermal/Humidity Cycle Test**Test Equipment :**

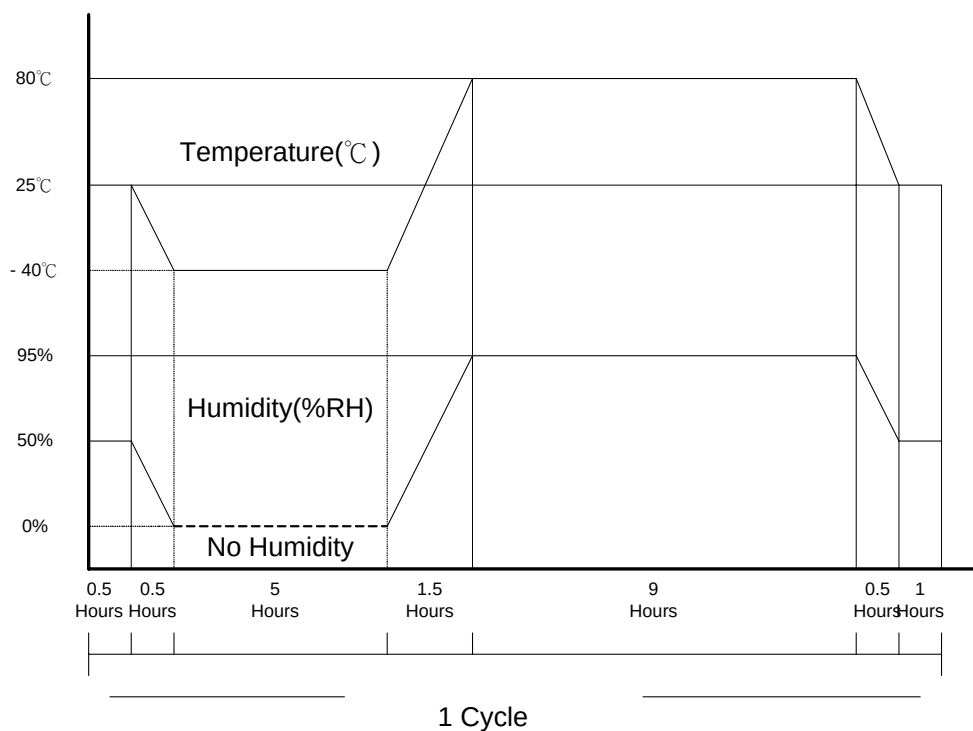
Client = IXIA 1600T

Switch: EKI-2728-AE

KSON Programmable Temperature & Humidity Chamber: THS-A4H+100

Thermal/Humidity Test Curve :

- Test Duration: 3 Cycles (Total of 52.5 hour).
- Operating temperature And humidity test range:
- temperature -40°C to 80°C
- Humidity 0%~95%RH

**Test Method :**

Run IXIA 1600T to inject packets for Thermal/Humidity 3 Cycles.

Test Result: Passed

- Transmit Mode:burst 52.5 HR
- Data Length:Random byte
- Test pairs:1<->2,2<->3.....,8<->1

	10.0.0.1:03.01	10.0.0.1:03.02	10.0.0.1:03.03	10.0.0.1:03.04	10.0.0.1:04.01	10.0.0.1:04.02	10.0.0.1:04.03	10.0.0.1:04.04
Link State	Link Up	Link Up	Link Up	Link Up	Link Up	Link Up	Link Up	Link Up
Line Speed	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps	1000 Mbps
Duplex Mode	Full	Full	Full	Full	Full	Full	Full	Full
Frames Sent	29125518249	29125248334	29125263554	29125350093	29125438680	29125331985	29125495749	29125523257
Frames Sent Rate	0	0	0	0	0	0	0	0
Valid Frames Received	29125523257	29125518249	29125248334	29125263554	29125350093	29125438680	29125331985	29125495749
Valid Frames Received Rate	0	0	0	0	0	0	0	0
Bytes Sent	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13
Bytes Sent Rate	0	0	0	0	0	0	0	0
Bytes Received	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13	2.30382E+13
Bytes Received Rate	0	0	0	0	0	0	0	0
Fragments	0	0	0	0	0	0	0	0
Undersize	0	0	0	0	0	0	0	0
Oversize and Good CRCs	0	0	0	0	0	0	0	0
CRC Errors	0	0	0	0	0	0	0	0
Vlan Tagged Frames	0	0	0	0	0	0	0	0
Flow Control Frames	0	0	0	0	0	0	0	0
Alignment Errors	0	0	0	0	0	0	0	0
Dribble Errors	0	0	0	0	0	0	0	0
Collisions	0	0	0	0	0	0	0	0
Late Collisions	0	0	0	0	0	0	0	0
Collision Frames	0	0	0	0	0	0	0	0
Excessive Collision Frames	0	0	0	0	0	0	0	0
Oversize and CRC Errors	0	0	0	0	0	0	0	0
User Defined Stat 1	0	0	0	0	0	0	0	0
User Defined Stat 2	0	0	0	0	0	0	0	0
Capture Trigger (UDS 3)	29125523257	29125518249	29125248334	29125263554	29125350093	29125438680	29125331985	29125495749
Capture Filter (UDS 4)	29125523257	29125518249	29125248334	29125263554	29125350093	29125438680	29125331985	29125495749
ProtocolServer Transmit	0	0	0	0	0	0	0	0
ProtocolServer Receive	0	0	0	0	0	0	0	0
Transmit Arp Reply	0	0	0	0	0	0	0	0
Transmit Arp Request	0	0	0	0	0	0	0	0
Transmit Ping Reply	0	0	0	0	0	0	0	0
Transmit Ping Request	0	0	0	0	0	0	0	0
Receive Arp Reply	0	0	0	0	0	0	0	0
Receive Arp Request	0	0	0	0	0	0	0	0
Receive Ping Reply	0	0	0	0	0	0	0	0
Receive Ping Request	0	0	0	0	0	0	0	0
Transmit Duration(Cleared on Start Tx)	30:00.2	30:00.2	30:00.2	30:00.2	30:00.3	30:00.3	30:00.2	30:00.2
Protocol Server Vlan Dropped Frames	0	0	0	0	0	0	0	0
Scheduled Frames Sent	29125518249	29125248334	29125263554	29125350093	29125438680	29125331985	29125495749	29125523257
Asynchronous Frames Sent	0	0	0	0	0	0	0	0
Port CPU Frames Sent	0	0	0	0	0	0	0	0
Scheduled Transmit Duration	00:00.0	00:00.0	00:00.0	00:00.0	00:00.0	00:00.0	00:00.0	00:00.0
Bytes Sent / Transmit Duration	121894994	121895129	121895096	121895250	121895257	121894944	121895147	121894862
Bits Sent	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14
Bits Sent Rate	0	0	0	0	0	0	0	0
Bits Received	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14	1.84E+14
Bits Received Rate	0	0	0	0	0	0	0	0
Port CPU Status	Ready	Ready	Ready	Ready	Ready	Ready	Ready	Ready
Port CPU DoD Status	Ready	Ready	Ready	Ready	Ready	Ready	Ready	Ready

● Cold Start Test**Test Equipment:**

Client = IXIA 1600T

Switch: EKI-2728-AE

KSON Programmable Temperature & Humidity Chamber: THS-A4H+100

Test Conditions:

1. Test Condition: Power off
2. Test temperature: -40°C
3. Test Duration: 4 hours (after 3 hours then power on)

Procedure:

1. After 3 hours then power on.
2. Use IXIA 1600T to inject 500,000,000 packet into DUT.
3. To observe having or not error packet and packet loss.

Expectation:

After stress test, using IXIA 1600T to perform traffic test, there are no CRC errors, Alignment errors, Oversize and packets loss.

Test Result: Passed

- Transmit Mode burst 500,000,000 burst
- Data Length: Random byte
- Test pairs:1<->2,2<->3.....,8<->1

[illegible]

Appendix 2C

● Component Temperature Test

Location	Component Name	Radiator	Temperature (25℃)		
			DC 48V	DC 24V	DC 12V
D3	KBL406G	No	47.0℃	48.1℃	53.0℃
D5	KBL406G	No	47.8℃	50.1℃	58.0℃
U5	E-L4973D	No	70.3℃	63.4℃	64.1℃
C20	E/C 100uF/63V	No	55.1℃	54.7℃	58.2℃
C25	E/C 100uF/63V	No	49.4℃	49.8℃	53.2℃
F1	PTC 60V	No	50.6℃	52.1℃	59.2℃
D1	FM560	No	65.1℃	63.1℃	63.2℃
L2	COIL	No	65.2℃	63.5℃	63.6℃
U6	VITESSE 7398	Yes	71.3℃	71.0℃	71.5℃
L1	COIL	No	65.2℃	65.2℃	65.6℃
U2	MVPG31	No	68.0℃	68.0℃	68.3℃
Y1	OSC 25M	No	59.9℃	60.2℃	60.4℃
Case top			37.4℃	38.0℃	38.6℃
Case bottom			48.7℃	48.8℃	49.7℃
Test Environment Temperature			24.4℃	24.8℃	25.2℃

Test Result: **Passed**