

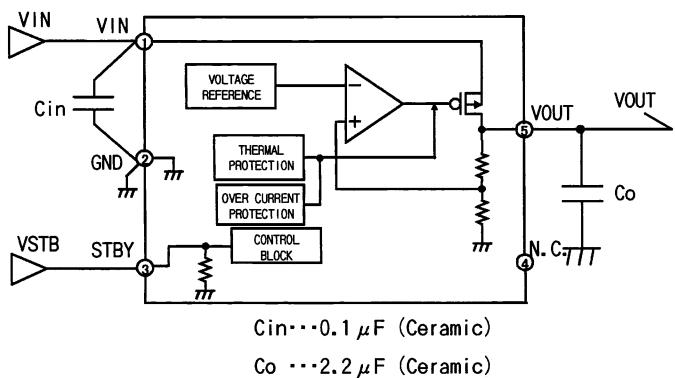
STRUCTURE Silicon Monolithic Integrated Circuit

PRODUCT CMOS Type series regulator

TYPE

B H F B 1 WG Series

BLOCK DIAGRAM and APPLICATION CIRCUIT



PIN DESCRIPTION

PIN No.	PIN NAME	DESCRIPTION
1	VIN	INPUT Pin
2	GND	GROUND Pin
3	STBY	OUTPUT CONTROL(High:ON, Low:OFF)
4	N.C.	NO CONNECT
5	VOUT	OUTPUT Pin

Fig.1 BLOCK DIAGRAM and APPLICATION CIRCUIT

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VMAX	-0.3 ~ +6.5	V
Power Dissipation	Pd	540 (Note.1)	mW
Operating Temperature Range	Topr	-30 ~ +85	°C
Storage Temperature Range	Tstg	-55 ~ +125	°C

Note. 1 Pd derated at 5.4mW/°C for temperature above Ta=25°C,
mounted on 70mm×70mm×1.6mm glass-epoxy PCB.

Application example

The application circuit is recommended for use. Make sure to confirm the adequacy of the characteristics.

When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including static and transitional characteristics as well as dispersion of the IC.

Note that ROHM cannot provide adequate confirmation of patents.

The product described in this specification is designed to be used with ordinary electronic equipment or devices (such as audio-visual equipment, office-automation equipment, communications devices, electrical appliances, and electronic toys).

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RECOMMENDED OPERATING RANGE

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VIN	2.5~5.5	V
Output MAX Current	I _{MAX}	150	mA

● ELECTRICAL CHARACTERISTICS

(Ta=25°C, VIN=VOUT+1.0V, STBY=1.5V, Cin=0.1 μF, Co=1 μF, unless otherwise noted.)

Parameter	Symbol	Limit			Unit	Conditions
		Min.	Typ.	Max.		
【REG】						
Output Voltage	VOUT	VOUT×0.99	VOUT	VOUT×1.01	V	IOUT=1mA
Circuit Current	IGND	–	40	70	μA	IOUT=50mA
Circuit Current (STBY)	ISTBY	–	–	1.0	μA	STBY=0V
Ripple Rejection Ratio	RR	–	70	–	dB	VRR=–20dB, fRR=1kHz, IOUT=10mA
Load Response 1	LTV1	–	50	–	mV	IOUT=1mA to 30mA
Load Response 2	LTV2	–	50	–	mV	IOUT=30mA to 1mA
Input output voltage difference	VSAT	–	250	450	mV	VIN=0.98*VOUT, IOUT=100mA
Line Regulation	VDL1	–	2	20	mV	VIN=VOUT+1.0V to 5.5V, IOUT=50mA
Load Regulation 1	VDL01	–	10	30	mV	IOUT=1mA to 100mA
Load Regulation 2	VDL02	–	15	90	mV	IOUT=1mA to 150mA
【OCP】						
Limit Current	ILMAX	–	250	–	mA	Vo=VOUT*0.98
Short Current	ISHORT	–	50	–	mA	Vo=0V
【STBY】						
STBY Pull-down Resistor	RSTB	550	1100	2200	kΩ	
STBY Control Voltage	ON	VSTBH	1.5	–	VIN	V
	OFF	VSTBL	–0.3	–	0.3	V

●This product is not designed for protection against radio active rays.

●RECOMMENDED OPERATING CONDITION

PARAMETER	Symbol	MIN	TYP	MAX	UNIT	CONDITION
Input Capacitor	Cin	0.1	-	-	μF	Ceramic capacitor recommended
Output Capacitor	Co	1.0 (Note2)	2.2	-	μF	Ceramic capacitor recommended

Note.2 Recommended 2.2 μ F ceramic capacitor (All temperature range),
since an output may become unstable at the time of low temperature and light load. (Fig.5 Reference)

TEST CIRCUIT

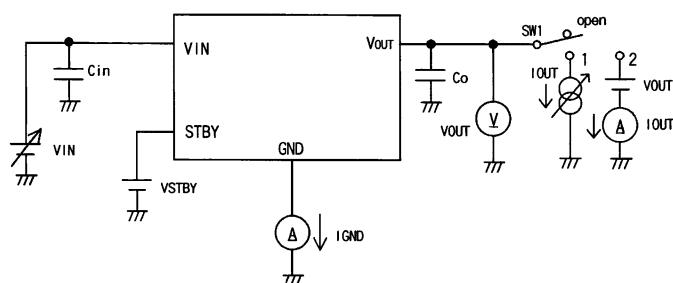


Fig.2 TEST CIRCUIT

○ Power Dissipation Reduction

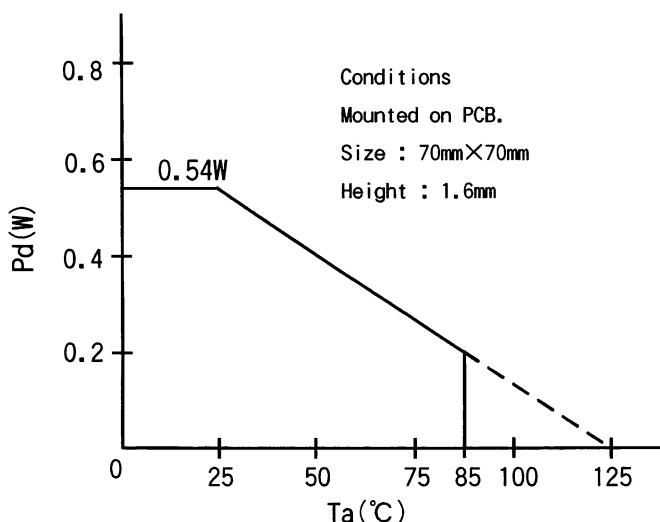


Fig.3 Pd reduction (example)

○ Device Name and Marking

Device Name : BH FB1WG

a

Symbol	Description		Device Mark
	□□	Output Voltage	
a	25	2.5V typ.	CD
	28	2.8V typ.	CE
	29	2.9V typ.	CF
	30	3.0V typ.	CG
	31	3.1V typ.	CH
	33	3.3V typ.	CJ

○ Package dimensions (SSOP5)

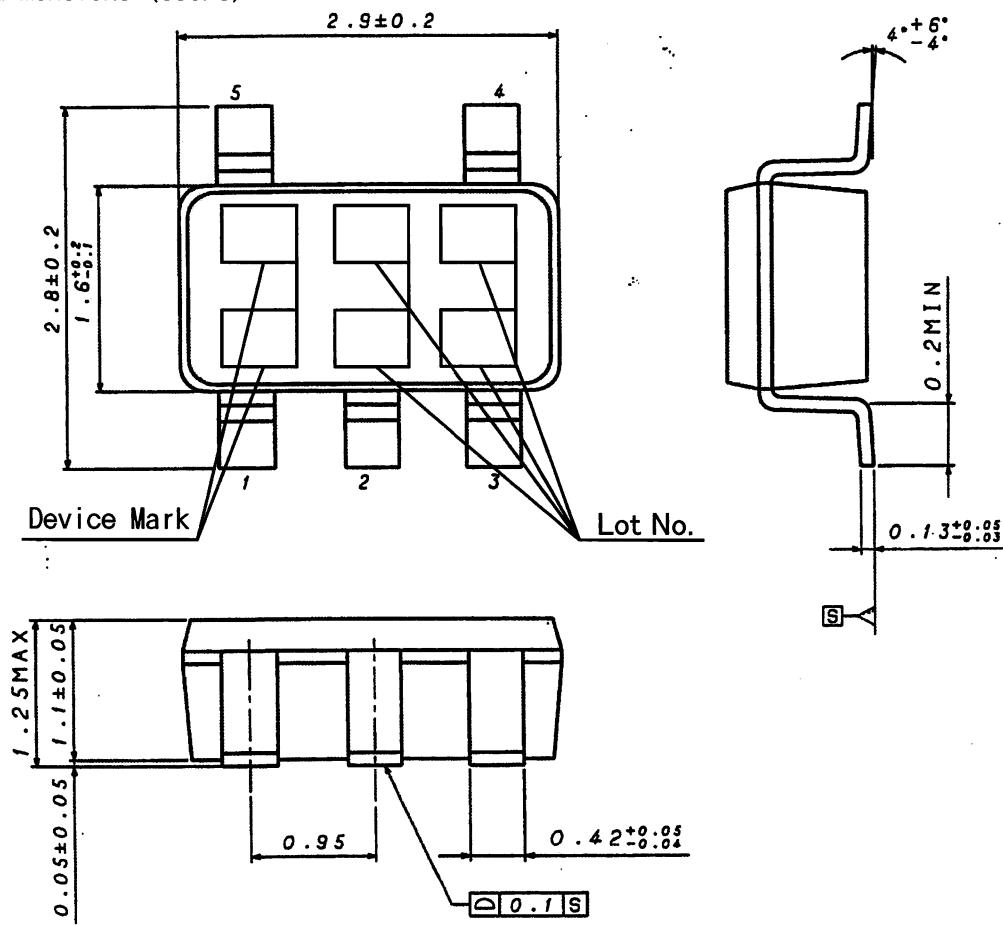


Fig.4 Package dimensions (UNIT:mm)

○Operation Notes

1.) Absolute maximum ratings

May be destroyed if it is operated beyond its absolute maximum ratings. If the device is destroyed in exceeding the recommended maximum ratings, the failure mode will be difficult to determine. (E.g. short mode, open mode) Therefore, physical protection counter-measures (like fuse) should be implemented when operating conditions are beyond the absolute maximum ratings specified.

2.) GND potential

GND potential must be the lowest potential no matter what may happen. Actually, including transitional states, all pins except GND must not be the voltage below GND.

3.) Setting of heat

Consider P_d of actually using states, carry out the heat design that have adequate margin.

4.) Pin short and mistake fitting

When mounting the IC on the PCB, pay attention to the orientation of the IC. If there is a placement mistake, the IC may be burned up.

5.) Actions in strong magnetic field

Using the IC within a strong magnetic field may cause a malfunction.

6.) Mutual impedance

Use short and wide wiring tracks for the power supply and ground to keep the mutual impedance as small as possible. Use a capacitor to keep ripple to a minimum.

7.) Voltage of STB pin

For standby mode, set STB voltage below 0.3V. For normal operation, set the pin voltage beyond 1.5V. It is not recommended to set STB voltage between 0.3V and 1.5V, and it may cause improper operation.

8.) Over current protection circuit

Over current and short circuit protection is built-in at the output, and IC destruction is prevented at the time of load short circuit. These protection circuits is effective in the destructive prevention by the sudden accident, please avoid use to which a protection circuit operates continuously.

9.) Thermal shutdown

In cases of operation at high temperature, thermal shut-down will be activated and output will be turned off. Once IC is returned on normal operating temperature, the output will be turned back on.

10.) Output capacitor

To prevent oscillation at output, it is recommended that the IC be operated at the stable region show as Fig.5. (When the output capacitor is $1.0\ \mu F$ output may become unstable at low temperature and light load.) Output capacitor value above $2.2\ \mu F$ have shown stable output operation range under varying load conditions.

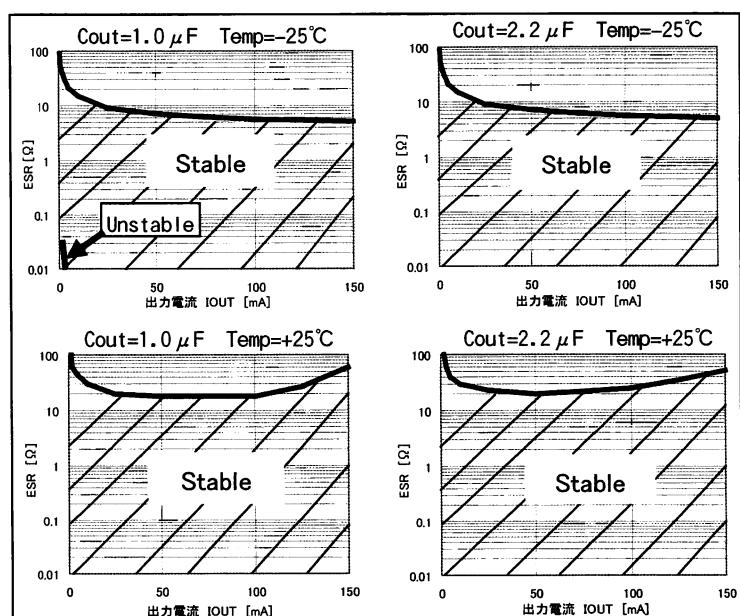


Fig.5 Stable region (Example)

Appendix

Notes

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