



TSM1012

LOW CONSUMPTION VOLTAGE AND CURRENT CONTROLLER FOR BATTERY CHARGERS AND ADAPTORS

- CONSTANT VOLTAGE AND CONSTANT CURRENT CONTROL
- LOW CONSUMPTION
- LOW VOLTAGE OPERATION
- LOW EXTERNAL COMPONENT COUNT
- CURRENT SINK OUTPUT STAGE
- EASY COMPENSATION
- HIGH AC MAINS VOLTAGE REJECTION

VOLTAGE REFERENCE

- FIXED OUTPUT VOLTAGE REFERENCE 1.25V
- 0.5% AND 1% VOLTAGE PRECISION

DESCRIPTION

TSM1012 is a highly integrated solution for SMPS applications requiring CV (constant voltage) and CC (constant current) mode.

TSM1012 integrates one voltage reference and two operational amplifiers (with ORed outputs - common collectors).

The voltage reference combined with one operational amplifier makes it an ideal voltage controller. The other operational, combined with few external resistors and the voltage reference, can be used as a current limiter.

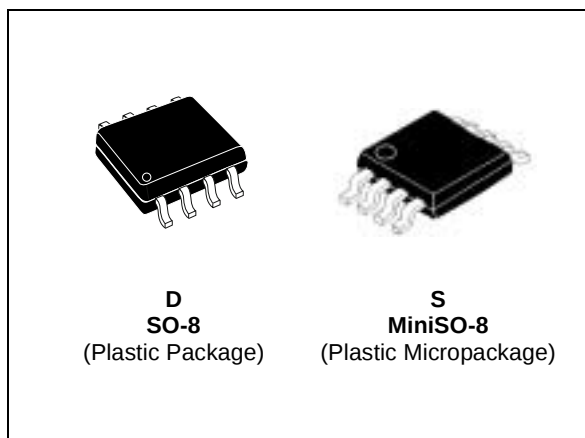
APPLICATIONS

- ADAPTERS
- BATTERY CHARGERS

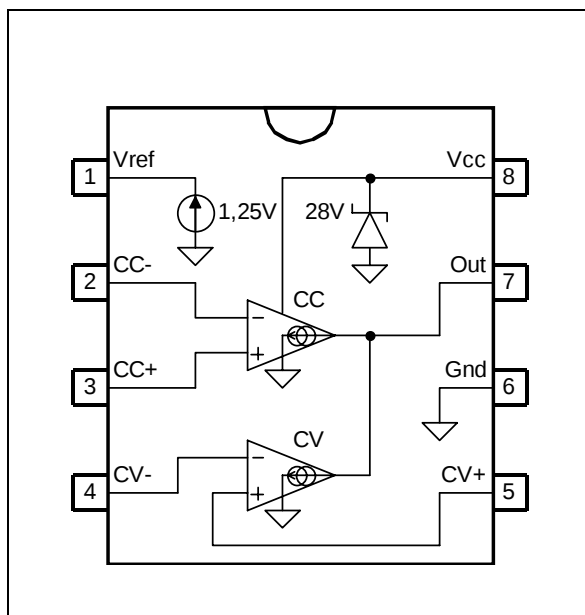
ORDER CODE

Part Number	Temperature Range	Package		Vref %	Marking
		S	D		
TSM1012I	-40 to 105°C		•	1	M1012
TSM1012AI	-40 to 105°C		•	0.5	M1012A
TSM1012I	-40 to 105°C	•		1	M804
TSM1012AI	-40 to 105°C	•		0.5	M805

D = Small Outline Package (SO) - also available in Tape & Reel (DT)
S = Small Outline Package (MiniSO8) - also available in Tape & Reel (ST)



PIN CONNECTIONS (top view)



PIN DESCRIPTION**SO8 & MiniSO8 Pin out**

Name	Pin #	Type	Function
Vref	1	Analog Output	Voltage Reference
CC-	2	Analog Input	Input pin of the operational amplifier
CC+	3	Analog Input	Input pin of the operational amplifier
CV-	4	Analog Input	Input pin of the operational amplifier
CV+	5	Analog Input	Input pin of the operational amplifier
Gnd	6	Power Supply	Ground Line. 0V Reference For All Voltages
Out	7	Analog Output	Output of the two operational amplifier
Vcc	8	Power Supply	Power supply line.

ABSOLUTE MAXIMUM RATINGS

Symbol	DC Supply Voltage	Value	Unit
Vcc	DC Supply Voltage (50mA =< Icc)	-0.3V to Vz	V
Vi	Input Voltage	-0.3 to Vcc	V
Tstg	Storage temperature	-55 to 150	°C
Tj	Junction temperature	150	°C
Iref	Voltage reference output current	2.5	mA
ESD	Electrostatic Discharge	2	kV
Rthja	Thermal Resistance Junction to Ambient Mini SO8 package	180	°C/W
Rthja	Thermal Resistance Junction to Ambient SO8 package	175	°C/W

OPERATING CONDITIONS

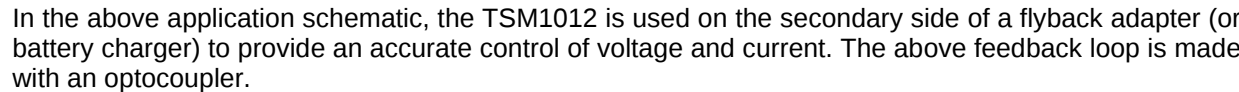
Symbol	Parameter	Value	Unit
Vcc	DC Supply Conditions	4.5 to Vz	V
Toper	Operational temperature	-40 to 105	°C

ELECTRICAL CHARACTERISTICST_{amb} = 25°C and V_{cc} = +18V (unless otherwise specified)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
Total Current Consumption						
I _{cc}	Total Supply Current, excluding current in Voltage Reference ¹⁾ .	V _{cc} = 18V, no load T _{min.} < T _{amb} < T _{max.}		100	180	μA
V _z	V _{cc} clamp voltage	I _{cc} = 50mA		28		V
Operators						
V _{io}	Input Offset Voltage TSM1012 TSM1012A	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.} T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		1 0.5	4 5 2 3	mV
DV _{io}	Input Offset Voltage Drift			7		μV/°C
I _{io}	Input Offset Current	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		2	30 50	nA
I _{ib}	Input Bias Current	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		20 50	150 200	nA
SVR	Supply Voltage Rejection Ration	V _{CC} = 4.5V to 28V	65	100		dB
V _{icm}	Input Common Mode Voltage Range		0		V _{cc} -1.5	V
CMR	Common Mode Rejection Ratio	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	70 60	85		dB
Output stage						
G _m	Transconductance Gain. Sink Current Only ²⁾	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	0.5	1 1		mA/mV
V _{ol}	Low output voltage at 5 mA sinking current	T _{min.} ≤ T _{amb} ≤ T _{max.}		250	400	mV
I _{os}	Output Short Circuit Current. Output to (V _{cc} -0.6V). Sink Current Only	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	6 5	10		mA
Voltage reference						
V _{ref}	Reference Input Voltage TSM1012 1% precision TSM1012A 0.5% precision	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.} T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	1.238 1.225 1.244 1.237	1.25 1.25	1.262 1.273 1.256 1.261	V
ΔV _{ref}	Reference Input Voltage Deviation Over Temperature Range	T _{min.} ≤ T _{amb} ≤ T _{max.}		20	30	mV
RegLine	Reference input voltage deviation over V _{cc} range.	I _{load} = 1mA			20	mV
RegLoad	Reference input voltage deviation over output current.	V _{cc} = 18V, 0 < I _{load} < 2.5mA			10	mV

1. Test conditions: pin 2 and 6 connected to GND, pin 4 and 5 connected to 1.25V, pin 3 connected to 200mV.

2. The current depends on the difference voltage between the negative and the positive inputs of the amplifier. If the voltage on the minus input is 1mV higher than the positive amplifier, the sinking current at the output OUT will be increased by G_m*1mA.



PRINCIPLE OF OPERATION AND APPLICATION HINTS

1. Voltage and Current Control

1.1. Voltage Control

The voltage loop is controlled via a first trans-conductance operational amplifier, the resistor bridge R1, R2, and the optocoupler which is directly connected to the output.

The relation between the values of R1 and R2 should be chosen as written in Equation 1.

$$R1 = R2 \times V_{ref} / (V_{out} - V_{ref}) \quad \text{Eq1}$$

Where V_{out} is the desired output voltage.

To avoid the discharge of the load, the resistor bridge R1, R2 should be highly resistive. For this type of application, a total value of 100K Ω (or more) would be appropriate for the resistors R1 and R2.

As an example, with $R2 = 100\text{K}\Omega$, $V_{out} = 4.10\text{V}$, $V_{ref} = 1.210\text{V}$, then $R1 = 41.9\text{K}\Omega$.

Note that if the low drop diode should be inserted between the load and the voltage regulation resistor bridge to avoid current flowing from the load through the resistor bridge, this drop should be taken into account in the above calculations by replacing V_{out} by $(V_{out} + V_{drop})$.

1.2. Current Control

The current loop is controlled via the second trans-conductance operational amplifier, the sense resistor R_{sense} , and the optocoupler.

V_{sense} threshold is achieved externally by a resistor bridge tied to the V_{ref} voltage reference. Its middle point is tied to the positive input of the current control operational amplifier, and its foot is to be connected to lower potential point of the sense resistor as shown on the following figure. The resistors of this bridge are matched to provide the best precision possible

The control equation verifies:

$$R_{sense} \times I_{lim} = V_{sense} \quad \text{eq2}$$

$$V_{sense} = R5 \times V_{ref} / (R4 + R5)$$

$$I_{lim} = R5 \times V_{ref} / (R4 + R5) \times R_{sense} \quad \text{eq2'}$$

where I_{lim} is the desired limited current, and V_{sense} is the threshold voltage for the current control loop.

Note that the R_{sense} resistor should be chosen taking into account the maximum dissipation (P_{lim}) through it during full load operation.

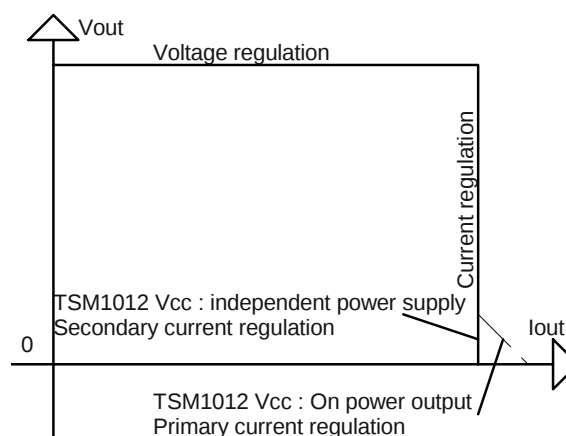
$$P_{lim} = V_{sense} \times I_{lim} \quad \text{eq3}$$

Therefore, for most adapter and battery charger applications, a quarter-watt, or half-watt resistor to make the current sensing function is sufficient.

The current sinking outputs of the two trans-conductance operational amplifiers are common (to the output of the IC). This makes an ORing function which ensures that whenever the current or the voltage reaches too high values, the optocoupler is activated.

The relation between the controlled current and the controlled output voltage can be described with a square characteristic as shown in the following V/I output-power graph.

Figure 3 : Output voltage versus output current



2. Compensation

The voltage-control trans-conductance operational amplifier can be fully compensated. Both of its output and negative input are directly accessible for external compensation components.

An example of a suitable compensation network is shown in Fig.2. It consists of a capacitor $C_{vc1}=2.2\text{nF}$ and a resistor $R_{cv1}=22\text{K}\Omega$ in series.

The current-control trans-conductance operational amplifier can be fully compensated. Both of its output and negative input are directly accessible for external compensation components.

An example of a suitable compensation network is shown in Fig.2. It consists of a capacitor $C_{ic1}=2.2\text{nF}$ and a resistor $R_{ic1}=22\text{K}\Omega$ in series.

3. Start Up and Short Circuit Conditions

Under start-up or short-circuit conditions the TSM1012 is not provided with a high enough supply voltage. This is due to the fact that the chip has its power supply line in common with the power supply line of the system.

Therefore, the current limitation can only be ensured by the primary PWM module, which should be chosen accordingly.

If the primary current limitation is considered not to be precise enough for the application, then a sufficient supply for the TSM1012 has to be ensured under any condition. It would then be necessary to add some circuitry to supply the chip with a separate power line. This can be achieved in numerous ways, including an additional winding on the transformer.

4. Voltage clamp

The following schematic shows how to realize a low-cost power supply for the TSM1012 (with no additional windings). Please pay attention to the fact that in the particular case presented here, this low-cost power supply can reach voltages as high as twice the voltage of the regulated line. Since the Absolute Maximum Rating of the TSM1012 supply voltage is 28V. In the aim to protect the TSM1012 against such high voltage values a internal zener clamp is integrated.

$$R_{limit} = (V_{cc} - V_z) / I_{Vz}$$

Figure 4 : Clamp voltage

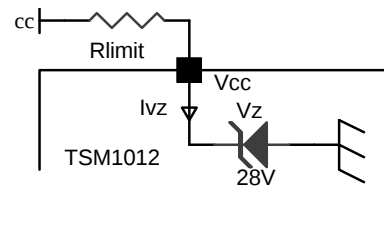
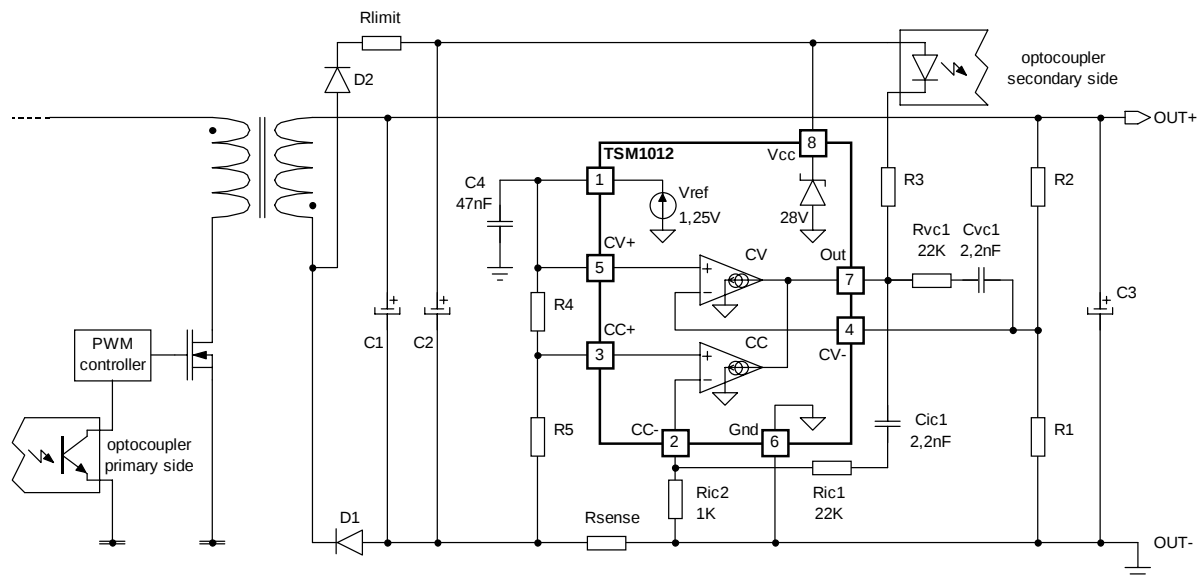


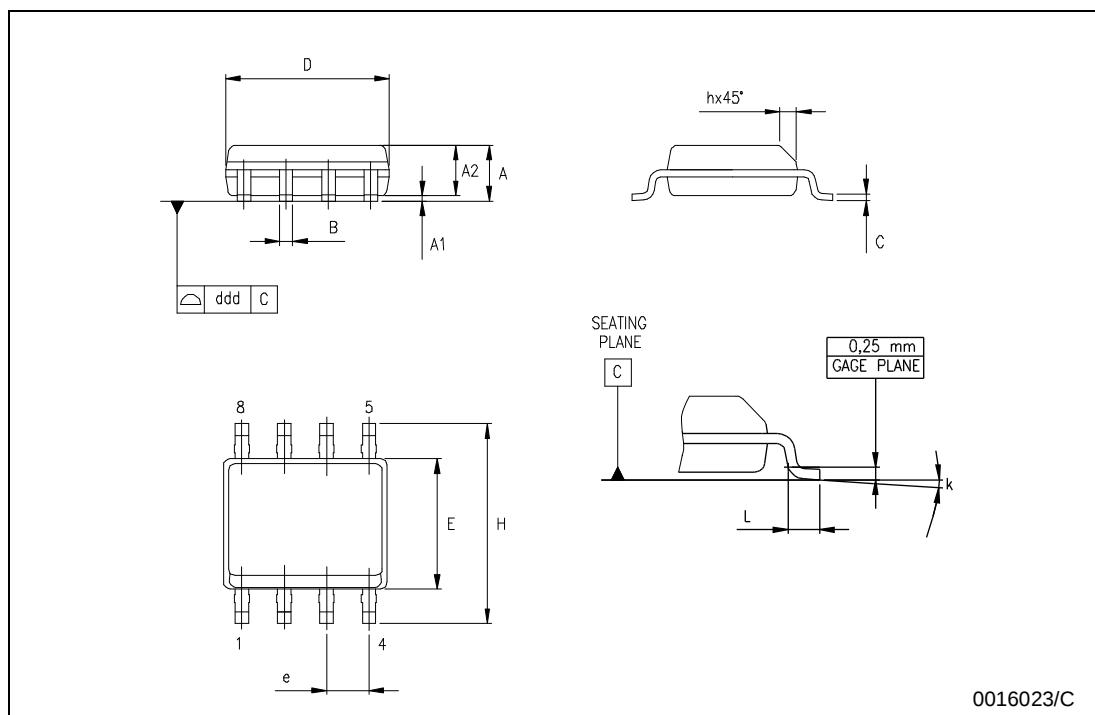
Figure 5 :



PACKAGE MECHANICAL DATA

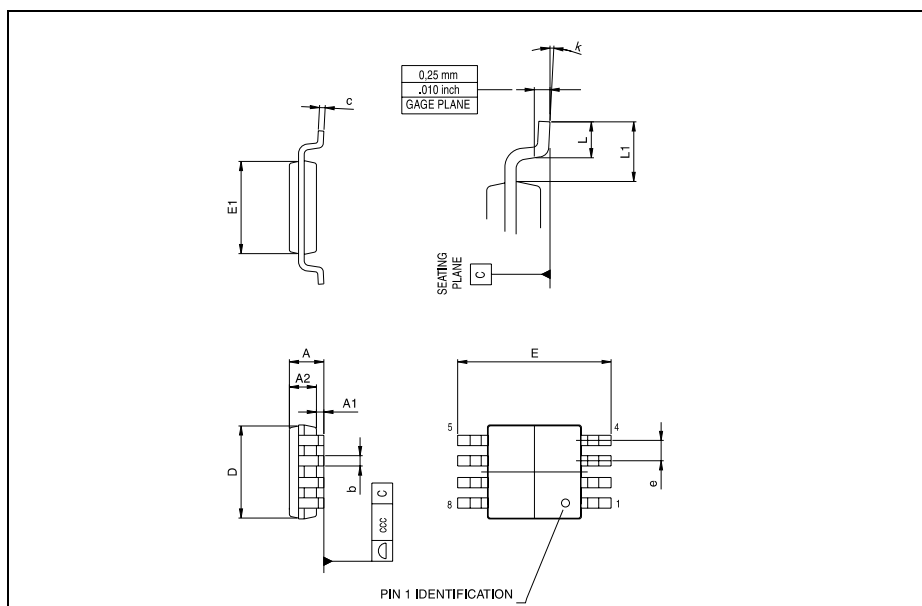
SO-8 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.04		0.010
A2	1.10		1.65	0.043		0.065
B	0.33		0.51	0.013		0.020
C	0.19		0.25	0.007		0.010
D	4.80		5.00	0.189		0.197
E	3.80		4.00	0.150		0.157
e		1.27			0.050	
H	5.80		6.20	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
k	8° (max.)					
ddd			0.1			0.04



PACKAGE MECHANICAL DATA

miniISO-8 MECHANICAL DATA						
DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.1			0.043
A1	0.05	0.10	0.15	0.002	0.004	0.006
A2	0.78	0.86	0.94	0.031	0.031	0.037
b	0.25	0.33	0.40	0.010	0.13	0.013
c	0.13	0.18	0.23	0.005	0.007	0.009
D	2.90	3.00	3.10	0.114	0.118	0.122
E	4.75	4.90	5.05	0.187	0.193	0.199
E1	2.90	3.00	3.10	.0114	0.118	0.122
e		0.65			0.026	
K	0°		6°	0°		6°
L	0.40	0.55	0.70	0.016	0.022	0.028
L1			0.10			0.004



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