



MOTOROLA

MC54/74HCT00

Product Preview

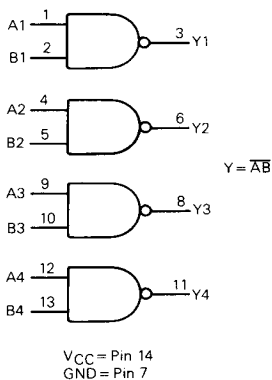
QUAD 2-INPUT NAND GATE (WITH LSTTL-COMPATIBLE INPUTS)

The MC54/74HCT00 may be used as a level converter for interfacing LSTTL to High-Speed CMOS. The inputs of HCT devices are compatible with both LSTTL and CMOS output voltage levels. Therefore, no pullup resistors are required at the inputs of the HCT00 when interfacing with LSTTL.

The HCT00 is identical in pinout to the LS00.

- Compatible with LSTTL Outputs — No Pullup Resistor Required
- Low Power Consumption Characteristic of CMOS Devices
- Output Drive Capability: 10 LSTTL Loads Minimum
- Operating Voltage Range: 4.5 to 5.5 Volts
- Low Quiescent Current Characteristic of CMOS Devices
- Diode Protection on All Inputs

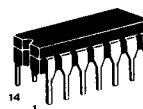
LOGIC DIAGRAM



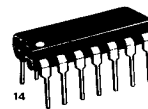
HIGH-PERFORMANCE CMOS

LOW-POWER COMPLEMENTARY MOS
SILICON-GATE

QUAD 2-INPUT NAND GATE (WITH LSTTL-COMPATIBLE INPUTS)



J SUFFIX
CERAMIC PACKAGE
CASE 632



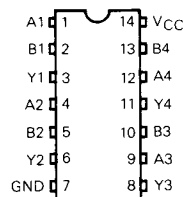
N SUFFIX
PLASTIC PACKAGE
CASE 646

ORDERING INFORMATION

54 Series: -55°C to $+125^{\circ}\text{C}$
MC54HCTXXJ (Ceramic Package Only)

74 Series: -40°C to $+85^{\circ}\text{C}$
MC74HCTXXN (Plastic Package)
MC74HCTXXJ (Ceramic Package)

PIN ASSIGNMENT



This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.