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SN74LVT16374 Status: OBSOLETE

3.3-V ABT 16-Bit Edge-Triggered D-Type Flip-Flops With 3-State Outputs

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Replaced By:

SN74LVTH16374 The device has the SAME FUNCTIONALITY and PINOUT as the compared device but is NOT an exact equivalent. Device Revision

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Other qualified versions of SN74LVT16374

Version	Part Number	Definition
Military	SN54LVT16374	QML certified for Military and Defense Applications

Pricing / Packaging / CAD Design Tools / Samples

			Packaging		CAD Design Tools	Samples
Device	Status	Temp (°C)	Package Pins	Top Side Marking	Footprints	Samples
SN74LVT16374DGGR	OBSOLETE	-40 to 85	TSSOP (DGG) 48	View	View	Not Available
SN74LVT16374DL	OBSOLETE	-40 to 85	SSOP (DL) 48	View	View	Not Available
SN74LVT16374DLR	OBSOLETE	-40 to 85	SSOP (DL) 48	View	View	Not Available

Inventory

	TI Inventory Status				Reported Distributor Inventory			
SN74LVT16374DL	As of 9:31 AM GMT, 12 Nov 2008				As of 9:31 AM GMT, 12 Nov 2008			
	In Stock	In Progress QTY	Date	Lead Time	Region	Company	In Stock	Purchase
	0*			Call**	Americas	Rochester Electronics	4	Buy Now
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Quality & Lead (Pb)-Free Data

If the information you are requesting is not available online at this time, contact one of our [Product Information Centers](#) regarding the availability of this information.

Technical Documents

Application Notes

Shelf-Life Evaluation of Lead-Free Component Finishes (szza046.htm, 8 KB)
24 May 2004 [Abstract](#)

Understanding and Interpreting Standard-Logic Data Sheets (Rev. B) (szza036b.htm, 8 KB)
28 May 2003 [Abstract](#)

TI IBIS File Creation, Validation, and Distribution Processes (szza034.htm, 9 KB)
29 Aug 2002 [Abstract](#)

16-Bit Widebus Logic Families in 56-Ball, 0.65-mm Pitch Very Thin Fine-Pitch BGA (Rev. B) (szza029b.htm, 8 KB)
22 May 2002 [Abstract](#)

Power-Up 3-State (PU3S) Circuits in TI Standard Logic Devices (szza033.htm, 9 KB)
10 May 2002 [Abstract](#)

LVT-to-LVTH Conversion (scea010.htm, 8 KB)
08 Dec 1998 [Abstract](#)

LVT Family Characteristics (Rev. A) (scea002a.htm, 8 KB)
01 Mar 1998 [Abstract](#)

Implications of Slow or Floating CMOS Inputs (Rev. C) (scba004c.htm, 8 KB)
01 Feb 1998 [Abstract](#)

Bus-Interface Devices With Output-Damping Resistors Or Reduced-Drive Outputs (Rev. A) (scba012a.htm, 8 KB)
01 Aug 1997 [Abstract](#)

Live Insertion (sdya012.htm, 8 KB)
01 Oct 1996 [Abstract](#)

Input and Output Characteristics of Digital Integrated Circuits (sdya010.htm, 9 KB)
01 Oct 1996 [Abstract](#)

Understanding Advanced Bus-Interface Products Design Guide (scaa029.pdf, 253 KB)
01 May 1996 [Download](#)

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User Guides

LOGIC Pocket Data Book (Rev. B) (scyd013b.pdf, 6001 KB)
16 Jan 2007 [Download](#)

More Literature

Logic Selection Guide 2007 (Rev. Z) (sdyu001z.pdf, 2862 KB)
24 Apr 2007 [Download](#)

Military Semiconductors Selection Guide 2004-2005 (Rev. D) (sgyc003d.pdf, 964 KB)
10 Aug 2004 [Download](#)

Logic Cross-Reference (Rev. A) (scyb017a.pdf, 2938 KB)
07 Oct 2003 [Download](#)

Military Low Voltage Solutions (sgyn139.pdf, 103 KB)
04 Apr 2001 [Download](#)

Advanced Bus Interface Logic Selection Guide (scyt126.pdf, 464 KB)
09 Jan 2001 [Download](#)

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