

PMBFJ308; PMBFJ309; PMBFJ310

N-channel silicon field-effect transistors

Rev. 4 — 20 September 2011

Product data sheet

1. Product profile

1.1 General description

Symmetrical N-channel silicon junction field-effect transistors in a SOT23 package.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features and benefits

- Low noise
- Interchangeability of drain and source connections
- High gain.

1.3 Applications

- AM input stage in car radios
- VHF amplifiers
- Oscillators and mixers.

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage		-	-	±25	V
V _{GSoff}	gate-source cut-off voltage					
	PMBFJ308	$V_{DS} = 10 \text{ V}; I_D = 1 \mu\text{A}$	-1	-	-6.5	V
	PMBFJ309	$V_{DS} = 10 \text{ V}; I_{D} = 1 \mu\text{A}$	-1	-	-4	V
	PMBFJ310	$V_{DS} = 10 \text{ V}; I_D = 1 \mu\text{A}$	-2	-	-6.5	V



Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I_{DSS}	drain current					
	PMBFJ308	$V_{GS} = 0 \text{ V}; V_{DS} = 10 \text{ V}$	12	-	60	mΑ
	PMBFJ309	$V_{GS} = 0 \text{ V}; V_{DS} = 10 \text{ V}$	12	-	30	mΑ
	PMBFJ310	$V_{GS} = 0 \text{ V}; V_{DS} = 10 \text{ V}$	24	-	60	mΑ
P _{tot}	total power dissipation	up to T _{amb} = 25 °C	-	-	250	mW
y _{fs}	forward transfer admittance	$V_{DS} = 10 \text{ V}; I_D = 10 \text{ mA}$	10	-	-	mS

2. Pinning information

Table 2. Discrete pinning[1]

Pin	Description	Simplified outline	Symbol
1	source		
2	drain		3 - 1
3	gate	1 2	sym060

3. Ordering information

Table 3. Ordering information

Type number	Package	је		
	Name	Description	Version	
PMBFJ308	-	plastic surface mounted package; 3 leads	SOT23	
PMBFJ309				
PMBFJ310				

4. Marking

Table 4. Marking

Type number	Marking code ^[1]
PMBFJ308	48*
PMBFJ309	49*
PMBFJ310	50*

^[1] * = p: Made in Hong Kong.

^[1] Drain and source are interchangeable.

^{* =} t: Made in Malaysia.

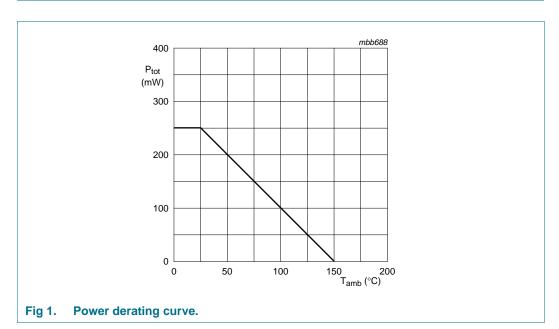
^{* =} W: Made in China.

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage (DC)		-	±25	V
V_{GSO}	gate-source voltage	open drain	-	-25	V
V_{GDO}	gate-drain voltage	open source	-	-25	V
I_{G}	forward gate current (DC)		-	50	mA
P _{tot}	total power dissipation	up to T _{amb} = 25 °C	-	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C



6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Тур	Unit
R _{th(j-a)}	thermal resistance from junction to ambient		<u>[1]</u>	500	K/W

[1] Device mounted on an FR4 printed-circuit board.

7. Static characteristics

Table 7. Static characteristics

 $T_i = 25$ °C; unless otherwise specified.

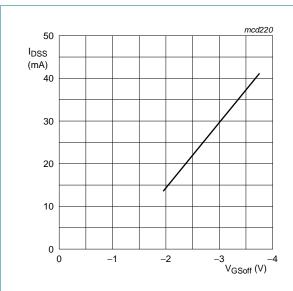
	<u> </u>					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)GSS}$	gate-source breakdown voltage	$I_G = -1 \mu A$; $V_{DS} = 0 V$	-25	-	-	V
V_{GSoff}	gate-source cut-off voltage					V
	PMBFJ308	$I_D = 1 \mu A; V_{DS} = 10 V$	-1	-	-6.5	V
	PMBFJ309	$I_D = 1 \mu A; V_{DS} = 10 V$	-1	-	-4	V
	PMBFJ310	$I_D = 1 \mu A; V_{DS} = 10 V$	-2	-	-6.5	V
V_{GSS}	gate-source forward voltage	$I_G = 1 \text{ mA}; V_{DS} = 0 \text{ V}$	-	-	1	V
I _{DSS}	drain-source leakage current					
	PMBFJ308	$V_{GS} = 0 \text{ V}; V_{DS} = 10 \text{ V}$	12	-	60	mΑ
	PMBFJ309	$V_{GS} = 0 \text{ V}; V_{DS} = 10 \text{ V}$	12	-	30	mΑ
	PMBFJ310	$V_{GS} = 0 \text{ V}; V_{DS} = 10 \text{ V}$	24	-	60	mΑ
I _{GSS}	gate-source leakage current	$V_{GS} = -15 \text{ V}; V_{DS} = 0 \text{ V}$	-	-	-1	nA
R _{DSon}	drain-source on-state resistance	$V_{GS} = 0 \text{ V}; V_{DS} = 100 \text{ mV}$	-	50	-	Ω
y _{fs}	forward transfer admittance	$I_D = 10 \text{ mA}; V_{DS} = 10 \text{ V}$	10	-	-	mS
yos	common source output admittance	$I_D = 10 \text{ mA}; V_{DS} = 10 \text{ V}$	-	-	250	μS

8. Dynamic characteristics

Table 8. Dynamic characteristics

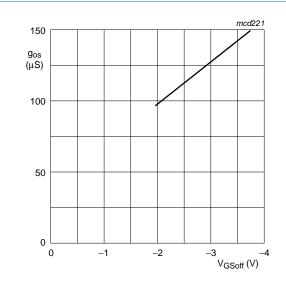
 $T_i = 25$ °C; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
C _{iss}	input capacitance	V _{DS} = 10 V				
		$V_{GS} = -10 \text{ V; } f = 1 \text{ MHz}$	-	3	5	pF
		$V_{GS} = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$	-	6	-	pF
C _{rss}	reverse transfer capacitance	$V_{DS} = 0 \text{ V}; V_{GS} = -10 \text{ V}; f = 1 \text{ MHz}$	-	1.3	2.5	pF
g _{is}	input conductance	$V_{DS} = 10 \text{ V}; I_D = 10 \text{ mA}$				
		f = 100 MHz	-	200	-	μS
		f = 450 MHz	-	3	-	mS
g _{fs} transfer conductance	transfer conductance	$V_{DS} = 10 \text{ V}; I_D = 10 \text{ mA}$				
		f = 100 MHz	-	13	-	mS
		f = 450 MHz	-	12	-	mS
g _{rs}	reverse conductance	$V_{DS} = 10 \text{ V}; I_D = 10 \text{ mA}$				
		f = 100 MHz	-	-30	-	μS
		f = 450 MHz	-	-450	-	μS
g _{os}	output conductance	$V_{DS} = 10 \text{ V}; I_D = 10 \text{ mA}$				
		f = 100 MHz	-	150	-	μS
		f = 450 MHz	-	400	-	μS
V _n	equivalent input noise voltage	$V_{DS} = 10 \text{ V}; I_D = 10 \text{ mA}; f = 100 \text{ Hz}$	-	6	-	nV/√Hz



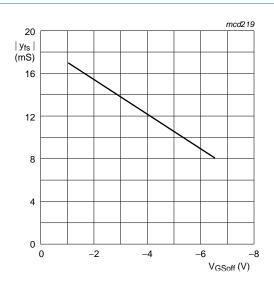
 $V_{DS} = 10 \text{ V}; T_j = 25 ^{\circ}\text{C}.$

Fig 2. Drain current as a function of gate-source cut-off voltage; typical values.



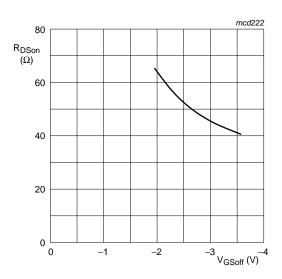
 $V_{DS} = 10 \text{ V}; I_D = 10 \text{ mA}; T_i = 25 \text{ }^{\circ}\text{C}.$

Fig 4. Common-source output conductance as a function of gate-source cut-off voltage; typical values.



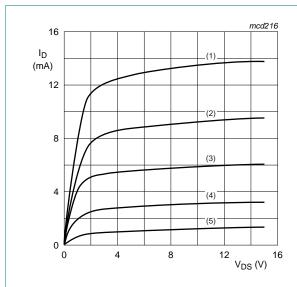
 V_{DS} = 10 V; I_D = 10 mA; T_j = 25 °C.

Fig 3. Forward transfer admittance as a function of gate-source cut-off voltage; typical values.



 V_{DS} = 100 mV; V_{GS} = 0 V; T_j = 25 °C.

Fig 5. Drain-source on-state resistance as a function of gate-source cut-off voltage; typical values.



 $T_i = 25 \, ^{\circ}C$.

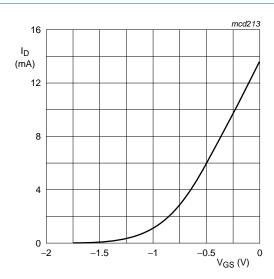
(1)
$$V_{GS} = 0 \text{ V}.$$

(2)
$$V_{GS} = -0.25 \text{ V}.$$

(3)
$$V_{GS} = -0.5 \text{ V}.$$

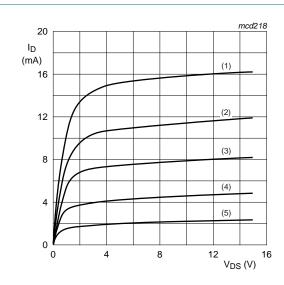
(4)
$$V_{GS} = -0.75 \text{ V}.$$

(5)
$$V_{GS} = -1 \text{ V}.$$



 $V_{DS}=10~V;~T_j=25~^{\circ}C.$

Fig 6. Typical output characteristics; PMBFJ308.



 $T_i = 25 \, ^{\circ}C$.

(1)
$$V_{GS} = 0 V$$
.

(2)
$$V_{GS} = -0.25 \text{ V}.$$

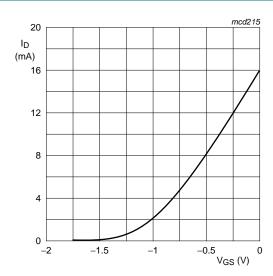
(3)
$$V_{GS} = -0.5 \text{ V}.$$

(4)
$$V_{GS} = -0.75 \text{ V}.$$

(5) $V_{GS} = -1 \text{ V}.$

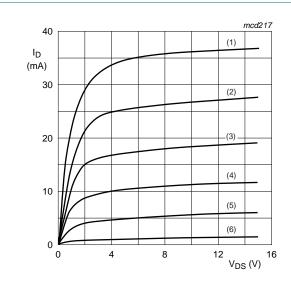
Fig 8. Typical output characteristics; PMBFJ309.





 $V_{DS} = 10 \text{ V}; T_i = 25 \,^{\circ}\text{C}.$

Fig 9. Typical transfer characteristics; PMBFJ309.



 $T_i = 25 \, ^{\circ}C$.

(1)
$$V_{GS} = 0 V$$
.

(2)
$$V_{GS} = -0.5 \text{ V}.$$

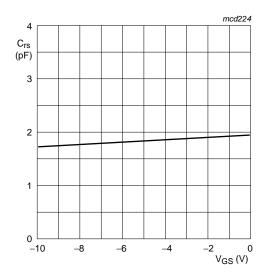
(3)
$$V_{GS} = -1 \text{ V}.$$

(4)
$$V_{GS} = -1.5 \text{ V}.$$

(5)
$$V_{GS} = -2 \text{ V}.$$

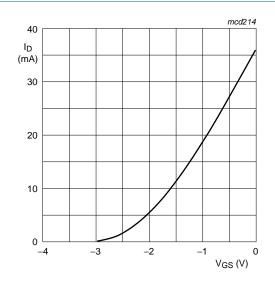
(6) $V_{GS} = -2.5 \text{ V}.$





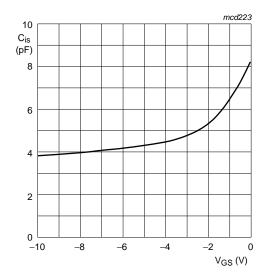
 $V_{DS} = 10 \text{ V}; T_j = 25 \text{ }^{\circ}\text{C}.$

Fig 12. Reverse transfer capacitance as a function of gate-source voltage; typical values.



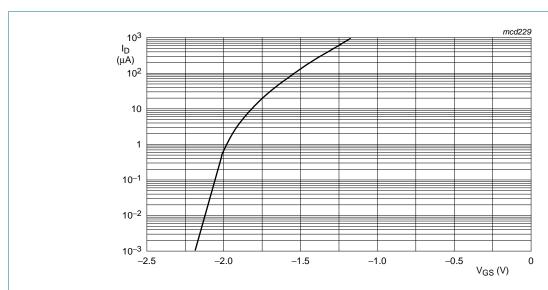
 V_{DS} = 10 V; T_j = 25 °C.

Fig 11. Typical transfer characteristics; PMBFJ310.



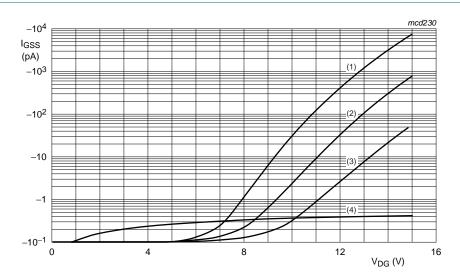
 $V_{DS} = 10 \text{ V}; T_j = 25 \text{ }^{\circ}\text{C}.$

Fig 13. Input capacitance as a function of gate-source voltage; typical values.



 $V_{DS} = 10 \text{ V}; T_j = 25 \,^{\circ}\text{C}.$

Fig 14. Drain current as a function of gate-source voltage; typical values.



 $T_i = 25 \, ^{\circ}C$.

- (1) $I_D = 10 \text{ mA}.$
- (2) $I_D = 1 \text{ mA}.$
- (3) $I_D = 100 \mu A$.
- (4) I_{GSS}.

Fig 15. Gate current as a function of drain-gate voltage; typical values.

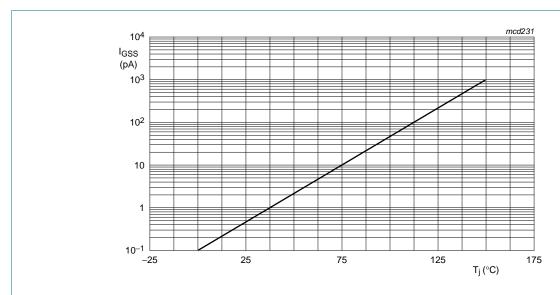


Fig 16. Gate current as a function of junction temperature; typical values.

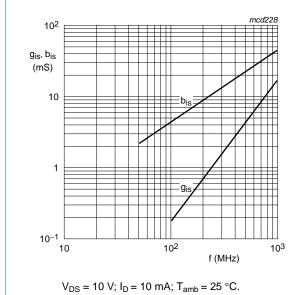
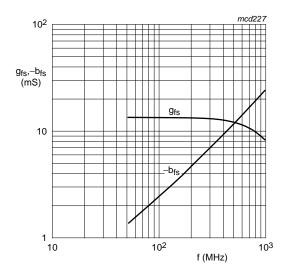
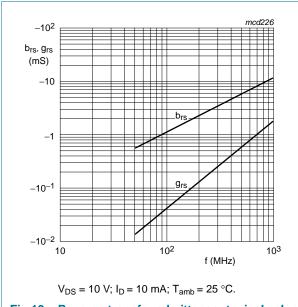


Fig 17. Input admittance; typical values.



 V_{DS} = 10 V; I_{D} = 10 mA; T_{amb} = 25 °C.

Fig 18. Forward transfer admittance; typical values.



 b_{os}, g_{os} (mS)

10 10^{-1} $10^{$

Fig 19. Reverse transfer admittance; typical values.

Fig 20. Output admittance; typical values.

9. Package outline

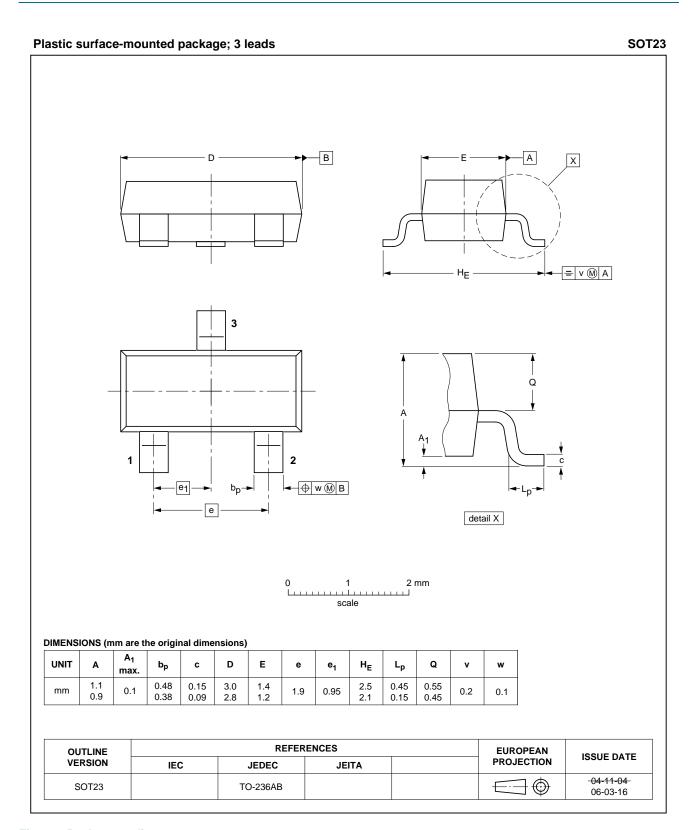


Fig 21. Package outline.

PMBFJ308_309_310

10. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMBFJ308_309_310 v.4	20110920	Product data sheet	-	PMBFJ308_309_310 v.3
Modifications:	guidelines • Legal texts	of NXP Semiconductors	he new company r	comply with the new identity name where appropriate. atest version.
PMBFJ308_309_310 v.3 (9397 750 13403)	20040723	Product data sheet	-	PMBFJ308_309_310 v.2
PMBFJ308_309_310 v.2 (9397 750 01141)	19960911	Product specification	-	-

11. Legal information

11.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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N-channel silicon field-effect transistors

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

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For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

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NXP Semiconductors

N-channel silicon field-effect transistors

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