

AN6477FBP

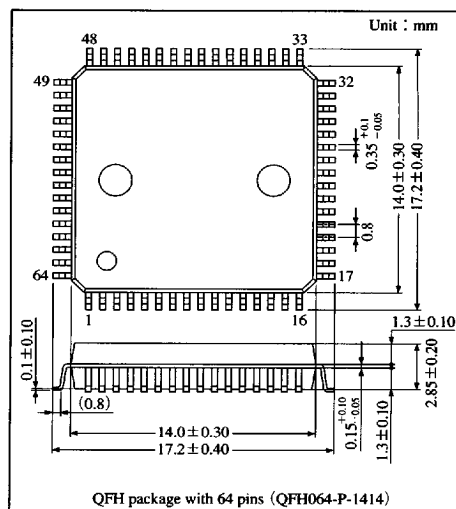
Speech Network IC Incorporating Cross-Point Switch

Overview

The AN6477FBP is a speech network IC which includes a transmitter/receiver noise reducing function and is suitable for quality cordless telephones. It incorporates a cross-point switch controlled by serial input. It allows speech path switching and mixing, and provides for three- or four-person communication and other sophisticated functions. It also incorporates REC/PLAY amplifiers with VOX circuits.

Features

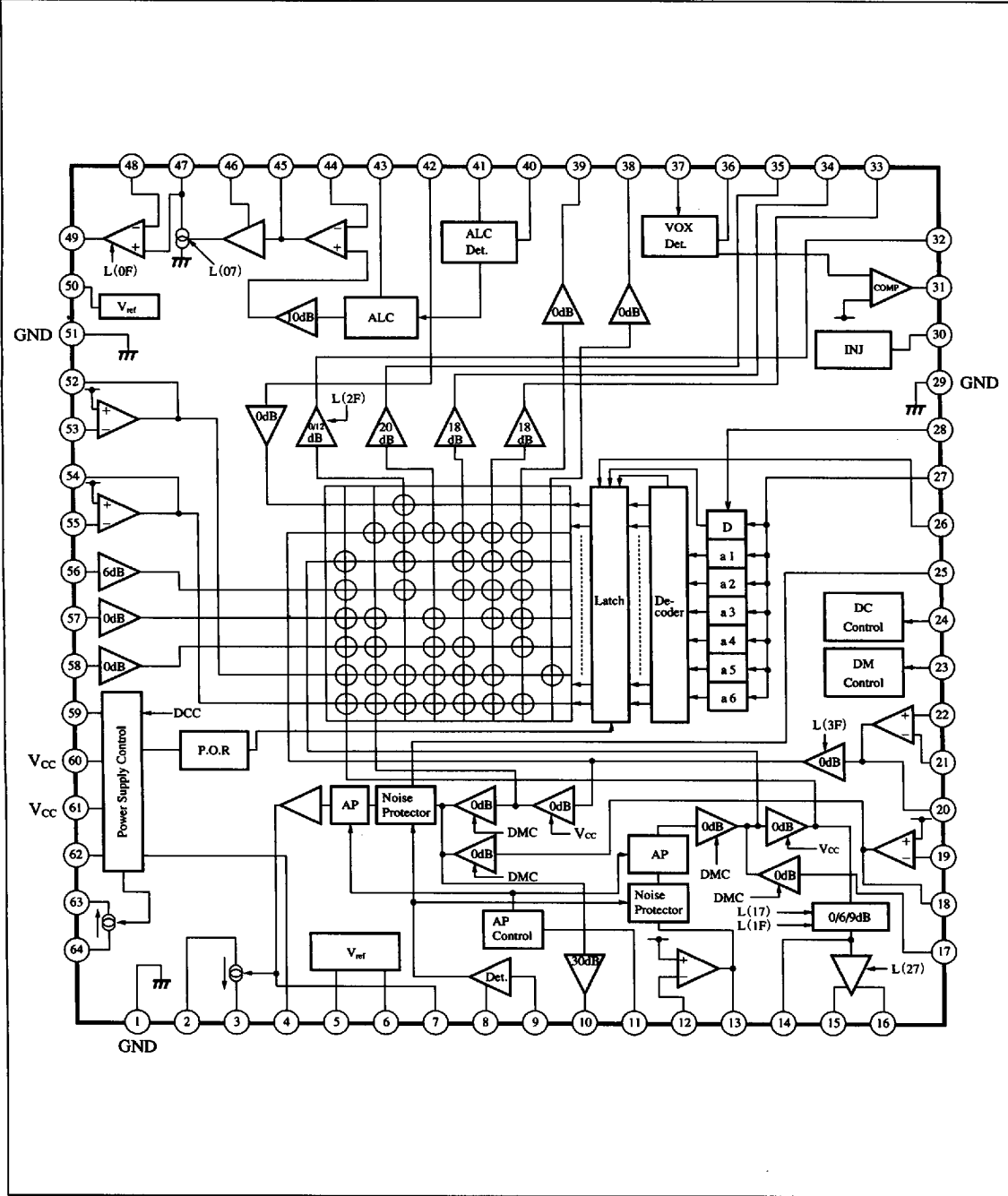
- The speech block can operate on line voltage, with no external power supply, and is operational even during a commercial power failure.
- Incorporates a transmitter/receiver noise reducing function to improve the handset's howling margin (transmitter noise reduction can be turned ON and OFF from outside).
- Incorporates auto. PAD, dial mute, DC voltage regulation, and other basic speech functions.
- The cross-point switch can be operated independently.
- Each output of the cross-point switch can correspond to multiple inputs, allowing three- or four-person communication.
- The REC/PLAY amplifiers incorporate ALC and VOX circuits.
- Receiver volume can be increased by 6dB or 9dB.



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■ Block Diagram



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Pin Descriptions

Pin No.	Pin name	Pin No.	Pin name
1	Ground	33	RF2 link output
2	Line power (+) input	34	RF1 link output
3	Side-tone adjustment	35	Intercom link output
4	Line voltage control (1)	36	VOX detection control
5	Int. ref. voltage output (2)	37	VOX amp. input
6	Int. ref. voltage output (1)	38	Time stamp link output
7	Trans. preamp. output	39	Recording link output
8	Noise reduction detection output	40	ALC input
9	Noise reduction detection input	41	ALC detection control
10	Noise reduction amp. output	42	Loudspeaker link input
11	Auto. PAD control	43	Recording input
12	Rec. preamp. input	44	Recording inverting input
13	Rec. preamp. output	45	Recording preamp. output
14	Rec. amp. input	46	Recording bias current control
15	Rec. amp. output (1)	47	To recording head
16	Rec. amp. output (2)	48	EQ amp. inverse input
17	BT signal input	49	EQ amp. output
18	DTMF preamp. output	50	REC/PLAY int. ref. voltage output
19	DTMF signal input	51	Ground
20	MIC preamp. output	52	MIX preamp. output
21	MIC preamp. input (1)	53	MIX link input
22	MIC preamp. input (2)	54	AUX preamp. output
23	Dial mute control	55	AUX link input
24	Line voltage control	56	Intercom link input
25	Trans. noise reduction control	57	RF1 link input
26	Strobe signal input	58	RF2 link input
27	Clock signal input	59	Power-ON reset control
28	Data input	60	External supply voltage input
29	Ground	61	Internal supply voltage output
30	Logic power supply input	62	Circuit voltage control (2)
31	VOX detector output	63	Line current bypass (2)
32	SP link output	64	Line current bypass (1)

Absolute Maximum Ratings (Ta=25℃)

Parameter	Symbol	Rating	Unit
Supply voltage (1)	V _{CC}	7.0	V
Supply current (1)	V _L	12.0	V
Supply voltage (2)	I _{CC}	50	mA
Supply current (2)	I _L	135	mA
Power dissipation ^{Note)}	P _D	619	mW
Operating ambient temperature	T _{opr}	−20 to +75	℃
Storage temperature	T _{stg}	−55 to +150	℃

Note) In a free-air condition with Ta = 75℃.

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■ Recommended Operating Range (Ta=25℃)

Parameter	Symbol	Range
Operating supply voltage range (1)	V _{CC}	4.5 to 5.5V
Operating supply voltage range (2)	V _L	3 to 11V

■ Recommended Operating Conditions (Ta=−20 to +75℃)

Parameter		Symbol	Condition	min	typ	max	Unit
Supply voltage		V _{CC}		4.5	5	5.5	V
Clock frequency		f _{CLK}	Input duty 40 to 50%	—	—	250	kHz
Input pulse width	CLK (Low)	t _{WL} (CLK)		1.6	—	2.0	μs
	CLK (High)	t _{WH} (CLK)		2.0	—	2.4	μs
	STB	t _W (STB)		1.2	—	—	μs
Setup time	DATA	t _{SU} (DATA)		1.6	—	—	μs
	STB	t _{SU} (STB)		0.8	—	—	μs
Hold time	DATA	t _H (DATA)		1.6	—	—	μs
	STB	t _H (STB)		1.2	—	—	μs
Clock pulse rise time		t _r (CLK)		—	—	20	μs
Clock pulse fall time		t _f (CLK)		—	—	20	μs
Input voltage		V _I		0	—	V _{CC}	V

■ Electrical Characteristics (Ta=25±2℃)

Parameter	Symbol	Condition	min	typ	max	Unit
Power supply characteristics during power failure						
Line DC voltage (I−1)	V _{L−I1}	V−DCC=High, I _L =20mA V _{CC} =0V	3.3	3.6	3.9	V
Line DC voltage (I−2)	V _{L−I2}	V−DCC=High, I _L =60mA V _{CC} =0V	4.85	5.15	5.55	V
Line DC voltage (I−3)	V _{L−I3}	V−DCC=High, I _L =120mA V _{CC} =0V	6.85	7.25	7.75	V
Line DC voltage−H (I−1)	V _{LH−I1}	V−DCC=Low, I _L =30mA V _{CC} =0V	4.75	5.25	5.75	V
Line DC voltage−H (I−2)	V _{LH−I2}	V−DCC=Low, I _L =60mA V _{CC} =0V	5.95	6.45	6.95	V
Line DC voltage−H (I−3)	V _{LH−I3}	V−DCC=Low, I _L =120mA V _{CC} =0V	8.4	9.0	9.7	V
Internal supply voltage (I)	V _{reg−I}	V−DCC=High, I _L =20mA V _{CC} =0V	1.8	2.0	2.2	V
Internal ref. supply voltage (I)	V _{ref−I}	V−DCC=High, I _L =20mA V _{CC} =0V	0.9	1.0	1.1	V

Normal power supply characteristics

Line DC voltage (E−1)	V _{L−E1}	V−DCC=High, I _L =20mA V _{CC} =5V	3.15	3.45	3.85	V
Line DC voltage (E−2)	V _{L−E2}	V−DCC=High, I _L =60mA V _{CC} =5V	4.5	4.9	5.3	V
Line DC voltage (E−3)	V _{L−E3}	V−DCC=High, I _L =120mA V _{CC} =5V	6.7	7.2	7.7	V
Line DC voltage (E−1)	V _{LH−E1}	V−DCC=Low, I _L =30mA V _{CC} =5V	4.7	5.25	5.8	V



■ Electrical Characteristics (cont.) (Ta=25±2℃)

Parameter	Symbol	Condition	min	typ	max	Unit
Power supply characteristics during power failure (cont.)						
Line DC voltage - H (E-2)	V _{LH-E2}	V-D _{CC} =Low, I _L =60mA V _{CC} =5V	5.8	6.45	7.1	V
Line DC voltage - H (E-3)	V _{LH-E3}	V-D _{CC} =Low, I _L =120mA V _{CC} =5V	8.2	8.9	9.6	V
Internal supply voltage (E)	V _{reg-E}	V-D _{CC} =High, I _L =20mA V _{CC} =5V	4.6	4.85	5.0	V
Internal ref. supply voltage (E)	V _{ref-E}	V-D _{CC} =High, I _L =20mA V _{CC} =5V	2.25	2.5	2.7	V
Total circuit current	I _{total}	V-D _{CC} =High, I _L =20mA V _{CC} =5V	17	27	35	mA

Receiver during power failure

Rec. gain (I-1)	G _{V-IR1}	I _L =30mA, V _{CC} =0V V _{in} =-42dBm	30.5	32.5	34.5	dB
Rec. gain (I-2)	G _{V-IR2}	I _L =80mA, V _{CC} =0V V _{in} =-42dBm	27	29	31	dB
Rec. auto. PAD width (I) Note.1)	A _{P-IR}	I _L =30 to 80mA, V _{CC} =0V V _{in} =-42dBm	2.5	3.5	5	dB
Rec. max output (I)	V _{O-IR}	With I _L =30mA, V _{CC} =0V and THD=5%	0	4	—	dBm
Rec. noise reduction (I)	N _{L-IR}	I _L =30mA, V _{CC} =0V, V _{in} =-42 dBm, V _{in} -M=-65/-50dBm	4	6	8	dB
BT amp. gain (I)	G _{V-IBT}	I _L =30mA, V _{CC} =0V V-D _{MC} =Low, V _{in} =-30dBm	19	21	23	dB

Receiver on external power supply

Rec. gain (E-1)	D _{V-ER1}	I _L =30mA, V _{CC} =5V V _{in} =-42dBm	30.5	32.5	34.5	dB
Rec. gain (E-2)	D _{V-ER2}	I _L =80mA, V _{CC} =5V V _{in} =-42dBm	26.8	28.8	30.8	dB
Rec. auto. PAD width (E) Note.1)	A _{P-ER}	I _L =30 to 80mA, V _{CC} =5V V _{in} =-42dBm	2.5	3.7	5	dB
Rec. max output (E)	V _{O-ER}	With I _L =30mA, V _{CC} =5V and THD=5%	4	12	—	dBm
Rec. noise reduction (E)	N _{L-ER}	I _L =30mA, V _{CC} =5V, V _{in} =-42 dBm, V _{in} -M=-65/-50dBm	6	8	10	dB
Rec. digital volume (1) Note.2)	G _{V-DV1}	I _L =30mA, V _{CC} =5V V _{in} =-42dBm, DV-1 ON	5	6	7	dB
Rec. digital volume (2) Note.2)	G _{V-DV2}	I _L =30mA, V _{CC} =5V V _{in} =-42dBm, DV-2 ON	7.5	9	10.5	dB
BT amp. gain (E)	G _{V-EBT}	I _L =30mA, V _{CC} =5V V-D _{MC} =Low, V _{in} =-30dBm	19.5	21.5	23.5	dB
Rec. gain difference	ΔG-R	For V _{CC} =0V and V _{CC} =5V (between G _{V-IR1} and G _{V-ER1})	-1.2	-0.1	1.2	dB

Transmitter amp. during power failure

Trans. gain (I-1)	G _{V-IM1}	R=27Ω (pin③), I _L =30mA V _{CC} =0V, V _{in} =-38dBm	28.2	30.2	32.2	dB
Trans. gain (I-2)	G _{V-IM2}	I _L =80mA, V _{CC} =0V V _{in} =-38dBm	24.4	26.4	28.4	dB
Trans. auto. PAD width (I) Note.1)	A _{P-IM}	I _L =30 to 80mA, V _{CC} =0V V _{in} =-38dBm	2.1	3.4	4.6	dB

Note.1) Gain decrease when line current I_L is changed from 30 to 80mA. If Pin⑪ (auto. PAD control) is connected to Pin⑩ (int. supply voltage output), the gain will not change.

Note.2) Gain increase from receiver gain (E-1).

Note) Unless otherwise specified, input signal f_{in}=1kHz, control voltage V-D_{CC}=High, and control voltage V-D_{MC}=High.

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■ Electrical Characteristics (cont.) (Ta=25±2℃)

Parameter	Symbol	Condition	min	typ	max	Unit
Transmitter amp. during power failure (cont.)						
Trans. max output (I-1)	VO-IM1	With IL=30mA, VCC=0V and THD=5%	0	3.5	—	dBm
Trans. max output (I-2)	VO-IM2	IL=30mA, VCC=0V THD=5%, V-DCC=Low	0	3.5	—	dBm
Trans. noise reduction (I)	NL-IT	Trans. gain difference for VIN=-75, -55dBm with IL=30mA, VCC=0V	11	14	17	dB
DTMF gain (I-1)	GV-ID1	IL=30mA, VCC=0V V-DMC=Low, VIN=-30dBm	17.5	19.5	21.5	dB
DTMF gain (I-2)	GV-ID2	IL=80mA, VCC=0V V-DMC=Low, VIN=-30dBm	13.7	15.7	17.7	dB
DTMF auto. PAD width (I) Note 1)	AP-IDT	IL=30 to 80mA, VCC=0V V-DMC=Low, VIN=-30dBm	2.1	3.4	4.6	dB
DTMF max output (I-1)	VO-ID1	IL=30mA, VCC=0V V-DMC=Low, THD=5%	0	3.8	—	dBm
DTMF max output (I-2)	VO-ID2	IL=30mA, VCC=0V, THD=5% V-DMC=Low, V-DCC=Low	0	3.5	—	dBm
Transmitter amp. on normal power supply						
Trans. gain (E-1)	GV-EM1	IL=30mA, VCC=5V VIN=-38dBm	28.6	30.6	32.6	dB
Trans. gain (E-2)	GV-EM2	IL=80mA, VCC=5V VIN=-38dBm	25.0	27.0	29.0	dB
Trans. auto. PAD width (E) Note 1)	AP-EM	IL=30 to 80mA, VCC=5V VIN=-38dBm	2.1	3.6	4.6	dB
Trans. max output (E-1)	VO-EM1	With IL=30mA, VCC=5V and THD=5%	2	6	—	dBm
Trans. max output (E-2)	VO-EM2	IL=30mA, VCC=5V THD=5%, V-DCC=Low	2	6	—	dBm
Trans. noise reduction (E)	NL-ET	Trans. gain difference for VIN=-75, -55dBm with IL=30mA, VCC=5V	15	18	21	dB
DTMF gain (E-1)	GV-ED1	IL=30mA, VCC=5V DM=ON, VIN=-30dBm	18.1	20.1	22.1	dB
DTMF gain (E-2)	GV-ED2	IL=80mA, VCC=5V V-DMC=Low, VIN=-30dBm	14.5	16.5	18.5	dB
DTMF auto. PAD width (E) Note 1)	AP-EDT	IL=30 to 80mA, VCC=5V V-DMC=Low, VIN=-30dBm	2.1	3.6	4.6	dB ¹⁾
DTMF max output (E-1)	VO-ED1	IL=30mA, VCC=5V V-DMC=Low, THD=5%	2	6	—	dBm
DTMF max output (E-2)	VO-ED2	IL=30mA, VCC=5V, DM=ON V-DMC=Low, THD=5%	2	6	—	dBm
Trans. gain difference	ΔG-M	For VCC=0V, 5V (between GV-IM1 and GV-EM1)	-1.8	-0.8	0.7	dB
DTMF gain difference	ΔG-MF	For VCC=0V, 5V (between GV-ID1 - GV-ED1)	-2.3	-1.3	0.2	dB

Note 1) Gain decrease when line current IL is changed from 30 to 80mA. If Pin① (auto. PAD control) Pin⑥ (int. supply voltage output), the gain will not change.

Note) Unless otherwise specified, input signal fin=1kHz, control voltage V-DCC=High, and control voltage, V-DMC=High.

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■ Electrical Characteristics (cont.) (Ta=25±2℃)

Parameter	Symbol	Condition	min	typ	max	Unit
Recording preamplifiers						
Rec. preamp. gain	G _{V-RP}	V _{in} =-36dBm R _{in} =0Ω	43	45	47	dB
Rec. preamp. output	V _{O-RP}	V _{in} =-45dBm R _{in} =10kΩ	-13.4	-11.4	-9.4	dBm
Rec. preamp. output noise voltage ^{Note 1)}	V _{no-RP}	DIN/AUDIO R _s =10kΩ	—	0.8	2.5	mVrms
Recording amplifier						
Head bias current	I _{-REC}	L-SW (h07) =ON	145	180	215	μA
Head output	G _{V-REC}	L-SW (h07) =ON V _{in} =-15dBm, R _L =1kΩ	40.0	50.0	63.0	mVrms
Playing EQ amplifier						
EQ amp. gain	G _{V-EQ}	L-SW (h0F) =ON V _{in} =-40dBm	27.8	29.8	31.8	dB
EQ amp. output noise voltage	V _{no-EQ}	L-SW (h0F) =ON DIN/AUDIO, R _L =1kΩ	—	0.45	1.2	mVrms
VOX detector						
VOX sensitivity (1)	V _{S1}	I-VOX=12.5 μA	3.5	4.8	—	V
VOX sensitivity (2)	V _{S2}	I-VOX=24.5 μA	—	0.025	0.5	V
Link SW input amplifier						
MIX amp. gain	G _{V-MIX}	V _{in} =-36dBm	5	6	7	dB
AUX amp. gain	G _{V-AUX}	V _{in} =-36dBm	5	6	7	dB
Link SW output amplifier						
SP output gain (1) ^{Note 1)}	G _{V-SP01}	AUX IN, V _{in} =-36dBm L-SW (h3A) =ON	11	12	13	dB
SP output gain (2) ^{Note 1)}	G _{V-SP02}	AUX IN, V _{in} =-36dBm L-SW (h3A&h2F) =ON	-1.5	-0.5	0.5	dB
Intercom output gain ^{Note 1)}	G _{V-DHO}	AUX IN, V _{in} =-36dBm L-SW (h3B) =ON	18.5	20	21.5	dB
RF1 output gain ^{Note 1)}	G _{V-RF1O}	AUX IN, V _{in} =-36dBm L-SW (h3C) =ON	16.5	18	19.5	dB
RF2 output gain ^{Note 1)}	G _{V-RF2O}	AUX IN, V _{in} =-36dBm L-SW (h3D) =ON	16.5	18	19.5	dB
Recording output gain ^{Note 1)}	G _{V-RECO}	AUX IN, V _{in} =-36dBm L-SW (h3E) =ON	-1.0	0	1.0	dB
Receiver output gain ^{Note 1)}	G _{V-RO}	AUX IN, V _{in} =-36dBm L-SW (h38) =ON	19.9	21.4	22.9	dB
Line output gain ^{Note 1)}	G _{V-TO}	AUX IN, V _{in} =-36dBm L-SW (h39) =ON	17.7	19.2	20.7	dB
Rec. output gain difference ^{Note 2)}	G _{d-RO}	MIX IN, V _{in} =-36dBm L-SW (h30) =ON	-1.0	0	1.0	dB
Time stamp output gain ^{Note 1)}	G _{V-TSO}	MIX IN, V _{in} =-36dBm L-SW (h37) =ON	-1.0	0	1.0	dB

Note 1) Each amp. gain is measured from AUX OUT or MIX OUT to its output (the AUX or MIX preamp. gain is not included in the calculation).
Note 2) The difference from the receiver output gain (G_{V-RO}).
Note) Unless otherwise specified, external supply voltage V_{CC}=5V, line current I_L=0mA, input signal frequency f_{in}=1kHz, control voltage V-DCC=High, and control voltage V-DMC=High.

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■ Electrical Characteristics (cont.) (Ta=25±2℃)

Parameter	Symbol	Condition	min	typ	max	Unit
Link SW input						
MIC input gain ^{Note 2)}	G _{V-MI}	V _{in} =-38dBm, rec. output L-SW (h0E)=ON	-1	0	1	dB
Rec. input gain ^{Note 2)}	G _{V-RI}	V _{in} =-42dBm, rec. output L-SW (h16)=ON	-1	0	1	dB
Intercom input gain	G _{V-DHI}	V _{in} =-30dBm, rec. output L-SW (h1E)=ON	5	6	7	dB
RF1 input gain	G _{V-RF1I}	V _{in} =-30dBm, rec. output L-SW (h26)=ON	-1	0	1	dB
RF2 input gain	G _{V-RF2I}	V _{in} =-30dBm, rec. output L-SW (h2E)=ON	-1	0	1	dB
SP link input gain	G _{V-SPI}	V _{in} =-30dBm, rec. output L-SW (h02)=ON	11	12	13	dB
Link maximum output						
SP OUT max. output	V _{O-SP}	Input L-SP IN, THD=5% L-SW (h02)=ON	0	4	—	dBm
DH OUT max. output	V _{O-DH}	Input RF1 IN, THD=5% L-SW (h23)=ON	0	4	—	dBm
RF1 OUT max. output	V _{O-RF1}	Input RF2 IN, THD=5% L-SW (h25)=ON	0	4	—	dBm
RF2 OUT max. output	V _{O-RF2}	Input RF1 IN, THD=5% L-SW (h2C)=ON	0	4	—	dBm
L-REC OUT max. output	V _{O-LR}	Input AUX IN, THD=5% L-SW (h3E)=ON	0	4	—	dBm
Controls						
Dial mute High level voltage	V _{DMC-H}		2	—	V _{CC} +0.2	V
Dial mute High level control current	I _{DMC-H}	V-DMC=5V	15	38	80	μA
Dial mute Low level voltage	V _{DMC-L}		-0.2	—	0.3	V
Dial mute Low level control current	I _{DMC-L}	V-DMC=0V	-40	-20	-10	μA
DC voltage control High level voltage	V _{DCC-H}		2	—	V _{CC} +0.2	V
DC voltage control High level control current	I _{DCC-H}	V-DCC=5V	10	25	50	μA
DC voltage control Low level voltage	V _{DCC-L}		-0.2	—	0.4	V
DC voltage control Low level control current	I _{DCC-L}	V-DCC=0V	-2	-0.1	—	μA
Trans. noise reduction High level voltage	V _{NLT-H}		2	—	V _{CC} +0.2	V
Trans. noise reduction High level control current	I _{NLT-H}	V-NLT=5V	10	25	50	μA
Trans. noise reduction Low level voltage	V _{NLT-L}		-0.2	—	0.4	V
Trans. noise reduction Low level control current	I _{NLT-L}	V-NLT=0V	-2	-0.1	—	μA

Note) Unless otherwise specified, external supply voltage V_{CC}=5V, line current I_L=0mA, input signal frequency f_{in}=1kHz, control voltage V-DCC=High, and control voltage V-DMC=High.

Note 2) Each amp. gain is measured from MIC OUT or R PRE OUT to its output.

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■ Electrical Characteristics (cont.) (Ta=25±2℃)

Parameter	Symbol	Condition	min	typ	max	Unit
Controls (cont.)						
Data input high level voltage	V _{DIN-H}		2	—	V _{CC} +0.2	V
Data input high level control current	I _{DIN-H}	V-DIN=5V	70	160	250	μA
Data input low level voltage	V _{DIN-L}		-0.2	—	0.3	V
Data input low level control current	I _{DIN-L}	V-DIN=0V	-1	-0.1	—	μA

Power supply block

AC impedance (I)	Z _{AC-I}	I _L =80mA, V _{CC} =0V V _{in} =200mVrms, F _{in} =1kHz	450	570	750	Ω
AC impedance (E)	Z _{AC-E}	I _L =80mA, V _{CC} =5V V _{in} =200mVrms, F _{in} =1kHz	450	580	750	Ω

Input impedance

BT amp. input impedance	Z _{in-BT}	Pin ⑰ input	8.7	9.7	10.7	kΩ
ALC amp. input impedance	Z _{in-ALC}	Pin ④⑨ input	8.5	9.5	10.5	kΩ
Intercom preamp. input impedance	Z _{in-DH}	Pin ⑤⑥ input	8.5	9.5	10.5	kΩ
RF1 preamp. input impedance	Z _{in-RF1}	Pin ⑤⑦ input	8.5	9.5	10.5	kΩ
RF2 preamp. input impedance	Z _{in-RF2}	Pin ⑤⑧ input	8.5	9.5	10.5	kΩ

■ Electrical Characteristics (Design Values for Reference) (Ta=25±2℃)

The following are design values for reference, and not guaranteed values.

Parameter	Symbol	Condition	min	typ	max	Unit
Speech block						
Rec. output noise voltage (I)	V _{n-IR}	I _L =30mA, V _{CC} =0V DIN/AUDIO	—	0.3	—	mVrms
Rec. output noise voltage (E)	V _{n-ER}	I _L =30mA, V _{CC} =5V DIN/AUDIO	—	0.3	—	mVrms
Trans. output noise voltage (I)	V _{n-IT}	I _L =30mA, V _{CC} =0V DIN/AUDIO	—	0.3	—	mVrms
Trans. output noise voltage (E)	V _{n-ET}	I _L =30mA, V _{CC} =5V DIN/AUDIO	—	0.3	—	mVrms
Dial mute trans. amp. mute attenuation	M-TDM	I _L =30mA, V _{CC} =5V V _{in} =-30dBm, V-DBM=H/L	—	75	—	dB
Trans. preamp. mute attenuation	M-TM	I _L =30mA, V _{in} =-30dBm, V _{CC} =5V, L _{SW} (h3f) =OFF/ON	—	70	—	dB
Rec. output mute attenuation	M-RO	I _L =30mA, V _{in} =-30dBm, V _{CC} =5V, L _{SW} (h27) =OFF/ON	—	50	—	dB
Rec. preamp. input impedance	Z _{in-R}	Pin ⑫ input	—	500	—	kΩ
MIC preamp. input impedance	Z _{in-M}	Pin ⑫ and ⑬ input	—	500	—	kΩ

REC/PLAY block

ALC amp. ALC width	W-ALC	I _L =0mA, V _{CC} =5V and ALC output distortion ≤2%	—	40	—	dB
ALC amp. ALC effect	ΔALC	I _L =0mA, V _{CC} =5V V _{in} =-45dBm to -20dBm	—	1	—	dB
Rec. amp. mute attenuation	M-REC	V _{CC} =5V, V _{in} =-10dBm I _L =0mA, LSW (h07) =ON/OFF	—	80	—	dB

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■ Electrical Characteristics (Design Values for Reference) (cont.) (Ta=25±2℃)

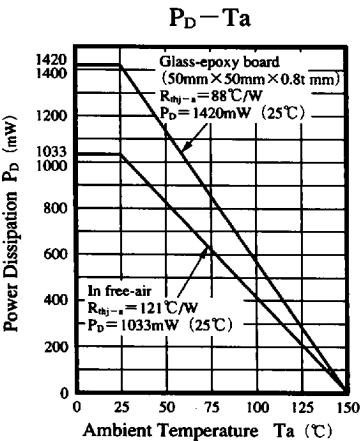
The following are design values for reference, and not guaranteed values.

Parameter	Symbol	Condition	min	typ	max	Unit
EQ amp. mute attenuation	M-EQ	V _{CC} =5V, V _{in} =-30dBm I _L =0mA, LSW (h0F) =ON/OFF	—	80	—	dB
Rec. preamp. input impedance	Z _{in} -REC	Pin ④3 input	—	10	—	kΩ
EQ amp. input impedance	Z _{in} -EQ	Pin ④7 and ④9 input	—	500	—	kΩ
VOX amp. input impedance	Z _{in} -VOX	Pin ⑤7 input	—	500	—	Ω

Link switch

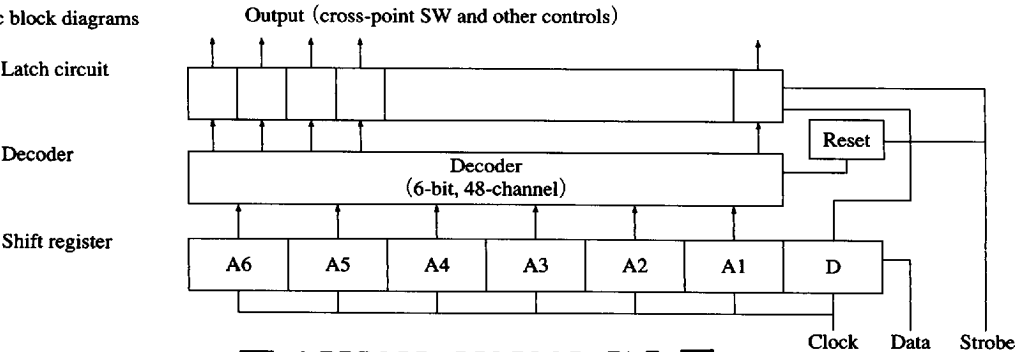
Link SW mute attenuation	M-LS	V _{CC} =5V, I _L =30mA, AC output measured at link ON/OFF,	—	75	—	dB
MIX preamp. input impedance	Z _{in} -MIX	Pin ⑤3 input	—	500	—	kΩ
AUX preamp. input impedance	Z _{in} -AUX	Pin ⑤9 input	—	500	—	kΩ
CPC output impedance	Z _{OUT} -CPC	Pin ⑥9 input	—	100	—	kΩ
Noise reduction amp. input impedance	Z _{in} -NL	Pin ⑨ input	—	45	—	Ω

■ Characteristics Curve

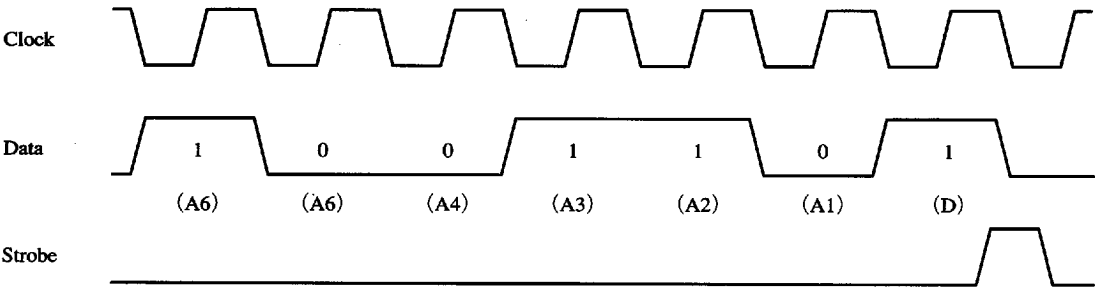


■ Logic Specifications

Basic block diagrams



Time Charts (assuming the address h26 latch is to be set)



- 1. Data is read into the shift register in synchronization with a rising edge of the clock, with the higher data being shifted sequentially on a first-come highest-bit basis.
- 2. When the strobe is low, data is shifted sequentially on the shift register in synchronization with the clock. Data on the latch circuit will not change.
- 3. When the strobe goes high, the latched data whose address is represented by the highest six bits of the shift register is updated. Latched data is set when the least significant bit is 1, and reset when the bit is 0.
- 4. Referring to 3 above, if the address is h00 (the highest six bits of the shift register are all 0s), the latch circuit is cleared (all reset) regardless of the data content.
- 5. At power-on (V_{CC} ON), the latch circuit is cleared (by power-ON reset).

■ Logic Circuits Address Specifications

1. Cross-point switch

Output Input	Handset rec.	Line output	Loudspeaker	Intercom	RF1	RF2	Recording	Time stamp
Loudspeaker			02					
Microphone		09	0A	0B	0C	0D	0E	
Receiver	10		12		14	15	16	
Intercom	18		1A		1C	1D	1E	
RF1	20	21		23		25	26	
RF2	28	29		2B	2C		2E	
MIX	30	31	32	33	34	35		37
AUX	38	39	3A	3B	3C	3D	3E	

Note) Empty space means "not applicable." Address is in hexadecimal.

2. Other control switches

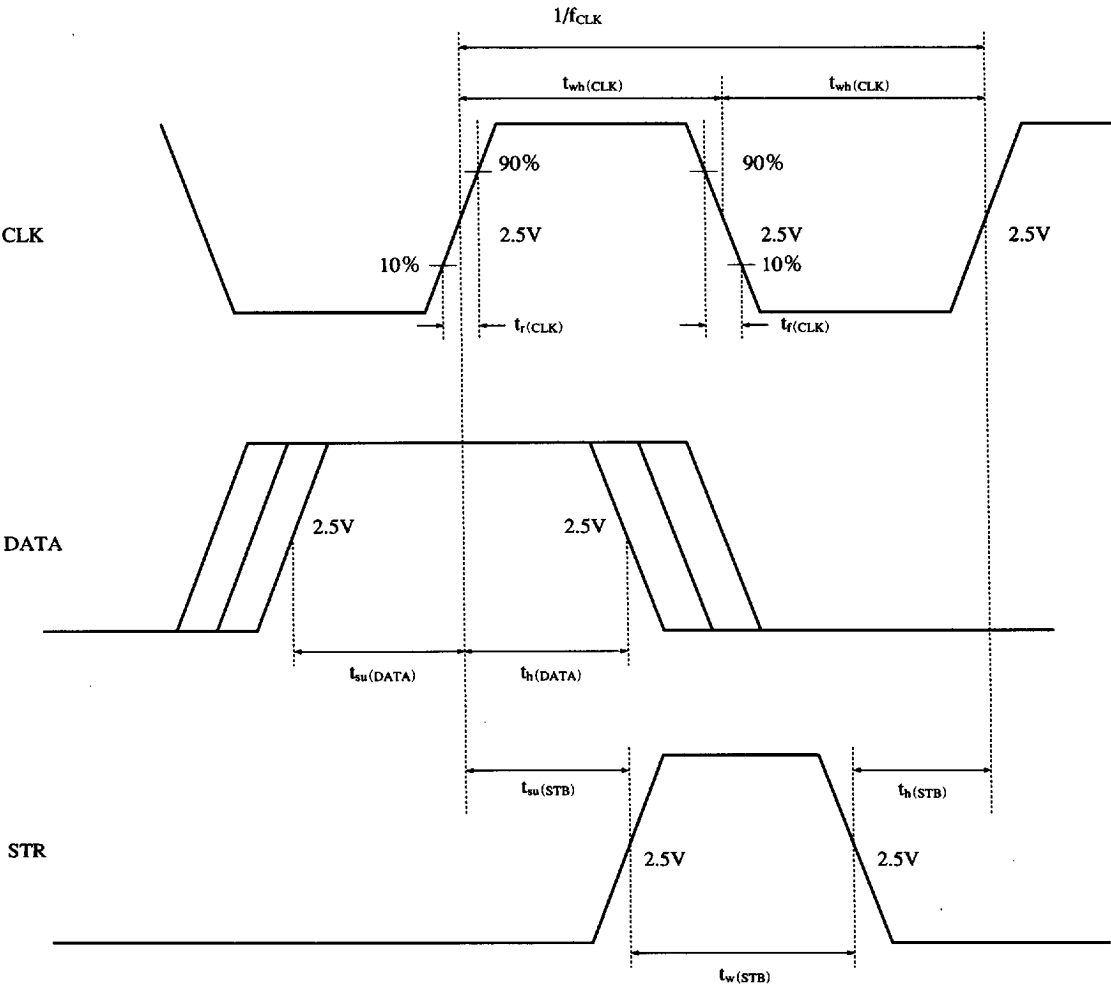
Address	Description
00	Cross-point SW all reset
07	Recording amp. ON
0F	Playing amp. ON
17	Receiver volume 6 dB up
1F	Receiver volume 9 dB up
27	Handset receiver amp. mute
2F	SP output amp. gain 12 dB down
3F	MIC preamp. mute
09, 21, 29	Receiver noise reduction is enabled.

Note) Address is in hexadecimal.

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■ Timing Charts



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