

TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

MT6L05FS

VHF~UHF Band Low-Noise Amplifier Applications

Two devices are incorporated in a fine-pitch, small-mold package (6 pins): fS6.

- Superior noise characteristics
- Superior performance in buffer and oscillator applications.
- Lead (Pb)-free.

Mounted Devices

	Q1/Q2
Corresponding three-pin products: TESM(fSM) mold products	MT3S05T (MT3S05FS)

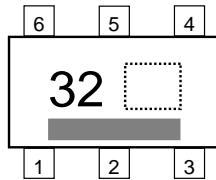
Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector- base voltage	V _{CBO}	10	V
Collector- emitter voltage	V _{CEO}	5	V
Emitter- base voltage	V _{EBO}	2	V
Collector current	I _C	40	mA
Base current	I _B	10	mA
Collector power dissipation	P _C (Note 1)	100 110 (Note 2)	mW
Junction temperature	T _j	125	°C
Storage temperature range	T _{stg}	-55~125	°C

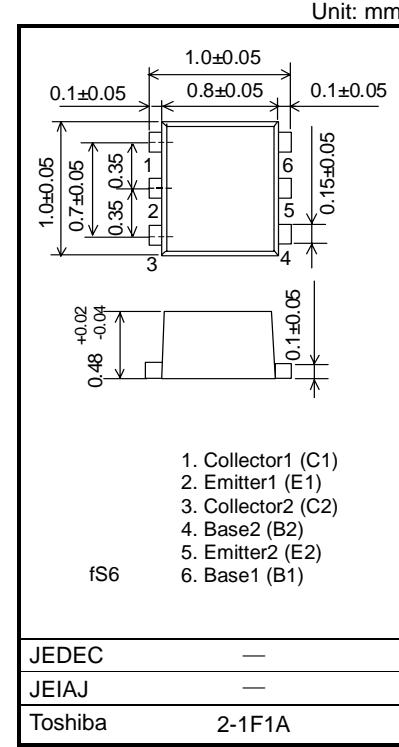
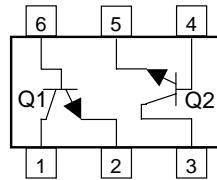
Note 1: 10 mm² × 1.0 mm (t), mounted on a glass-epoxy printed circuit board.

Note 2: During two-element operation

Marking (top view)



Pin Assignment (top view)



Weight: 0.001g (typ.)

Electrical Characteristics Q1/Q2 (Ta = 25°C)

Characteristic	Symbol	Condition	Min	Typ.	Max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 5 \text{ V}$, $I_E = 0$	—	—	0.1	μA
Emitter cutoff current	I_{EBO}	$V_{EB} = 1 \text{ V}$, $I_C = 0$	—	—	1	μA
DC current gain	h_{FE}	$V_{CE} = 1 \text{ V}$, $I_C = 5 \text{ mA}$	80	—	140	
Reverse transfer capacitance	C_{re} (Note)	$V_{CB} = 1 \text{ V}$, $I_E = 0$, $f = 1 \text{ MHz}$	—	0.9	1.15	pF
Transition frequency	f_T	$V_{CE} = 1 \text{ V}$, $I_C = 5 \text{ mA}$	2	4.5	—	GHz
Insertion gain	$ S_{21e} ^2$ (1)	$V_{CE} = 1 \text{ V}$, $I_C = 5 \text{ mA}$, $f = 1 \text{ GHz}$	—	8.5	—	dB
	$ S_{21e} ^2$ (2)	$V_{CE} = 3 \text{ V}$, $I_C = 20 \text{ mA}$, $f = 1 \text{ GHz}$	8.5	11.5	—	
Noise figure	NF	$V_{CE} = 1 \text{ V}$, $I_C = 5 \text{ mA}$, $f = 1 \text{ GHz}$	—	1.4	2.2	dB

Note: C_{re} is measured with a three-terminal method using a capacitance bridge.

Caution

This device is sensitive to electrostatic discharge. Ensure that tools and equipment are sufficiently grounded before handling. When handling individual devices (which are not yet mounted on a circuit board), ensure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

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