

MNLM120-12-H REV 0BL

 Original Creation Date: 07/06/95
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THREE TERMINAL NEGATIVE REGULATOR
Industry Part Number

LM120H-12V

NS Part Numbers

LM120H-12/883

Prime Die

LMJ056DG-12

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

MIL-STD-883, Method 5005

Subgrp	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

Electrical Characteristics

DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $I_L = 5\text{mA}$

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
I _q	Quiescent Current	V _{in} = -14V	2			4	mA	1, 2, 3
		V _{in} = -32V	2			4	mA	1, 2, 3
Delta/I _q	Quiescent Current Change	V _{in} = -17V, $5\text{mA} \leq I_L \leq 200\text{mA}$	2			0.4	mA	1
		V _{in} = -17V, $5\text{mA} \leq I_L \leq 200\text{mA}$	2			0.5	mA	2, 3
		-14V $\leq$ V _{in} $\leq$ -32V	2			0.4	mA	1
		-14V $\leq$ V _{in} $\leq$ -32V	2			0.5	mA	2, 3
R _{load}	Load Regulation	V _{in} = -17V, $5\text{mA} \leq I_L \leq 200\text{mA}$	2		-25	+25	mV	1
		V _{in} = -17V, $5\text{mA} \leq I_L \leq 200\text{mA}$	2		-50	+50	mV	2, 3
R _{line}	Line Regulation	-14V $\leq$ V _{in} $\leq$ -32V	2		-10	+10	mV	1
		-14V $\leq$ V _{in} $\leq$ -32V	2		-20	+20	mV	2, 3
V _{out}	Output Voltage	V _{in} = -17V	2		-12.3	-11.7	V	1
		V _{in} = -32V	2		-12.5	-11.5	V	1, 2, 3
		V _{in} = -32V, $I_L = 100\text{mA}$	2		-12.5	-11.5	V	1, 2, 3
		V _{in} = -14.5V	2		-12.5	-11.5	V	1, 2, 3
		V _{in} = -14.5V, $I_L = 200\text{mA}$	2		-12.5	-11.5	V	1, 2, 3
I _{os}	Short Circuit Output Current	V _{in} = -32V	2		-1.5	-0.1	A	1
R _r	Ripple Rejection	f = 120Hz	1, 2		56		dB	4

DC PARAMETERS: DRIFT VALUES

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $I_L = 5\text{mA}$. "Deltas not required on B-Level product. Deltas required for S-Level product ONLY as specified on Internal Processing Instructions (IPI)."

I _q	Quiescent Current	V _{in} = -14V	2		-0.4	0.4	mA	1
V _{out}	Output Voltage	V _{in} = 14.5V	2		-0.12	0.12	V	1

Note 1: Bench test per (SG)RPI-3-363.

Note 2: Pre Burn-In Stress Test per (SG)RPI-3-371.

Graphics and Diagrams

GRAPHICS#	DESCRIPTION
H03ARB	(blank)

See attached graphics following this page.