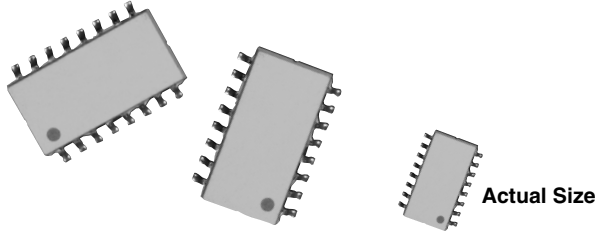


Molded, 50 Mil Pitch Resistor Networks



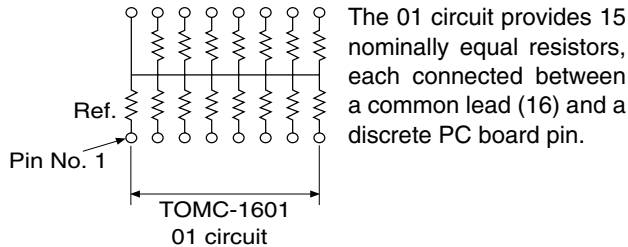
FEATURES

- Lead (Pb)-free available
- 0.090" (2.29 mm) maximum seated height
- Rugged, molded case construction (0.22" wide)
- Highly stable thin film (500 ppm at 70 °C, 10 000 hours)
- Low temperature coefficient, ± 25 ppm/°C (- 55 °C to + 125 °C)
- Wide resistance range 100 Ω to 100 k Ω


RoHS*
COMPLIANT

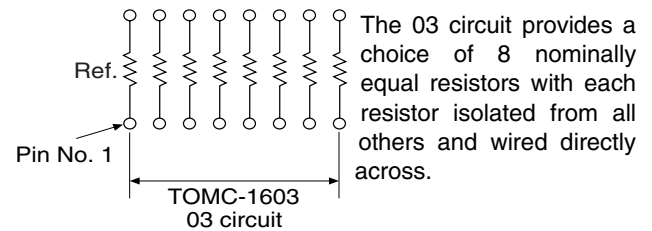
Vishay Thin Film offers standard circuits in 16 pin in a medium body molded surface mount package. The networks are available over a resistance range of 100 Ω to 100 k Ω . The network features tight ratio tolerances and close TCR tracking. In addition to the standards shown, custom circuits are available upon request.

SCHEMATIC



TYPICAL PERFORMANCE

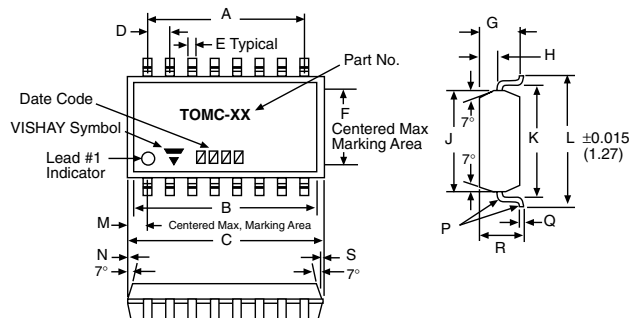
	ABS	TRACKING
TCR	25	5
	ABS	RATIO
TOL	0.1	0.025



STANDARD ELECTRICAL SPECIFICATIONS

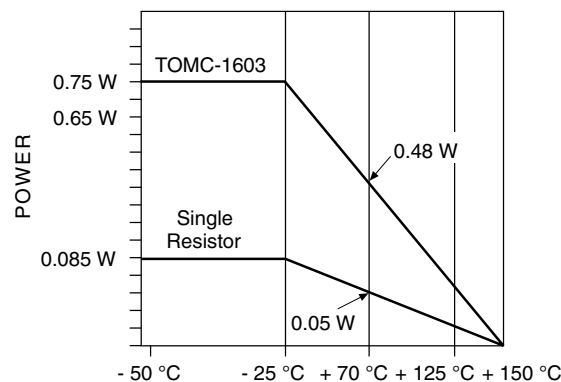
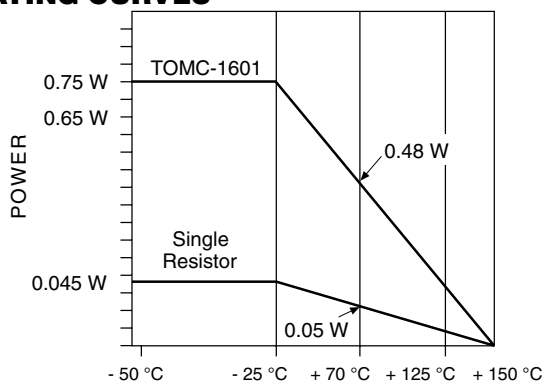
TEST	SPECIFICATIONS	CONDITIONS
Pin number	16	
Resistance Range	100 Ω to 100 k Ω	
TCR:	Tracking	± 5 ppm/°C
	Absolute	± 25 ppm/°C
Tolerance:	Ratio	± 0.5 %, ± 0.1 %, ± 0.05 %, ± 0.025 %
	Absolute	± 0.1 %, ± 0.5 %, ± 0.25 %, ± 0.1 %
Power Rating:	Resistor	Pin 1 Common = 50 mW Isolated = 100 mW
	Package	750 mW
Stability:	ΔR Absolute	500 ppm
	ΔR Ratio	150 ppm
Voltage Coefficient	0.1 ppm/V	
Working Voltage	50 V	
Operating Temperature Range	- 55 °C to + 125 °C	
Storage Temperature Range	- 55 °C to + 150 °C	
Noise	< - 30 dB	
Thermal EMF	0.08 μ V/°C	
Shelf Life Stability:	Absolute	100 ppm
	Ratio	20 ppm

* Pb containing terminations are not RoHS compliant, exemptions may apply

DIMENSIONS AND IMPRINTING in inches and millimeters

DIMENSION	INCHES	MM
D	0.050	1.27
E	0.018	0.457
F	0.160	4.06
G	0.08	2.03
H	0.036	0.914
J	0.22	5.59
K	0.244	6.20
L	0.30	7.52
M	0.045	1.14
N	0.003	0.076
P	0.005	1.27
Q	0.008	0.203
R	0.085	2.16
S	0.003	0.076

TYPE	A	B	C
16	0.350" (8.89)	0.400" (10.16)	0.440" (11.176)

DERATING CURVES**GLOBAL PART NUMBER INFORMATION**

New Global Part Numbering: TOMC16031002BUF (preferred part number format)

	T	O	M	C	1	6	0	3	1	0	0	2	B	U	F
T	O	M	C	T	1	6	0	1	1	0	0	3	Z	T	1

GLOBAL MODEL (4 or 5 digits)	PINS	SCHEMATIC	RESISTANCE	TOLERANCE AND RATIO TOLERANCE	PACKAGING
TOMC (Tin Lead)	16	01 = 15 bussed equal resistors	First 3 digits are significant figures and the last digit specifies the number of zeroes to follow. Example: 1002 = 10K 1003 = 100K	Abs. Tol. Ratio **A = 0.1 % 0.05 % B = 0.1 % 0.1 % C = 0.25 % 0.1 % D = 0.5 % 0.1 % F = 1 % 0.5 % *Z = 0.1 % 0.025 % * Tol. available 1K and up ** Tol. available 250 and up	TAPE AND REEL T0 = 100 Min 100 Mult T1 = 1000 Min 1000 Mult T3 = 300 Min 300 Mult T5 = 500 Min 500 Mult TF = Full Reel 2000 TS = 100 Min 1 Mult UF = TUBED
TOMCT (Lead (Pb)-free) (e3)		03 = 7 or 8 Isolated equal resistors			

Historical Part Number example: TOMC16011002Z (will continue to be accepted)

TOMC	16	01	1002	Z
SERIES	NUMBER OF LEADS	SCHEMATIC	RESISTANCE	TOLERANCE AND RATIO TOLERANCE



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