

HD74LS240

Octal Buffers / Line Drivers / Line Receivers
(inverted three-state outputs)

REJ03D0459-0200

Rev.2.00

Feb.18.2005

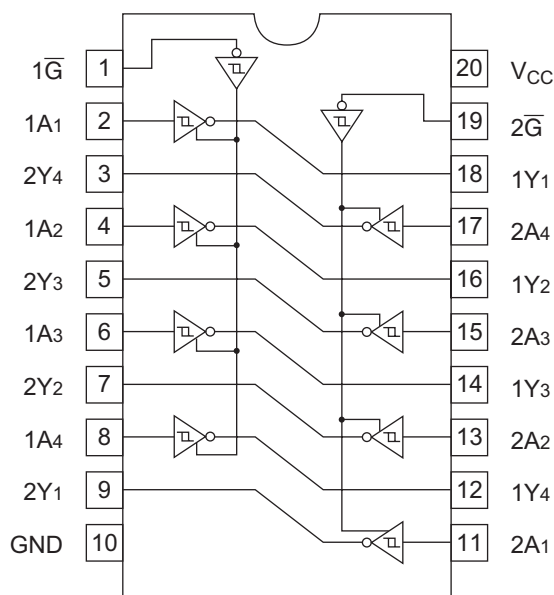
Features

- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS240P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	P	—
HD74LS240FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74LS240RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



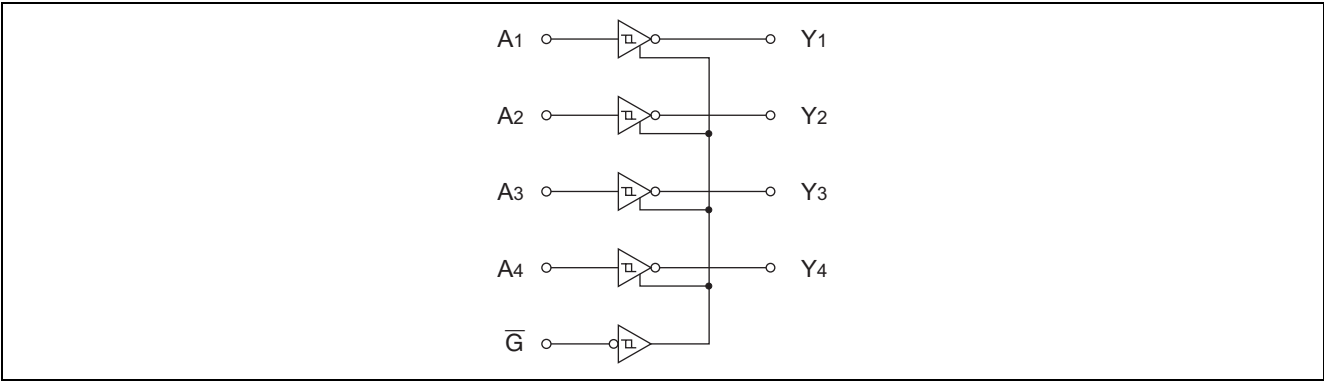
(Top view)

Function Table

Inputs		Output
$\overline{G}$	A	Y
H	X	Z
L	H	L
L	L	H

Note: H; high level, L; low level, X; irrelevant, Z; off (high-impedance) state of a 3-state output

Block Diagram (1/2)



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V_{CC}	7	V
Input voltage	V_{IN}	7	V
Power dissipation	P_T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Supply voltage	V_{CC}	4.75	5.00	5.25	V
Output current	I_{OH}	—	—	-15	mA
	I_{OL}	—	—	24	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

(Ta = -20 to +75 °C)

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V_{IH}	2.0	—	—	V	
	V_{IL}	—	—	0.8	V	
Hysteresis	$V_T^+ - V_T^-$	0.2	0.4	—	V	$V_{CC} = 4.75$ V
Output voltage	V_{OH}	2.4	—	—	V	$V_{IL} = 0.8$ V, $I_{OH} = -3$ mA
		2.0	—	—		$V_{IL} = 0.5$ V, $I_{OH} = -15$ mA
	V_{OL}	—	—	0.4	V	$I_{OL} = 12$ mA
		—	—	0.5		$I_{OL} = 24$ mA
Off-state output current	I_{OZH}	—	—	20	μ A	$V_O = 2.7$ V
	I_{OZL}	—	—	-20	μ A	$V_O = 0.4$ V
Input current	I_{IH}	—	—	20	μ A	$V_{CC} = 5.25$ V, $V_I = 2.7$ V
	I_{IL}	—	—	-0.2	mA	$V_{CC} = 5.25$ V, $V_I = 0.4$ V
	I_I	—	—	0.1	mA	$V_{CC} = 5.25$ V, $V_I = 7$ V
Short-circuit output current	I_{OS}	-40	—	-225	mA	$V_{CC} = 5.25$ V
Supply current**	Outputs high	I_{CC}	—	13	23	mA
	Outputs low		—	26	44	
	All outputs disabled		—	29	50	
Input clamp voltage	V_{IK}	—	—	-1.5	V	$V_{CC} = 4.75$ V, $I_{IN} = -18$ mA

Notes: * $V_{CC} = 5$ V, Ta = 25°C** I_{CC} is measured with all outputs open.

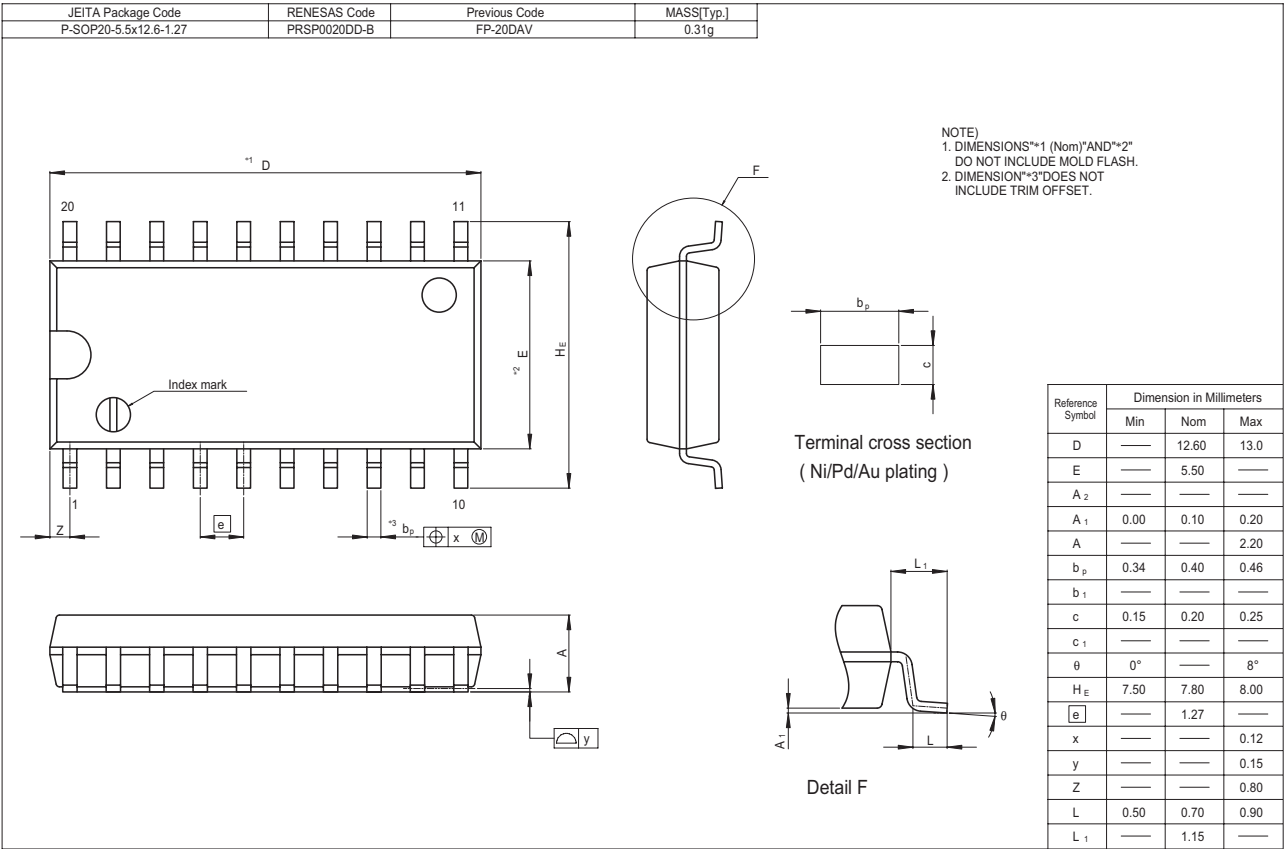
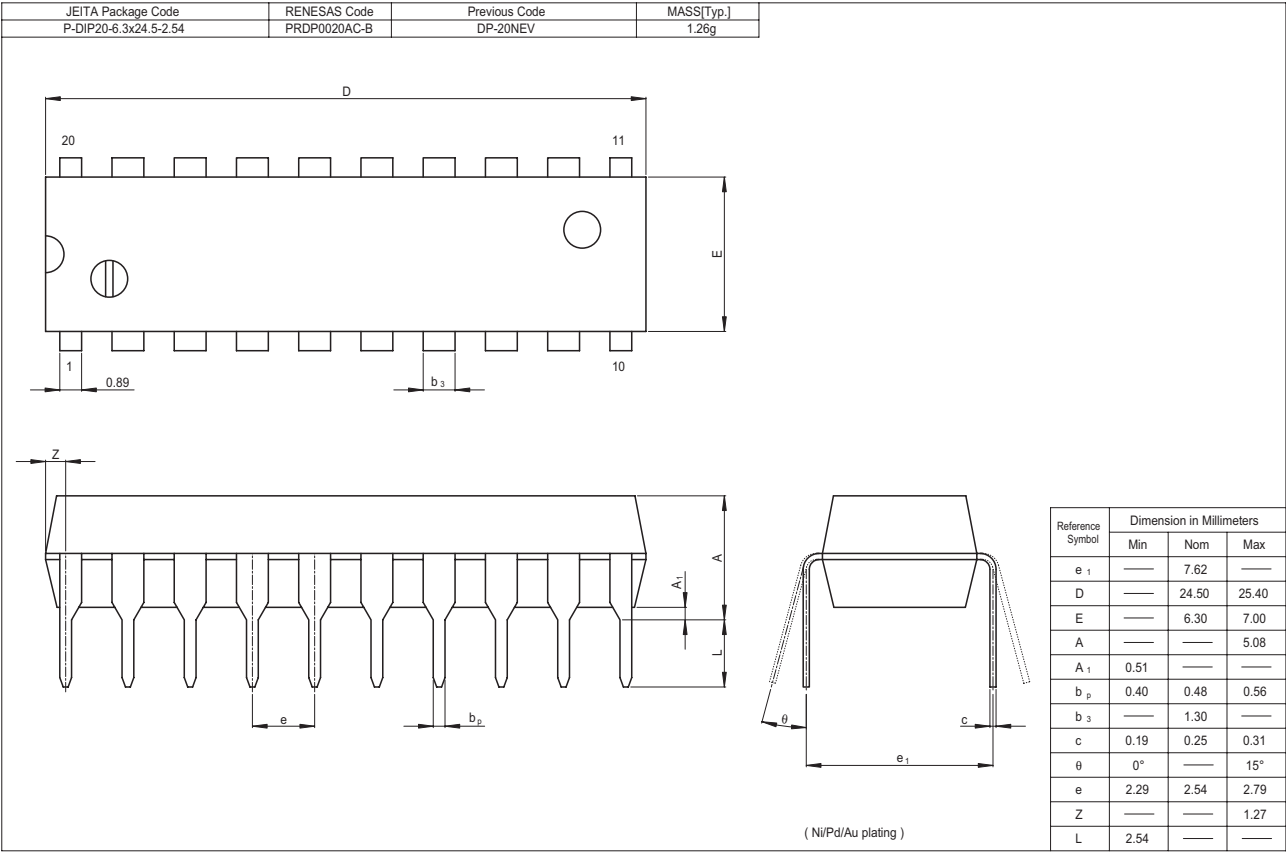
Switching Characteristics

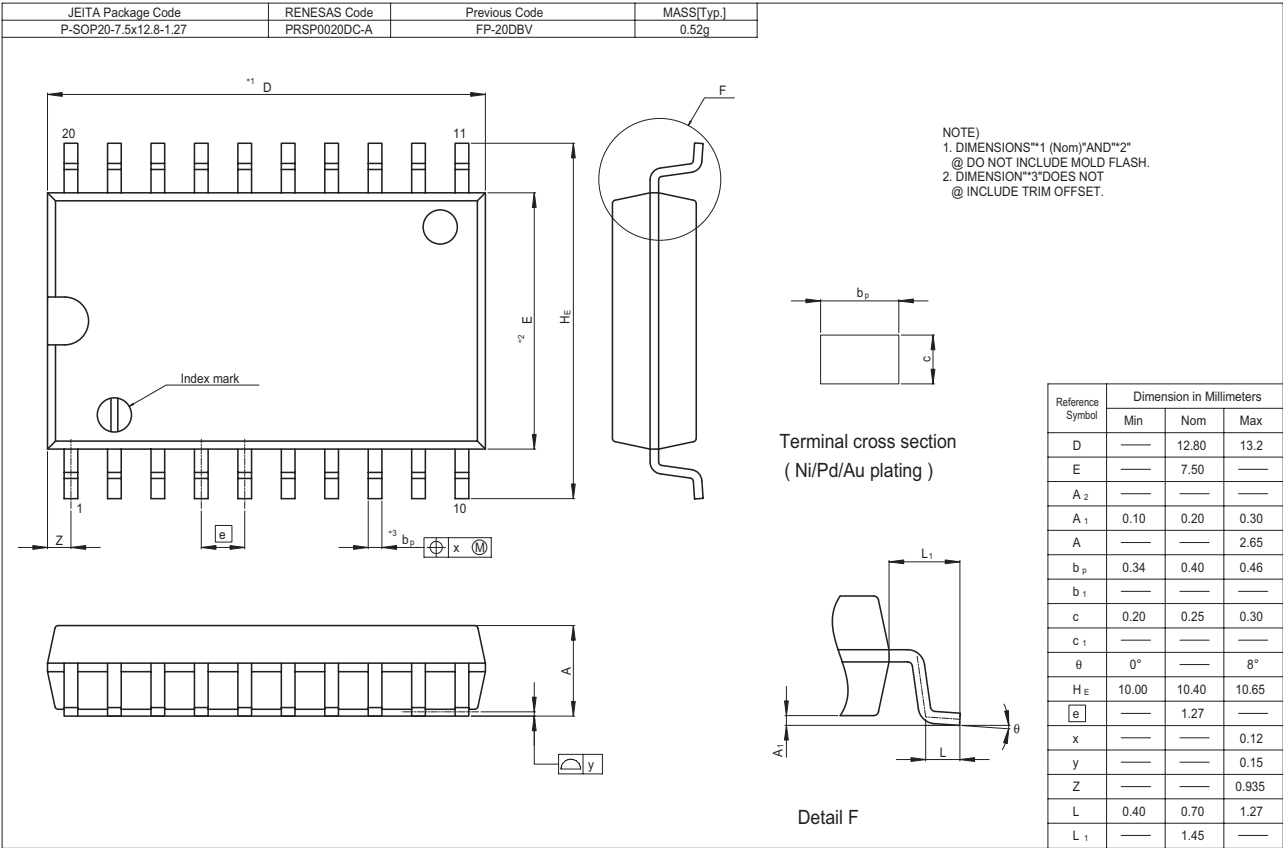
(V_{CC} = 5 V, Ta = 25°C)

Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t_{PLH}	—	9	14	ns	$C_L = 45$ pF, $R_L = 667$ Ω
	t_{PHL}	—	12	18		
Output enable time	t_{ZL}	—	20	30	ns	
	t_{ZH}	—	15	23	ns	
Output disable time	t_{LZ}	—	15	25	ns	$C_L = 5$ pF, $R_L = 667$ Ω
	t_{HZ}	—	10	18	ns	

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

Package Dimensions





Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001