



ON Semiconductor®

<http://onsemi.com>

2SK4197FS

N-Channel Power MOSFET 600V, 3.5A, 3.25Ω, TO-220F-3FS

Features

- High-speed switching.
- Avalanche resistance guarantee.
- 10V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		600	V
Gate-to-Source Voltage	V _{GSS}		±30	V
Drain Current (DC)	I _{DC} *1	Limited only by maximum temperature Tch=150°C	3.5	A
	I _{Dpack} *2	Tc=25°C (Our ideal heat dissipation condition)*3	3.3	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	13	A
Allowable Power Dissipation	P _D		2.0	W
		Tc=25°C (Our ideal heat dissipation condition)*3	28	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	E _{AS}		29	mJ
Avalanche Current *5	I _{AV}		3.3	A

Note : *1 Shows chip capability.

*2 Package limited.

*3 Our condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

*4 V_{DD}=50V, L=5mH, I_{AV}=3.3A

*5 L≤5mH, Single pulse

Marking : K4197FS

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

2SK4197FS

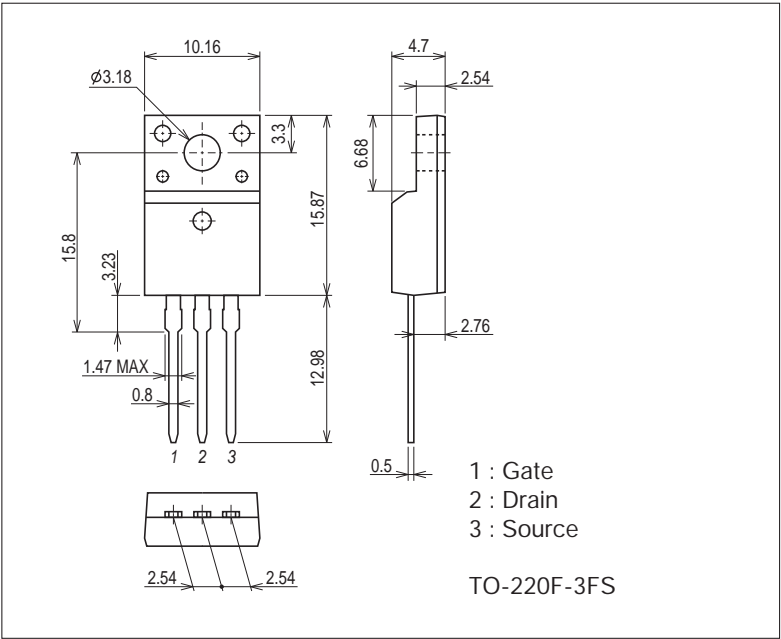
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=10mA, V_{GS}=0V$	600			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=480V, V_{GS}=0V$			100	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$			± 100	nA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	3		5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=1.8A$	0.8	1.6		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)}$	$I_D=1.8A, V_{GS}=10V$		2.5	3.25	Ω
Input Capacitance	C_{iss}	$V_{DS}=30V, f=1MHz$		260		pF
Output Capacitance	C_{oss}	$V_{DS}=30V, f=1MHz$		50		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=30V, f=1MHz$		9.7		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		12		ns
Rise Time	t_r	See specified Test Circuit.		20		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		28		ns
Fall Time	t_f	See specified Test Circuit.		12		ns
Total Gate Charge	Q_g	$V_{DS}=200V, V_{GS}=10V, I_D=3.5A$		11		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=200V, V_{GS}=10V, I_D=3.5A$		2.6		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=200V, V_{GS}=10V, I_D=3.5A$		5.8		nC
Diode Forward Voltage	V_{SD}	$I_S=3.5A, V_{GS}=0V$		0.9	1.2	V

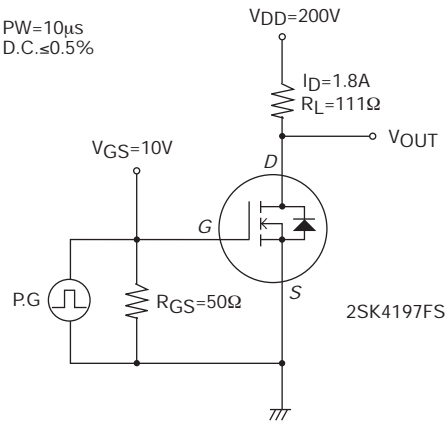
Package Dimensions

unit : mm (typ)

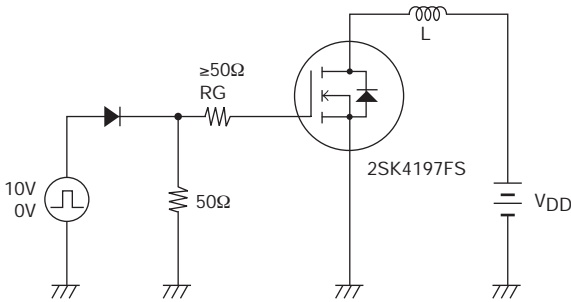
7528-001

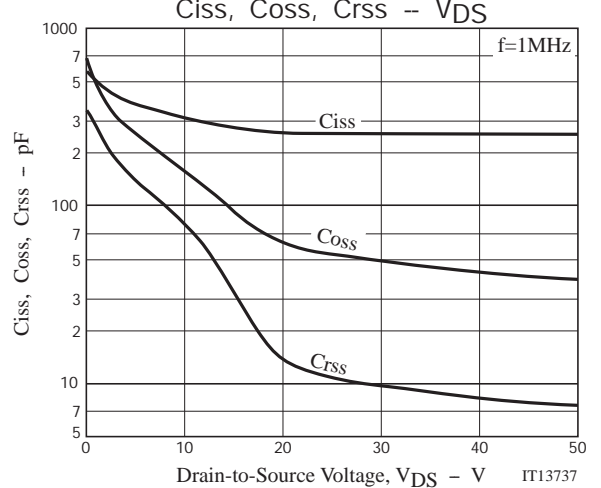
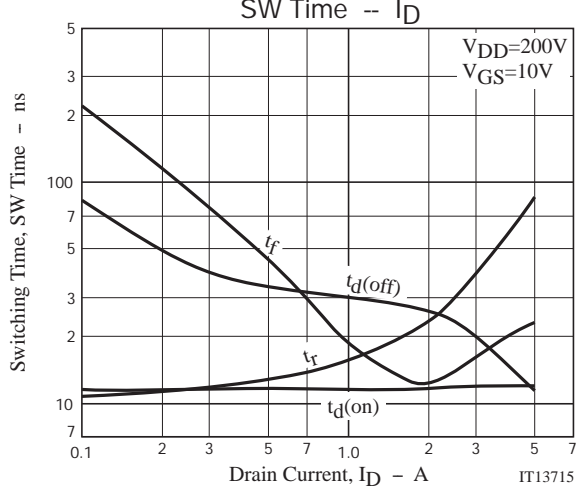
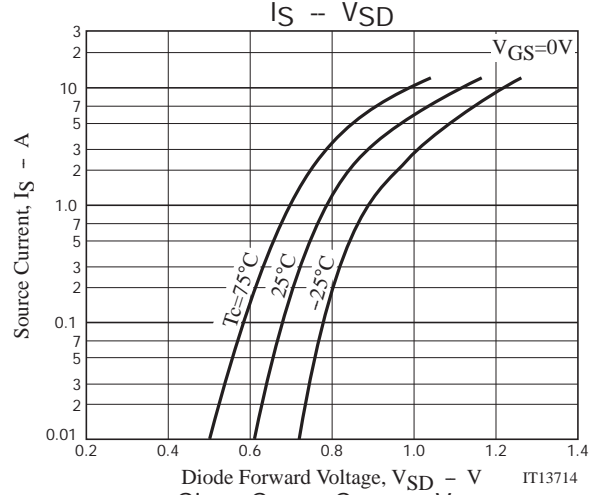
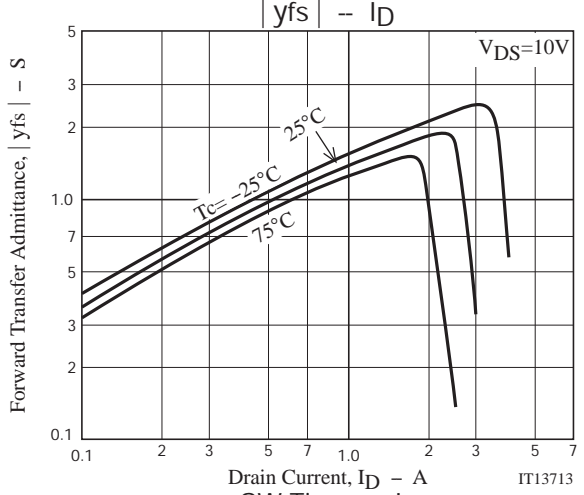
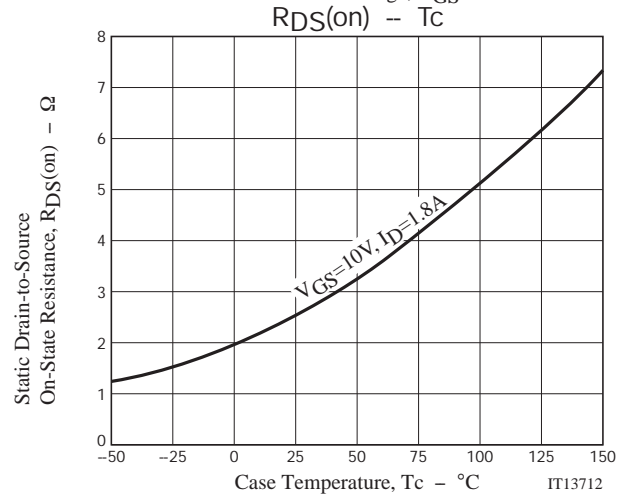
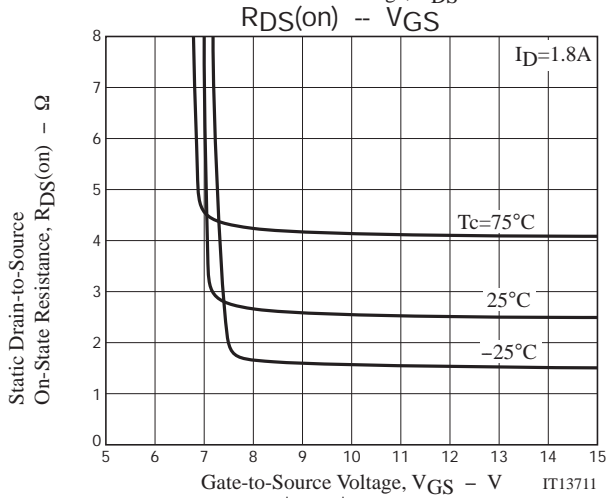
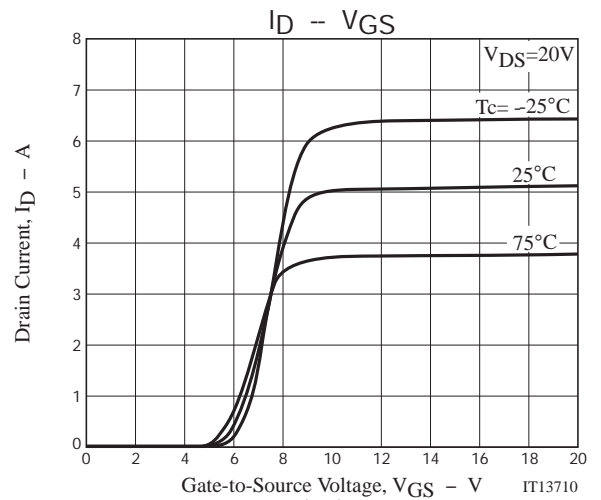
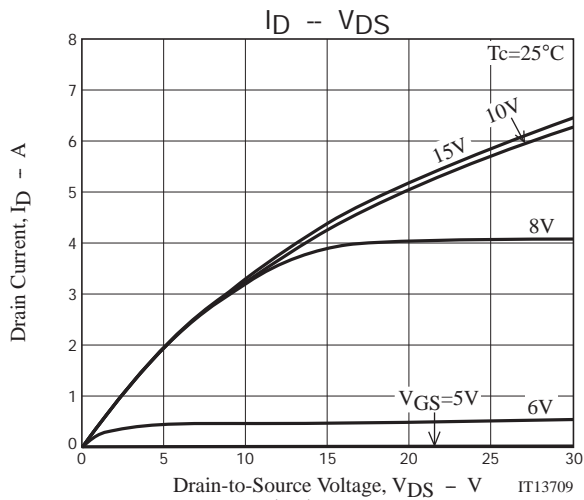


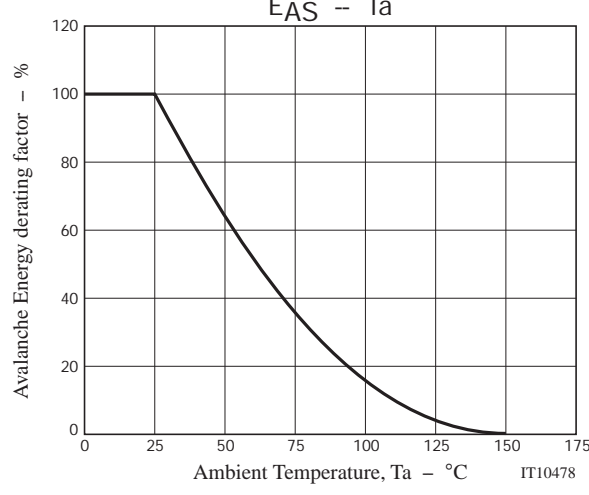
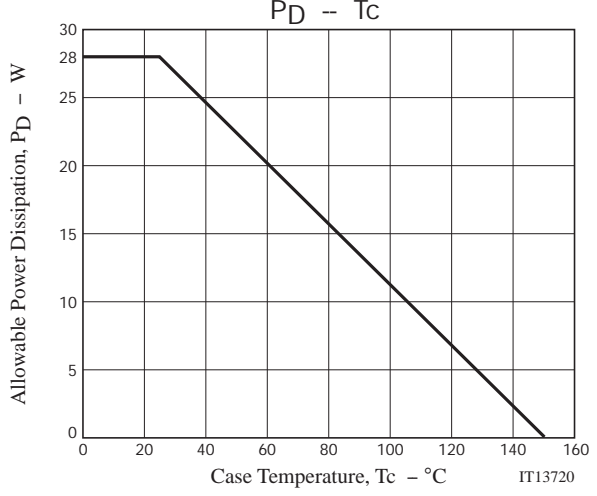
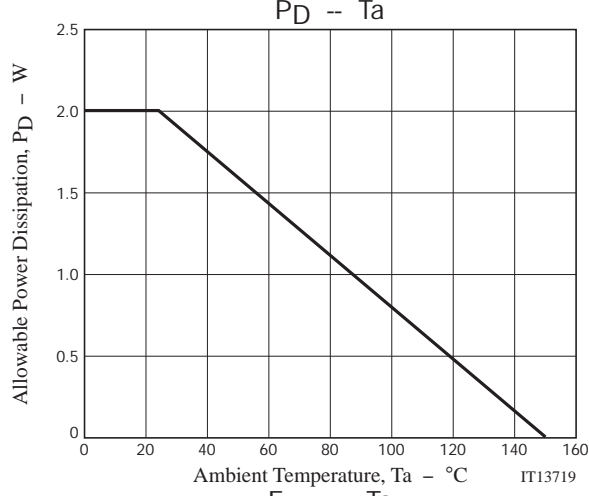
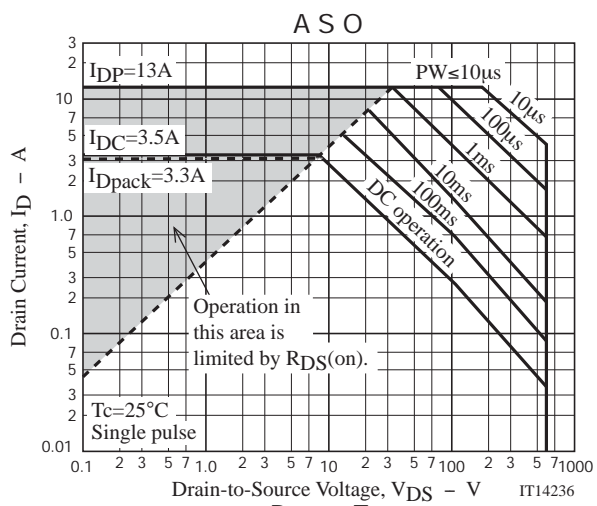
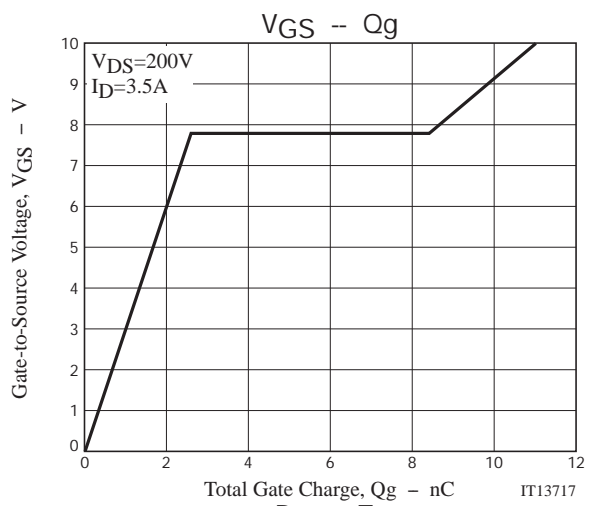
Switching Time Test Circuit



Avalanche Resistance Test Circuit







Note on usage : Since the 2SK4197FS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.