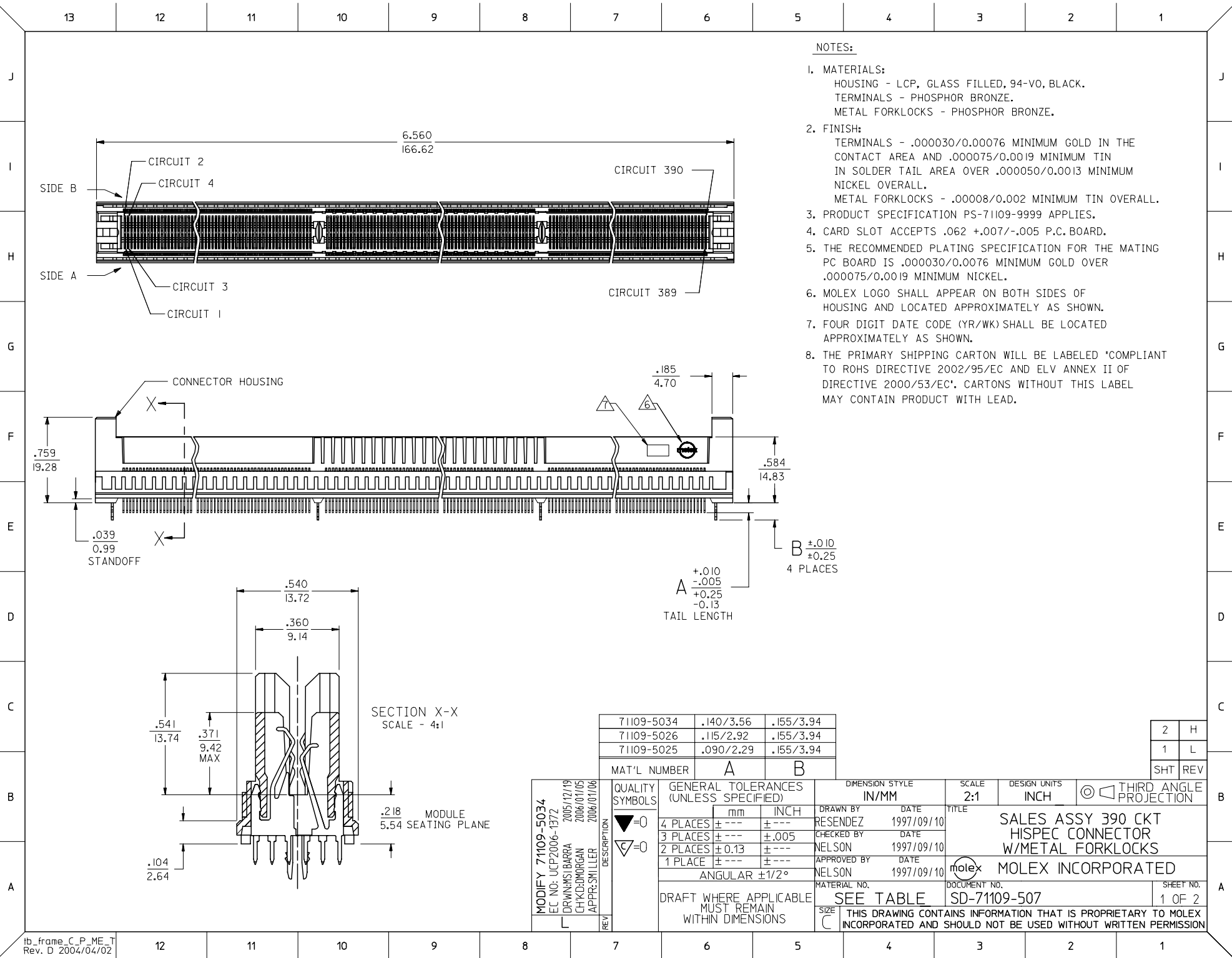
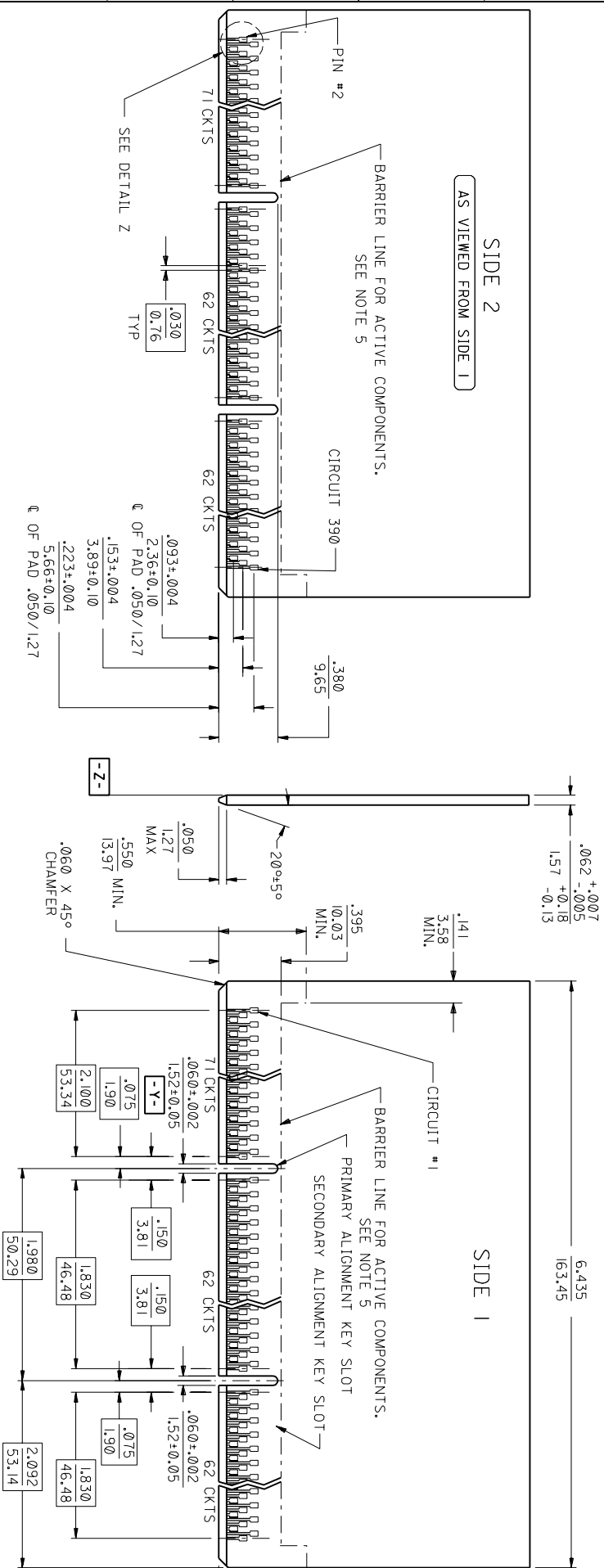
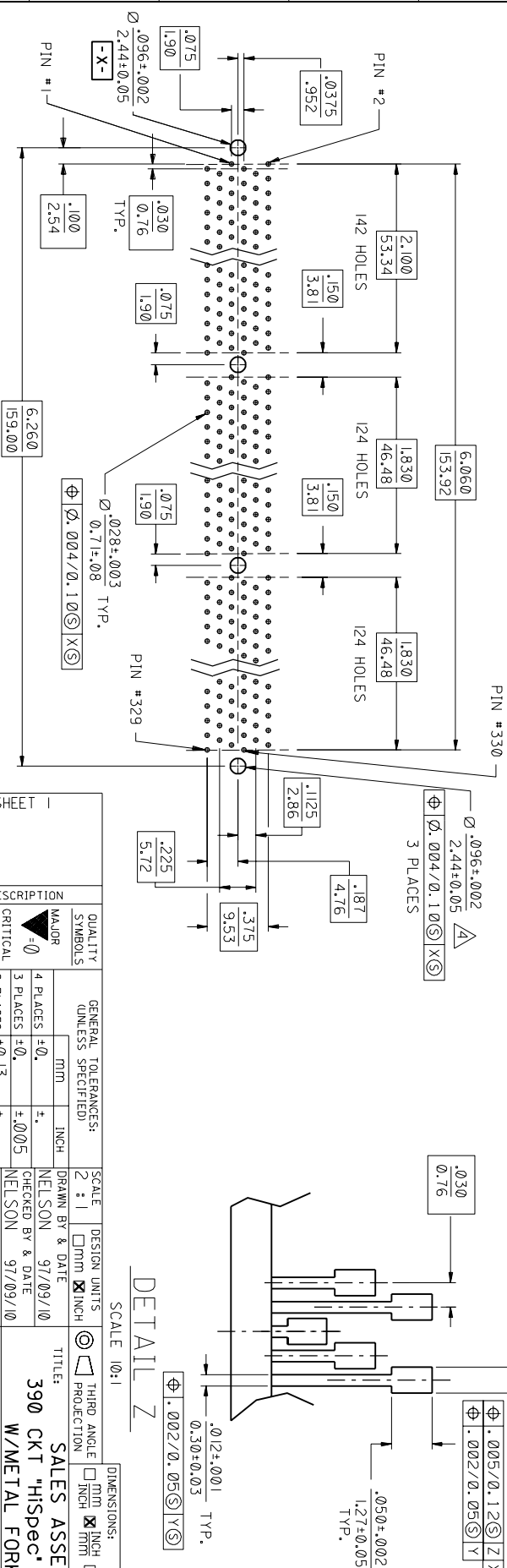






RECOMMENDED MODULE BOARD LAYOUT



RECOMMENDED PCB HOLE LAYOUT

[illegible]