Product Brief



OctalFRAMER

8 Channel E1/T1/J1 Framer PEF 22557 E

OctalFRAMER is the latest addition to Infineon's market leading FALC° family of highly advanced E1/T1/J1 components. The eight channel E1/T1/J1 framer is suitable for wide variety of network equipment including SDH/SONET Add Drop Multiplexer and digital cross connects.

The OctalFRAMER features a unique clock generation unit that accepts any reference clock between 1.02 and 20MHz.

Using industry leading Evaluation support tools, system developers can shorten design cycles while creating a wide range of highly flexible, low BOM E1/T1/J1 line cards.

Applications

- Broadband Wireless Access
- Multi-service access platforms
- Digital loop carriers
- Remote access servers/concentrators
- SONET/SDH Add/Drop multiplexers
- Digital Cross connects

Features

- ITU-T G.704 frame alignment/synthesis for 2.048/1.544 Mbit/s
- Programmable frame formats
 - E1: Double- & CRC Multi-frame
 - T1: F4, F12 (D4), Ext. Super Frame (ESF), F72 (SLC96)
- Detects and generates LOS, AIS and RAI alarms
- CRC-4 performance monitoring
- PRBS generation and monitoring
- Detects & generates LOS, AIS & RAI alarms
- System bus data rate scalable from 1.544 Mbit/s up to 16 Mbit/s
- Synchronization Status Message (SSM) generation and extraction

HDLC Controllers

- 24 HDLC controllers (three per channel) including 128-bytes deep FIFO buffers each
- CAS controller with micro-processor or system interface serial access
- Supports signaling system #7
- ANSI T1.403 Bit-Oriented Messages (BOM)
- Generates periodical performance reports

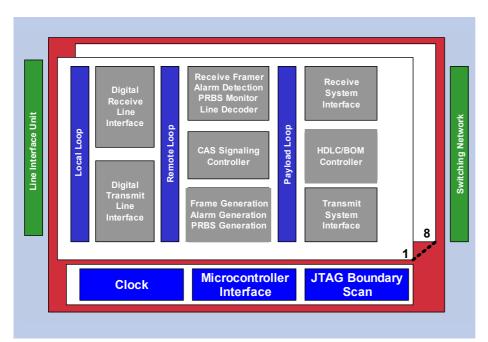
General Features

- Software compatible to QuadFALC and OctalFALC
- Meets Japanese standards including JT G.703, 704, 706, I.431
- Intel® or Motorola® type 8/16-bit microprocessor interface
- Serial SPI bus and serial SCI bus slave interfaces
- Low power (60 mW per channel typical)
- Dual voltage 1.8 V/3.3 V
- PG-LBGA-256, 17x17mm with 1.0mm ball pitch
- -40°C to +85°C operation
- Rohs compliant package

www.infineon.com/falc

COMMUNICATION

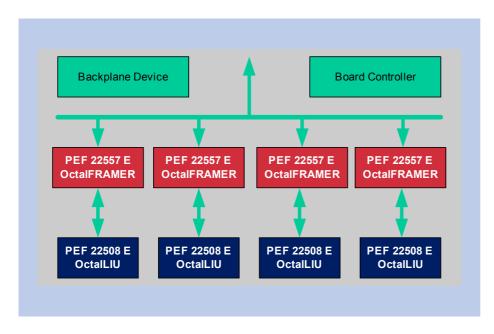




Block Diagram OctalFRAMER

Ordering Information

Туре	Description	Package
PEF 22557	8 channel E1/T1/J1	PG-LBGA-256
	Framer	



Application Example

How to reach us:

http://www.infineon.com

Published by Infineon Technologies AG St.-Martin-Strasse 53 81669 München

© Infineon Technologies AG 2004. All Rights Reserved.

Template: pb_tmplt.fm/4

Attention please!

The information herein is given to describe certain components and shall not be considered as a guarantee of characteristics. Terms of delivery and rights to technical change reserved.

We hereby disclaim any and all warranties, including but not limited to warranties of non-infringement, regarding circuits, descriptions and charts stated herein.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office.

Warnings

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in lifesupport devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.

Ordering No. B000-H0000-X-X-7600 Printed in Germany