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ADC1410S105F2 Obsolete

ADC1410S105F2 demoboard; LVDS/DDR version;

IDTs ADC1410S105F2 demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVDS DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

Features

- Single power supply (on-board regulators)
- SMA connector for clock and analog input signals
- LVDS/DDR outputs

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Buy Sample
ADC1410S105F2-DB	Obsolete	BD0	BOARD	0	I	No	Availability

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Demo Board Software ADC_1_r04	ZIP	2.33 MB	Jul 18, 2012

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ADC1410S105F2 - Evaluation Board

