

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSII)

2SK3236

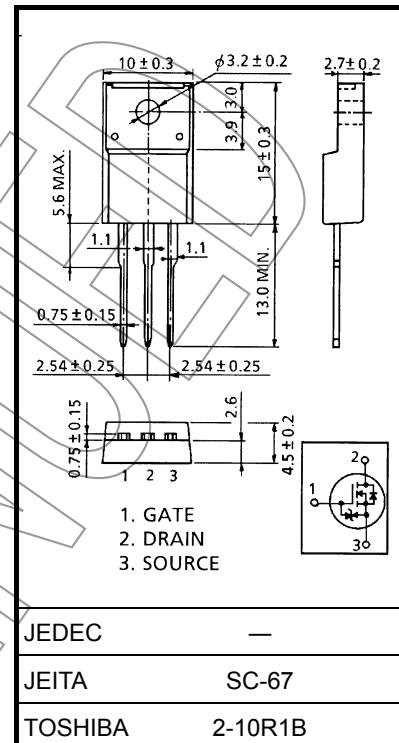
Switching Regulator Applications, DC-DC Converter and
Motor Drive Applications

Unit: mm

- 4-V gate drive
- Low drain-source ON resistance: $R_{DS\ (ON)} = 13.5\ m\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 42\ S$ (typ.)
- Low leakage current: $I_{DSS} = 100\ \mu A$ (max) ($V_{DS} = 60\ V$)
- Enhancement model: $V_{th} = 1.3$ to $2.5\ V$ ($V_{DS} = 10\ V$, $I_D = 1\ mA$)

Absolute Maximum Ratings ($T_a = 25^\circ C$)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	60	V
Drain-gate voltage ($R_{GS} = 20\ k\Omega$)	V_{DGR}	60	V
Gate-source voltage	V_{GSS}	± 20	V
Drain current	DC (Note 1)	I_D	A
	Pulse (Note 1)	I_{DP}	
Drain power dissipation ($T_c = 25^\circ C$)	P_D	30	W
Single pulse avalanche energy (Note 2)	E_{AS}	68	mJ
Avalanche current	I_{AR}	35	A
Repetitive avalanche energy (Note 3)	E_{AR}	3.0	mJ
Channel temperature	T_{ch}	150	$^\circ C$
Storage temperature range	T_{stg}	-55 to 150	$^\circ C$



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th\ (ch-c)}$	4.16	$^\circ C/W$
Thermal resistance, channel to ambient	$R_{th\ (ch-a)}$	62.5	$^\circ C/W$

Note 1: Ensure that the channel temperature does not exceed $150^\circ C$.

Note 2: $V_{DD} = 50\ V$, $T_{ch} = 25^\circ C$, $L = 40\ \mu H$, $R_G = 25\ \Omega$, $I_{AR} = 35\ A$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

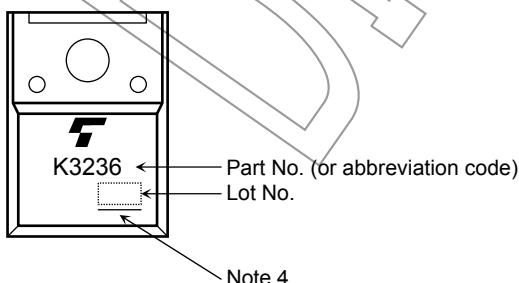
Electrical Characteristics ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GSS}	$V_{GS} = \pm 16\text{ V}$, $V_{DS} = 0\text{ V}$	—	—	± 10	μA
Drain cut-OFF current	I_{DSS}	$V_{DS} = 60\text{ V}$, $V_{GS} = 0\text{ V}$	—	—	100	μA
Drain-source breakdown voltage	$V_{(BR) DSS}$	$I_D = 10\text{ mA}$, $V_{GS} = 0\text{ V}$	60	—	—	V
Gate threshold voltage	V_{th}	$V_{DS} = 10\text{ V}$, $I_D = 1\text{ mA}$	1.3	—	2.5	V
Drain-source ON resistance	$R_{DS (\text{ON})}$	$V_{GS} = 4\text{ V}$, $I_D = 18\text{ A}$	—	22	36	$\text{m}\Omega$
		$V_{GS} = 10\text{ V}$, $I_D = 18\text{ A}$	—	13.5	20	
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = 10\text{ V}$, $I_D = 18\text{ A}$	21	42	—	S
Input capacitance	C_{iss}	$V_{DS} = 10\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	—	2300	—	pF
Reverse transfer capacitance	C_{rss}		—	220	—	
Output capacitance	C_{oss}		—	370	—	
Switching time	Rise time	t_r		—	9	ns
	Turn-ON time	t_{on}		23	—	
	Fall time	t_f		20	—	
	Turn-OFF time	t_{off}		100	—	
Total gate charge (gate-source plus gate-drain)	Q_g	$V_{DD} \approx 48\text{ V}$, $V_{GS} = 10\text{ V}$, $I_D = 35\text{ A}$	—	52	—	nC
Gate-source charge	Q_{gs}		—	37	—	
Gate-drain ("miller") charge	Q_{gd}		—	15	—	

Source-Drain Ratings and Characteristics ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	—	—	—	35	A
Pulse drain reverse current (Note 1)	I_{DRP}	—	—	—	105	A
Forward voltage (diode)	V_{DSF}	$I_{DR} = 35\text{ A}$, $V_{GS} = 0\text{ V}$	—	—	-1.7	V
Reverse recovery time	t_{rr}	$I_{DR} = 35\text{ A}$, $V_{GS} = 0\text{ V}$, $dI_{DR}/dt = 50\text{ A}/\mu\text{s}$	—	60	—	ns
Reverse recovery charge	Q_{rr}	—	—	81	—	nC

Marking

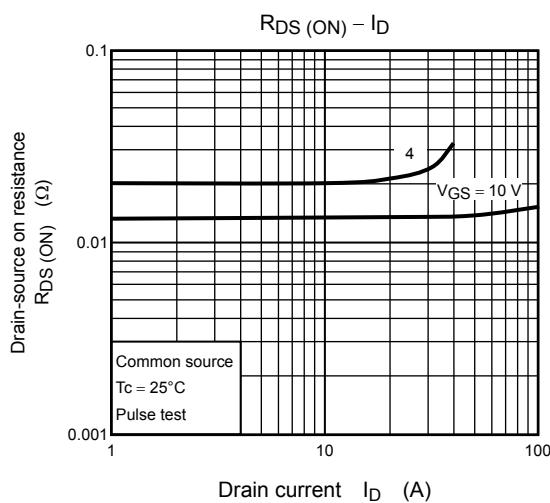
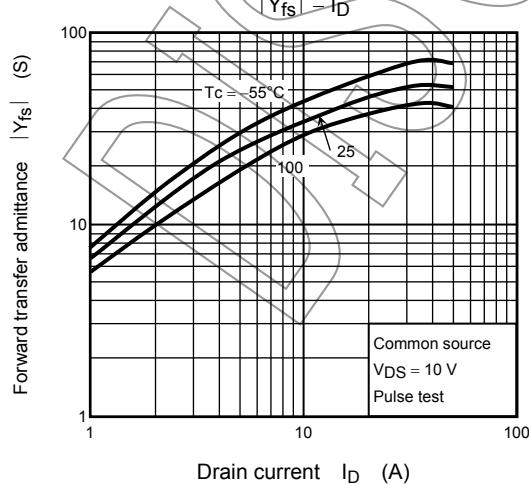
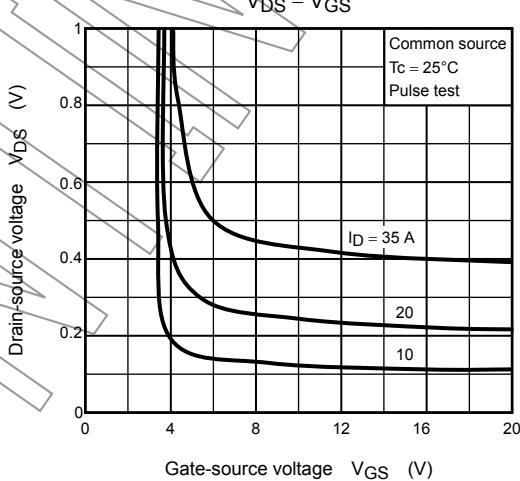
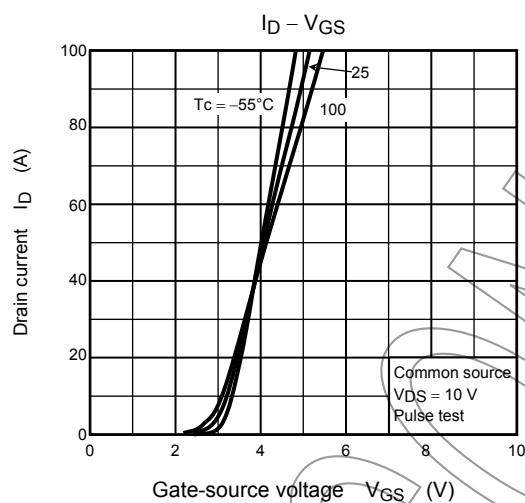
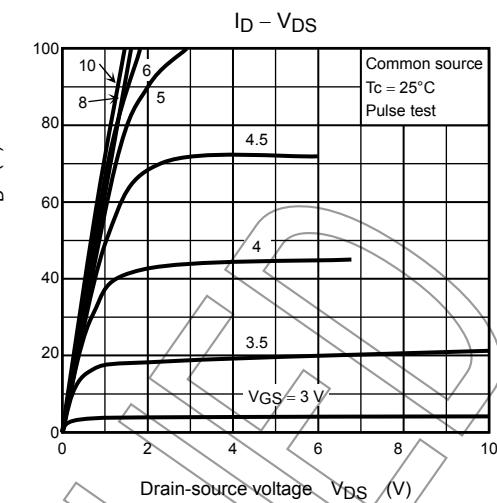
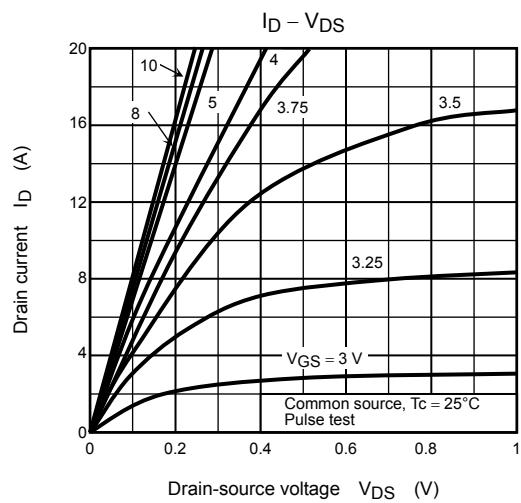


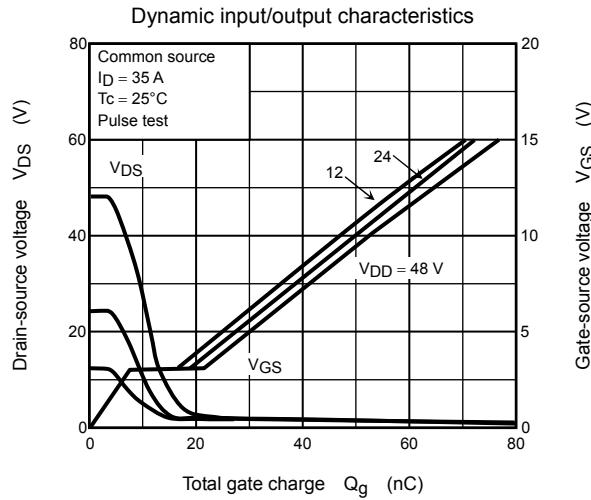
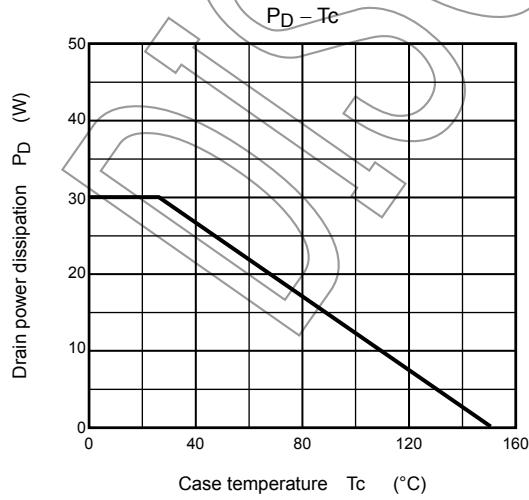
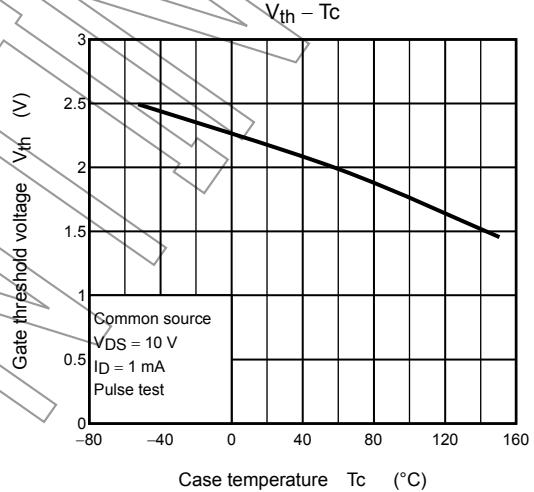
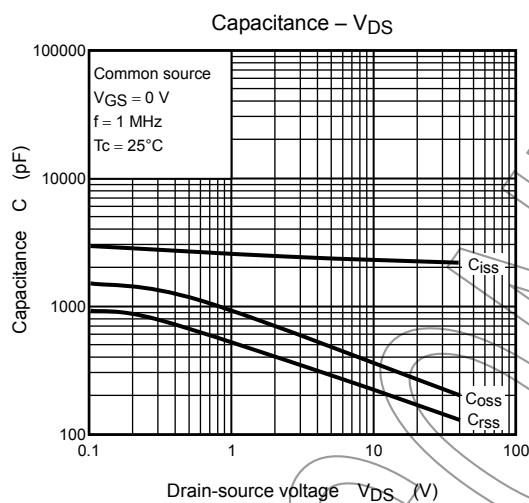
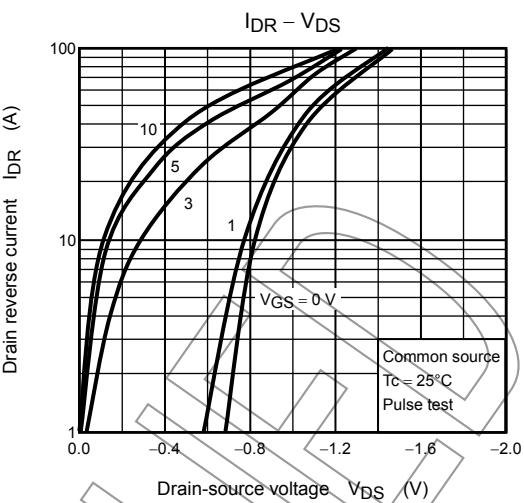
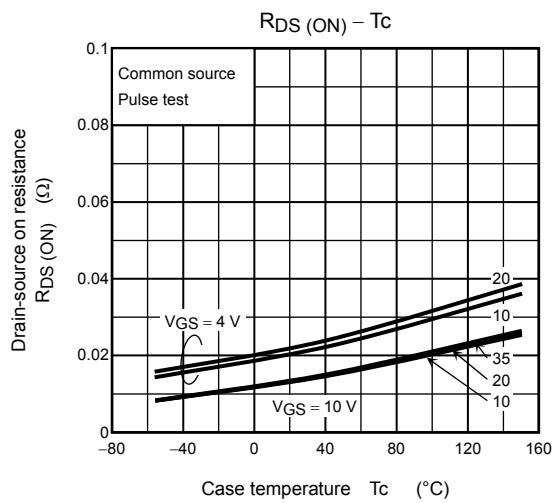
Note 4: A line under a Lot No. identifies the indication of product Labels.

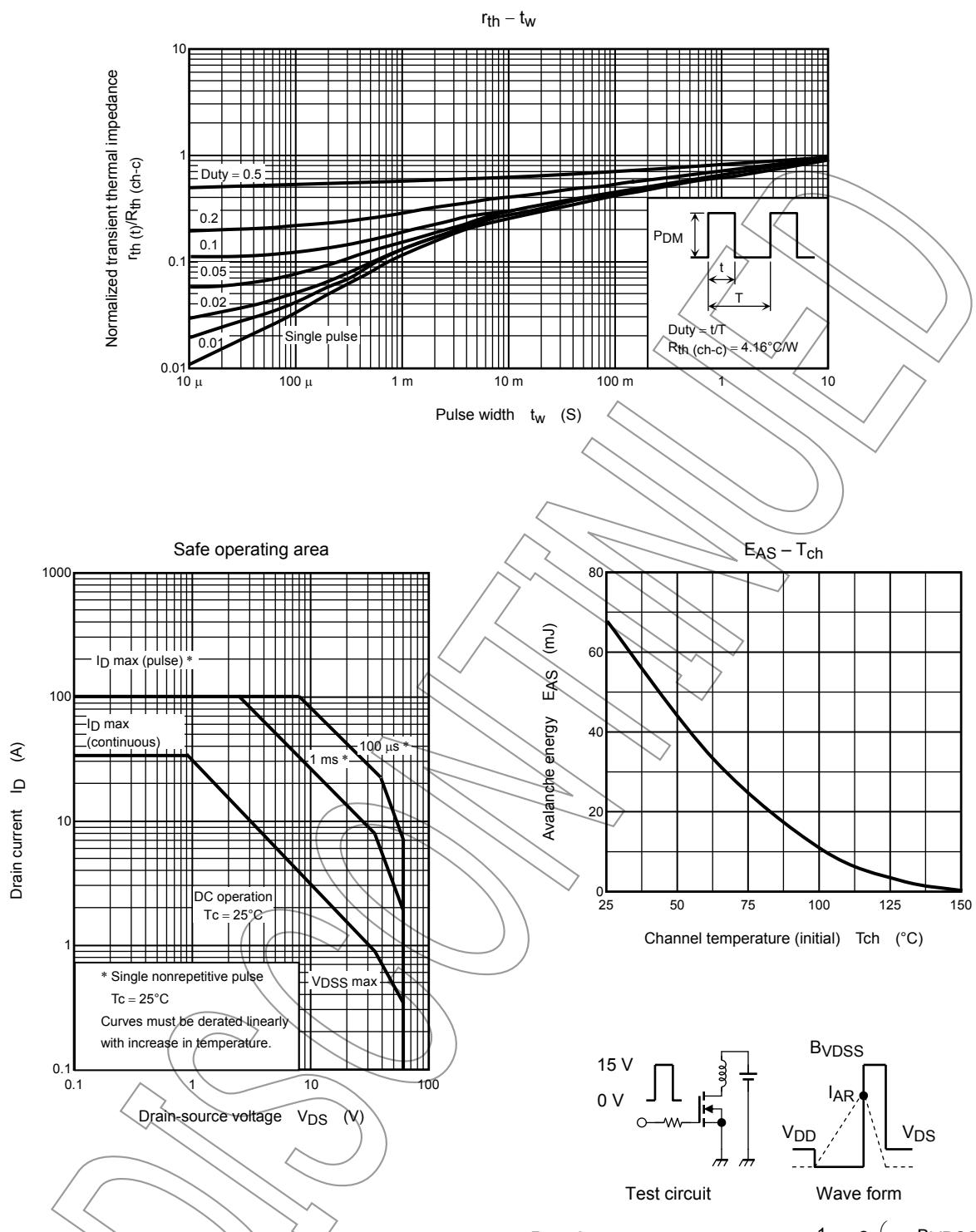
Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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