



SPECIFICATIONS:

ELECTRICAL CHARACTERISTICS:

DIELECTRIC	NPO
RATED VOLTAGE	100 VDC
CAPACITANCE RANGE (EIA)	12 pFd (120)
CAPACITANCE TOLERANCE	K ($\pm 10\%$)
TEST PARAMETERS	1 MHz ± 50 Hz, Values ≤ 100 pF 1 KHz ± 50 Hz, Values > 100 pF @ 1.0 VRMS, 25°C
TEMPERATURE COEFFICIENT	0 ± 30 ppm / °C (-55°C TO +125°C)
DISSIPATION FACTOR	0.1% Max, 25°C
INSULATION RESISTANCE	> 1000 Ohm-Farad OR 100 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement)
DIELECTRIC STRENGTH	$> 2.5 \times$ WVDC, 50 mA max

MECHANICAL CHARACTERISTICS

LENGTH "L"	WIDTH "W"	THICKNESS "T"	ENDBAND "EB"
.080 $\pm$.010	.050 $\pm$.010	.055 MAX	.020 $\pm$.010

TERMINATION DESCRIPTION:

TERMINATION: P - PALLADIUM SILVER FOR EPOXY BONDING

PACKAGING DESCRIPTION:

PACKAGE: T - 7" PLASTIC EMBOSSED TAPE/REEL

MARKING DESCRIPTION:

MARK: 4- NO MARK

				JOHANSON DIELECTRICS 		15191 BLEDSOE ST SYLMAR, CA 91342		
				TITLE				
REV	DESCRIPTION	APPROVED	DATE	MULTILAYER CHIP CAPACITOR				
ENG. MGR.		DATE	Q.A. MGR.	DATE	PART NO.		DWG SCALE	
					101R15N120KP4T		NONE	
DRN BY		DATE	CHKD BY	DATE	DWG. NO.		SHEET	RE V
C.ENGLAND		11/09/09			101R15N120KP4T		1 OF 1	