TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA8266HQ

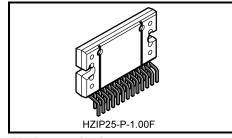
Max Power 35 W BTL × 4 ch Audio Power IC

The TA8266HQ is 4 ch BTL audio power amplifier for car audio application.

This IC can generate more high power: $P_{OUT}MAX = 35~W$ as it is included the pure complementary PNP and NPN transistor output stage.

It is designed low distortion ratio for 4 ch BTL audio power amplifier, built-in stand-by function, muting function, and diagnosis circuit which can detect output to V_{CC}/GND short and over voltage input mode.

Additionally, the AUX amplifier and various kind of protector for car audio use is built-in.



Weight: 7.7 g (typ.)

Features

- High power: POUTMAX (1) = 35 W (typ.)
 - $(\text{V}_{\text{CC}}=14.4~\text{V},\,\text{f}=1~\text{kHz},\,\text{J}\text{EITA}\,\text{max},\,\text{RL}=4~\Omega)$
 - : POUTMAX(2) = 31 W (typ.)
 - $(V_{CC} = 13.7 \text{ V}, f = 1 \text{ kHz}, \text{JEITA max}, R_L = 4 \Omega)$
 - : $P_{OUT}(1) = 23 \text{ W (typ.)}$
 - $(V_{CC} = 14.4 \text{ V}, f = 1 \text{ kHz}, THD = 10\%, R_L = 4 \Omega)$
 - : POUT(2) = 20 W (typ.)
 - $(V_{CC} = 13.2 \text{ V}, f = 1 \text{ kHz}, THD = 10\%, R_L = 4 \Omega)$
- Built-in diagnosis circuit (pin 25)
- Low distortion ratio: THD = 0.02% (typ.)

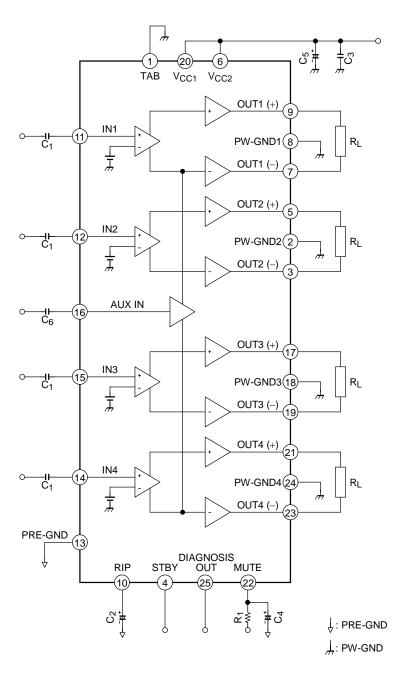
$$(V_{CC} = 13.2 \text{ V}, f = 1 \text{ kHz}, P_{OUT} = 5 \text{ W}, R_{L} = 4 \Omega)$$

• Low noise: VNO = 0.18 mVrms (typ.)

(V_{CC} = 13.2 V, R_g = 0
$$\Omega$$
, G_V = 34dB, BW = 20 Hz~20 kHz)

- Built-in stand-by switch function (pin 4)
- Built-in muting function (pin 22)
- Built-in AUX amplifier from single input to 2 channels output (pin 16)
- Built-in various protection circuit
 - : Thermal shut down, over voltage, out to GND, out to VCC, out to out short, speaker burned
- Operating supply voltage: VCC (opr) = 9~18 V
 - Note 1: Install the product correctly. Otherwise, it may result in break down, damage and/or degradation to the product or equipment.
 - Note 2: These protection functions are intended to avoid some output short circuits or other abnormal conditions temporarily. These protect functions do not warrant to prevent the IC from being damaged.
 - In case of the product would be operated with exceeded guaranteed operating ranges, these protection features may not operate and some output short circuits may result in the IC being damaged.

Block Diagram



Note3: Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purpose.

Caution and Application Method

(Description is made only on the single channel.)

1. Voltage Gain Adjustment

This IC has no NF (negative feedback) terminals. Therefore, the voltage gain can't adjusted, but it makes the device a space and total costs saver.

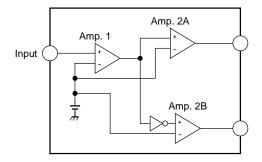


Figure 1 Block Diagram

 $\label{eq:continuous} \begin{array}{ll} The \ voltage \ gain \ of \ Amp. 1 & \vdots \ GV1 = 8dB \\ The \ voltage \ gain \ of \ Amp. 2A, \ B & \vdots \ GV2 = 20dB \\ The \ voltage \ gain \ of \ BLT \ Connection & \vdots \ GV \ (BTL) = 6dB \\ Therefore, \ the \ total \ voltage \ gain \ is \ decided \ by \ expression \ below. \end{array}$

$$G_V = G_{V1} + G_{V2} + G_{V}$$
 (BTL) = $8 + 20 + 6 = 34dB$

2. Stand-by SW Function (pin 4)

By means of controlling pin 4 (stand-by terminal) to high and low, the power supply can be set to ON and OFF. The threshold voltage of pin 4 is set at about $3V_{BE}$ (typ.), and the power supply current is about $2~\mu A$ (typ.) at the stand-by state.

Control Voltage of pin 4: V_{SB}

| Stand-by | Power | V _{SB} (V) |
|----------|-------|---------------------|
| ON | OFF | 0~1.5 |
| OFF | ON | 3~V _{CC} |

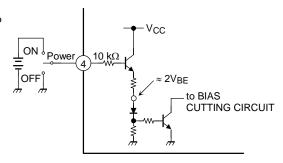


Figure 2 With pin 4 set to High, Power is turned ON

Adjustage of Stand-by SW

- Since VCC can directly be controlled to ON or OFF by the microcomputer, the switching relay can be omitted.
- (2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching

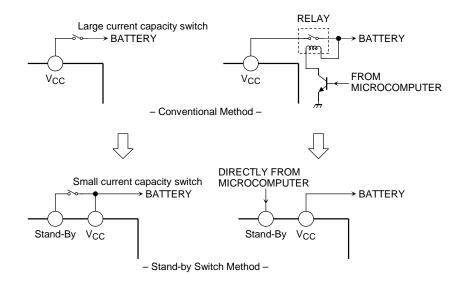


Figure 3

3. Muting Function (pin 22)

By means of controlling pin 22 less than 0.5 V, it can make the audio muting condition.

The muting time constant is decided by R₁ and C₄ and these parts is related the pop noise at power ON/OFF.

The series resistance; R1 must be set up less than $10 \text{ k}\Omega$ to get enough muting attenuation.

The muting function have to be controlled by a transistor, FET and micro-controller port which has $I_{MUTE}>250~\mu\text{A}$ ability.

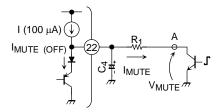
Pin 22 terminal voltage has the temperature characteristics of $4.6~\rm V$ (low temperature) to $3.2~\rm V$ (high temperature).

Therefore, it is need to design with attention as using the micro-controller of which operating voltage is less than 5 V.

Terminal 22 may not be pulled up and shall be controlled by OPEN/LOW.

When it is obliged to do, it must be pulled up via diode, because it has to defend flowing reverse current to internal circuit of pin 22.

<Recommended Application>



<Application for pulled up>

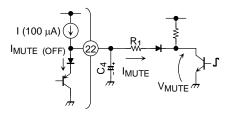


Figure 4 Muting Function

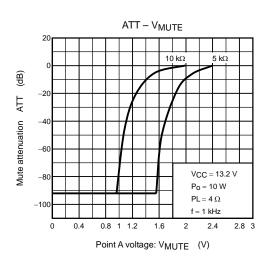


Figure 5 Mute Attenuation – V_{MUTE} (V)

4. AUX Input (pin 16)

The pin 16 is for input terminal of AUX amplifier.

The total gain is 0dB by using of AUX amplifier.

Therefore, the $\mu\text{-}\mathrm{COM}$ can directly drive the AUX amplifier.

BEEP sound or voice synthesizer signal can be input to pin 16 directly.

When AUX function is not used, this pin must be connected to PRE-GND (pin 13) via a capacitor.

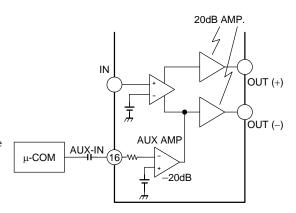


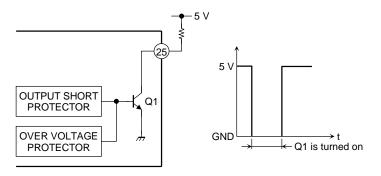
Figure 6 AUX Input

5. Diagnosis Output (pin 25)

This diagnosis output terminal of pin 25 has open collector output structure on chip as shown in Figure 7. In case diagnosis circuit that detect unusual case is operated, NPN transistor (Q1) is turned on.

It is possible to protect all the system of apparatus as well as power IC protection.

In case of being unused this function, use this IC as open-connection on pin 25.



pin 25: Open collector output (active low)

Figure 7 Self Diagnosis Output

5.1 In Case of Shorting Output to V_{CC}/GND or Over Voltage Power Supplied

NPN transistor (Q1) is turned on.

Threshold of over voltage protection: VCC = 22 V (typ.)

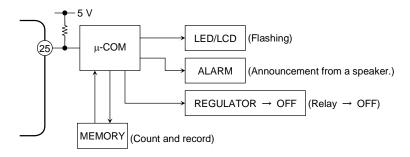


Figure 8 Application 1

5.2 In Case of Shorting Output to Output

NPN transistor (Q1) is turned on and off in response to the input signal voltage.

6. Prevention of speaker burning accident (In Case of Rare Short Circuit of Speaker)

When the direct current resistance between OUT + and OUT – terminal becomes 1 Ω or less and output current over 4 A flows, this IC makes a protection circuit operate and suppresses the current into a speaker. This system makes the burning accident of the speaker prevent as below mechanism.

<The guess mechanism of a burning accident of the speaker>

Abnormal output offset voltage (voltage between OUT + and OUT -) over 4 V is made by the external circuit failure.(Note 4)

J

The speaker impedance becomes 1 Ω or less as it is in a rare short circuit condition.

The current more than 4 A flows into the speaker and the speaker is burned.

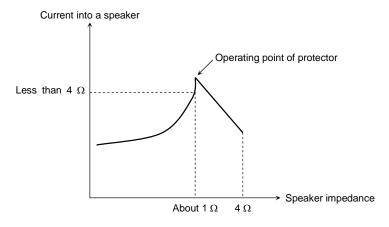


Figure 9

Note 4: It is appeared by biased input DC voltage (for example, large leakage of the input capacitor, short-circuit between copper patterns of PCB.)

6.1 Applications

When output terminals short-circuit to VCC or GND, the voltage of 25pin is fixed to "L".

And when shorting OUT + to OUT -, "L" and "H" are switched according to an input signal.

Therefore, it is possible to judge how the power IC condition is if a micro-controller detects the 25pin voltage that is smoothed out with LPF.

It is recommend that the threshold voltage (Vth) is set up as higher as possible because output level of LPF is changed according to an input signal.

(for example, Vth is set up to $4\ V$ if 25pin is pulled up to $5\ V$ line.)

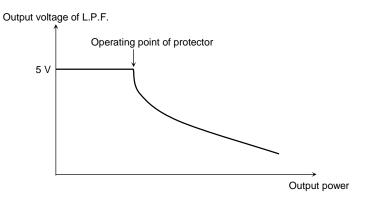


Figure 10

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | Symbol | Rating | Unit | |
|-----------------------------|-------------------------|---------|------|--|
| Peak supply voltage (0.2 s) | V _{CC} (surge) | 50 | V | |
| DC supply voltage | V _{CC (DC)} | 25 | V | |
| Operation supply voltage | V _{CC} (opr) | 18 | V | |
| Output current (peak) | I _{O (peak)} | 9 | Α | |
| Power dissipation | P _D (Note5) | 125 | W | |
| Operation temperature | T _{opr} | -40~85 | °C | |
| Storage temperature | T _{stg} | -55~150 | °C | |

Note5: Package thermal resistance $\theta_{j-T} = 1^{\circ}C/W$ (typ.)

(Ta = 25°C, with infinite heat sink)

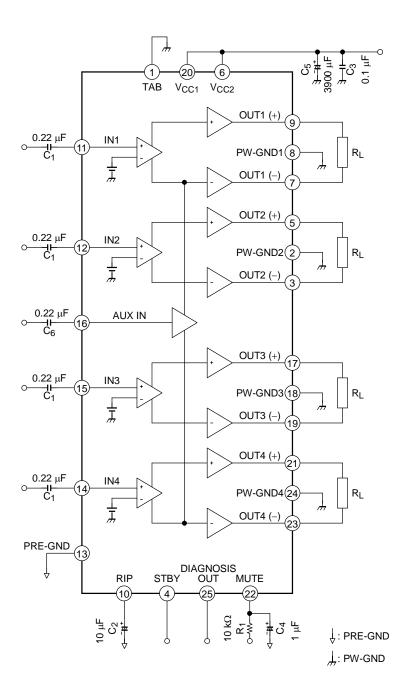
The absolute maximum ratings of a semiconductor device are a set of specified parameter values, which must not be exceeded during operation, even for an instant. If any of these rating would be exceeded during operation, the device electrical characteristics may be irreparably altered and the reliability and lifetime of the device can no longer be guaranteed. Moreover, these operations with exceeded ratings may cause break down, damage and/or degradation to any other equipment. Applications using the device should be designed such that each absolute maximum rating will never be exceeded in any operating conditions. Before using, creating and/or producing designs, refer to and comply with the precautions and conditions set forth in this documents.

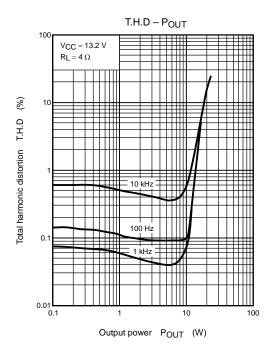
Electrical Characteristics (unless otherwise specified $V_{CC} = 13.2 \text{ V}$, f = 1 kHz, R_L = 4 Ω , Ta = 25°C)

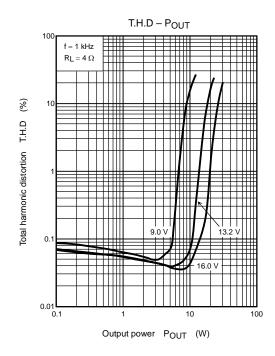
| Characteristics | Symbol | Test Circuit | Test Condition | Min | Тур. | Max | Unit |
|------------------------------|--------------------------|-----------------|-------------------------------------------------------------------------------------------------------------------------|------|------|-----------------|---------|
| Quiescent current | Iccq | _ | $V_{IN} = 0$ | _ | 200 | 400 | mA |
| Output power | Pout MAX (1) | _ | V _{CC} = 14.4 V, max Power | _ | 35 | _ | w |
| | P _{OUT} MAX (2) | _ | V _{CC} = 13.7 V, max Power | _ | 31 | _ | |
| | P _{OUT} (1) | _ | V _{CC} = 14.4 V, THD = 10% | _ | 23 | _ | |
| | P _{OUT} (2) | _ | THD = 10% | 17 | 20 | _ | |
| Total harmonic distortion | THD | _ | P _{OUT} = 5 W | _ | 0.02 | 0.2 | % |
| Voltage gain | G _V | _ | V _{OUT} = 0.775 Vrms (0dBm) | 32 | 34 | 36 | - dB |
| Voltage gain ratio | ΔG _V | _ | V _{OUT} = 0.775 Vrms (0dBm) | -1.0 | 0 | 1.0 | |
| Output noise voltage | V _{NO} (1) | _ | Rg = 0 Ω , DIN45405 | _ | 0.20 | _ | - mVrms |
| | V _{NO} (2) | _ | $Rg = 0 \Omega$, $BW = 20 Hz~20 kHz$ | _ | 0.18 | 0.42 | |
| Ripple rejection ratio | R.R. | _ | $ f_{\mbox{rip}} = \mbox{100 Hz}, \mbox{ Rg} = \mbox{620 } \Omega \\ \mbox{V}_{\mbox{rip}} = \mbox{0.775 Vrms (0dBm)} $ | 40 | 50 | | dB |
| Cross talk | C.T. | _ | $Rg = 620 \Omega$ $V_{OUT} = 0.775 \text{ Vrms (0dBm)}$ | _ | 60 | | dB |
| Output offset voltage | V _{OFFSET} | _ | _ | -150 | 0 | +150 | mV |
| Input resistance | R _{IN} | _ | _ | _ | 30 | _ | kΩ |
| Stand-by current | I _{SB} | _ | Stand-by condition | _ | 2 | 10 | μА |
| Stand-by control voltage | V _{SB} H | _ | Power: ON | 3.0 | _ | V _{CC} | V |
| | V _{SB} L | _ | Power: OFF | 0 | _ | 1.5 | |
| Mute control voltage (Note6) | V _M H | _ | Mute: OFF | Open | | | |
| | V _M L | _ | Mute: ON, $R_1 = 10 \text{ k}\Omega$ | 0 | | 0.5 | V |
| Mute attenuation | ATT M | _ | Mute: ON, V _{OUT} = 7.75 Vrms (20dBm) at Mute: OFF. | 80 | 90 | _ | dB |

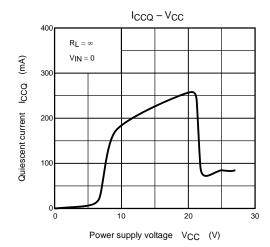
Note6: Muting function have to be controlled by open and low logic, which logic is a transistor, FET and μ -COM port of I_{MUTE} > 250 μ A ability.This means than the mute control terminal : pin 22 must not be pulled-up.

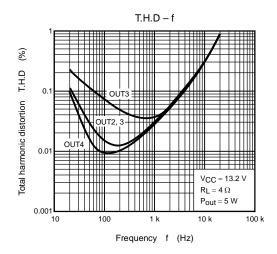
Test Circuit

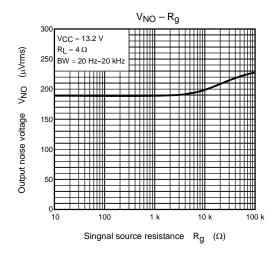


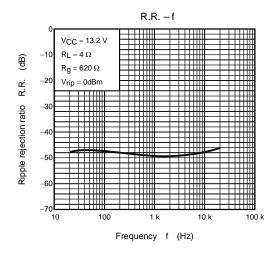


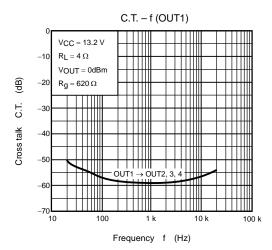


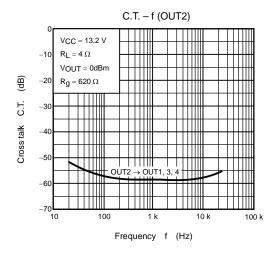


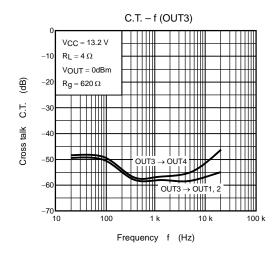


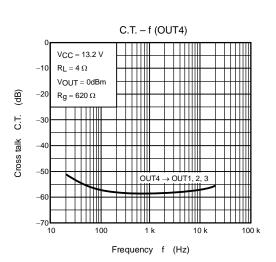


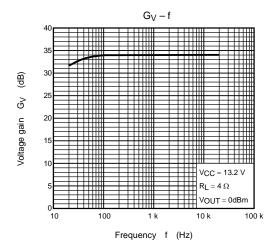


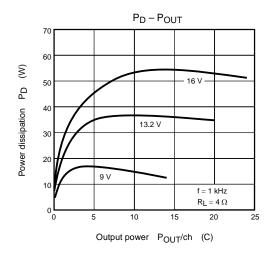


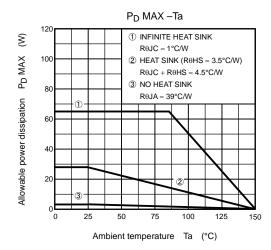






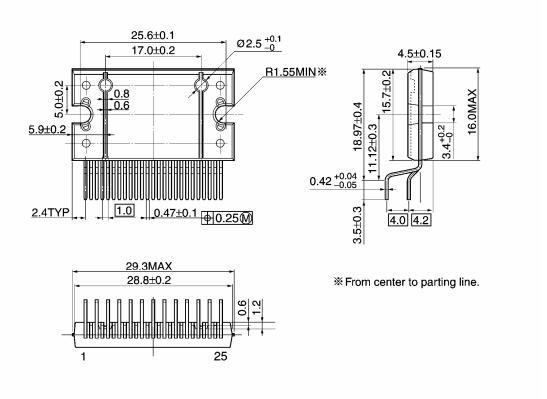






Package Dimensions

HZIP25-P-1.00F Unit: mm



Weight: 7.7 g (typ.)

- Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over
 current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute
 maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or
 load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the
 effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time
 and insertion circuit location, are required.
- If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to
 prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or
 the negative current resulting from the back electromotive force at power OFF. For details on how to connect a
 protection circuit such as a current limiting resistor or back electromotive force adsorption diode, refer to individual
 IC datasheets or the IC databook. IC breakdown may cause injury, smoke or ignition.
- Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- Carefully select external components (such as inputs and negative feedback capacitors) and load components
 (such as speakers), for example, power amp and regulator. If there is a large amount of leakage current such as
 input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to
 a speaker with low input withstand voltage, overcurrent or IC failure can cause smoke or ignition. (The over
 current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied
 Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.

• Over current Protection Circuit

Over current protection circuits (referred to as current limiter circuits) do not necessarily protect ICs under all circumstances. If the Over current protection circuits operate against the over current, clear the over current status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the over current protection circuit to not operate properly or IC breakdown before operation. In addition, depending on the method of use and usage conditions, if over current continues to flow for a long time after operation, the IC may generate heat resulting in breakdown.

Thermal Shutdown Circuit

Thermal shutdown circuits do not necessarily protect ICs under all circumstances. If the Thermal shutdown circuits operate against the over temperature, clear the heat generation status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the thermal shutdown circuit to not operate properly or IC breakdown before operation.

· Heat Radiation Design

When using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (Tj) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into considerate the effect of IC heat radiation with peripheral components.

• Installation to Heat Sink

Please install the power IC to the heat sink not to apply excessive mechanical stress to the IC. Excessive mechanical stress can lead to package cracks, resulting in a reduction in reliability or breakdown of internal IC chip. In addition, depending on the IC, the use of silicon rubber may be prohibited. Check whether the use of silicon rubber is prohibited for the IC you intend to use, or not. For details of power IC heat radiation design and heat sink installation, refer to individual technical datasheets or IC databooks.

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In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc. 021023_A

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- This product generates heat during normal operation. However, substandard performance or malfunction may
 cause the product and its peripherals to reach abnormally high temperatures.
 The product is often the final stage (the external output stage) of a circuit. Substandard performance or
 malfunction of the destination device to which the circuit supplies output may cause damage to the circuit or to the

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About solderability, following conditions were confirmed

Solderability

product. 030619_R

- (1) Use of Sn-37Pb solder Bath
 - · solder bath temperature = 230°C
 - · dipping time = 5 seconds
 - the number of times = once
 - · use of R-type flux
- (2) Use of Sn-3.0Ag-0.5Cu solder Bath
 - · solder bath temperature = 245°C
 - · dipping time = 5 seconds
 - · the number of times = once
 - · use of R-type flux