



SM44T Series 1.8 V CMOS Clock Oscillators

December 2011



- Pletronics' SM44 Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 0.8 to 145 MHZ
- 2.5 x 3.2 mm LCC Ceramic Package
- Enable/Disable Function
- Disable function includes low standby power mode
- Low Jitter

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following:

Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.041 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V_{CC} Supply Voltage	-0.5V to +7.0V
V_i Input Voltage	-0.5V to $V_{CC} + 0.5V$
V_o Output Voltage	-0.5V to $V_{CC} + 0.5V$
I_o Output Current	+25 mA to -25 mA

Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 40 to 60°C/Watt depending on the solder pads, ground plane and construction of the PCB.

Part Number:

SM44	45	T	E	X	- 75.0M	-XX		
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel	
							Frequency in MHZ	
							Supply Voltage V_{cc} X = 1.8V ± 10%	
							Temperature Range Blank = Temp. range -10 to +70°C C = Temp. range -20 to +70°C E = Temp. range -40 to +85°C	
							Series Model	
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm	
							Series Model	

Part Marking:

PFF.FFF
• **YMDxx**

Marking Legend:

P = Pletronics

FF.FFF = Frequency in MHZ

YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	10	1	2	3	4	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2010	2011	2012	2013	2014	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Electrical Specification for 1.80V $\pm 10\%$ over the specified temperature range

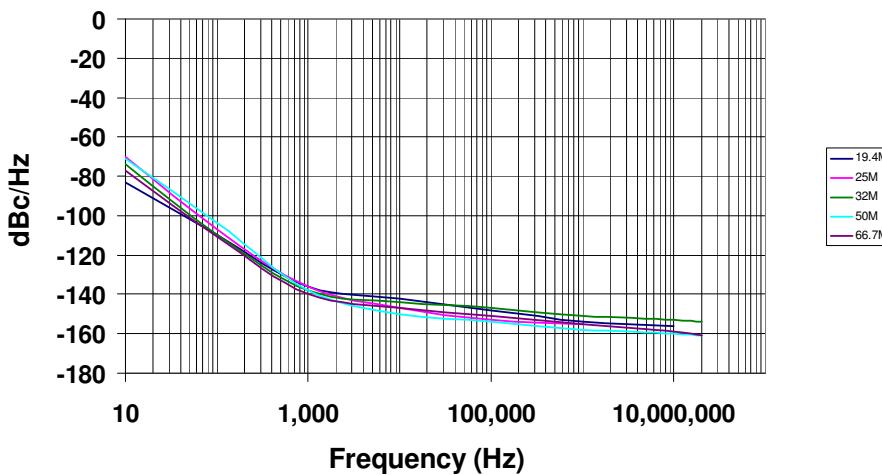
Item	Min	Max	Unit	Condition
Frequency Range	0.8	100	MHZ	
Frequency Accuracy "45" "44" "20"	-50 -25 -20	+50 +25 +20	ppm	For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures
Output Waveform		CMOS		
Output High Level	90	-	%	of V_{CC} for $I_{OH} = +2$ mA < 35 MHZ
	70	-		of V_{CC} for $I_{OH} = +8$ mA ≥ 35 MHZ
Output Low Level	-	10	%	of V_{CC} for $I_{OL} = -2$ mA < 35 MHZ
	-	30		of V_{CC} for $I_{OL} = -8$ mA ≥ 35 MHZ
Output Symmetry	45	55	%	at 50% point of V_{CC} (See load circuit)
Jitter Output: 1 to 15 MHZ	-	6.0	pS RMS	10 Hz to 1 MHZ from the output frequency
Output: 15 to 35 MHZ	-	5.0	pS RMS	
Output: 35 to 50 MHZ	-	4.0	pS RMS	
Output: > 50 MHZ	-	3.0	pS RMS	
Output: > 25 MHZ	-	0.7	pS RMS	12 KHz to 20 MHZ from the output frequency
E/D Internal Pull-up	50	500	Kohm	to V_{CC}
V disable	-	30	%	of V_{CC} applied to pin 1
V enable	70	-	%	
Output leakage $V_{OUT} = V_{CC}$	-10	+10	uA	Pin 1 low, device disabled
$V_{OUT} = 0V$	-10	+10	uA	
Standby Current I_{CC}	-	4	uA	< 35 MHZ
	-	100	uA	≥ 35 MHZ
Enable time	-	250	nS	Time for output to reach a logic state
Disable time	-	250	nS	Time for output to reach a high Z state
Start up time	-	10	mS	Time for output to reach specified frequency
Operating Temperature Range	-10	+70	°C	Standard Temperature Range
	-10	+70	°C	Extended Temperature Range "C" Option
	-40	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	+125	°C	

Electrical Specification for 1.80V $\pm 10\%$ over the specified temperature range

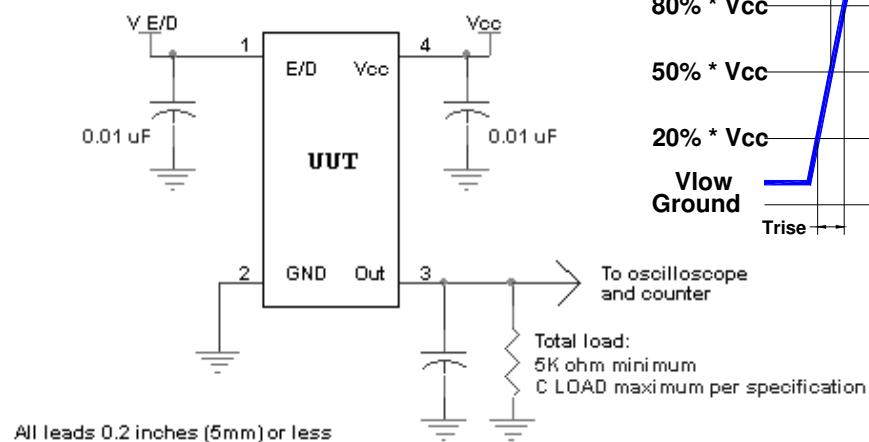
Item	Typ	Max	Unit	Condition	
Output T_{RISE} and T_{FALL}	2	5	nS	$< 35 \text{ MHZ}$	$C_{LOAD} = 15 \text{ pF}$ 20% to 80% of V_{cc} See Load Circuit
	1	3.5	nS	$\geq 35 \text{ MHZ}$ and $< 70 \text{ MHZ}$	
	1.5	2.5	nS	$\geq 70 \text{ MHZ}$	
	4	10	nS	$< 35 \text{ MHZ}$	$C_{LOAD} = 30 \text{ pF}$ 20% to 80% of V_{cc} See Load Circuit
	2	7	nS	$\geq 35 \text{ MHZ}$ and $< 70 \text{ MHZ}$	
	3	7	nS	$\geq 70 \text{ MHZ}$	
V_{cc} Supply Current (I_{cc})	1	2	mA	$< 8 \text{ MHZ}$	$C_{LOAD} = 15 \text{ pF}$
	1.5	2.5	mA	$\geq 8 \text{ MHZ}$ and $< 16 \text{ MHZ}$	
	2	3	mA	$\geq 16 \text{ MHZ}$ and $< 35 \text{ MHZ}$	
	12	18	mA	$\geq 35 \text{ MHZ}$ and $< 70 \text{ MHZ}$	
	17	27	mA	$\geq 70 \text{ MHZ}$ and $< 100 \text{ MHZ}$	
	23	37	mA	$\geq 120 \text{ MHZ}$	
	1.5	2.5	mA	$< 8 \text{ MHZ}$	$C_{LOAD} = 30 \text{ pF}$
	2	3	mA	$\geq 8 \text{ MHZ}$ and $< 16 \text{ MHZ}$	
	3	4	mA	$\geq 16 \text{ MHZ}$ and $< 35 \text{ MHZ}$	
	10	20	mA	$\geq 35 \text{ MHZ}$ and $< 85 \text{ MHZ}$	

Specifications with Pad 1 E/D open circuit

Typical phase noise plot for 5 oscillators at different output frequencies.



Load Circuit and Test Waveform



All leads 0.2 inches (5mm) or less

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm)

Font is Courier New

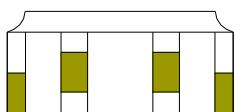
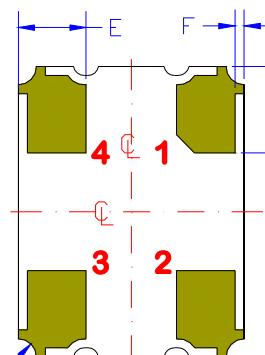
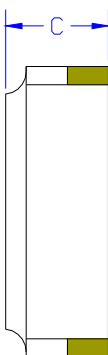
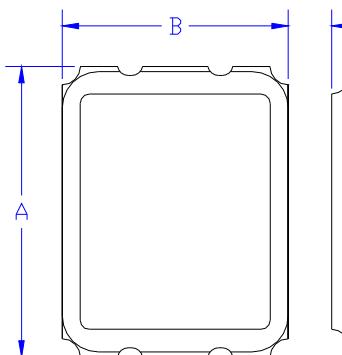
Bar code is 39-Full ASCII

Label is 1" x 2.6" (25.4mm x 66.7mm)

Font is Arial



Mechanical:



	Inches	mm
A	0.125 ± 0.006	3.20 ± 0.15
B	0.098 ± 0.006	2.50 ± 0.15
C	0.041 ± 0.004	1.05 ± 0.10
D ¹	0.008	0.20R
E ¹	0.030	0.75
F ¹	0.004	0.10
G ¹	0.037	0.95

Not to Scale

¹ Typical dimensions

Contacts :

Gold 11.8 to 39.4 μ inches (0.3 to 1.0 μ m) over Nickel 50 to 350 μ inches (1.27 to 8.89 μ m)

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is logic low the output will be inhibited (high impedance state.) Recommend connecting this pad to V_{CC} if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V_{CC})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

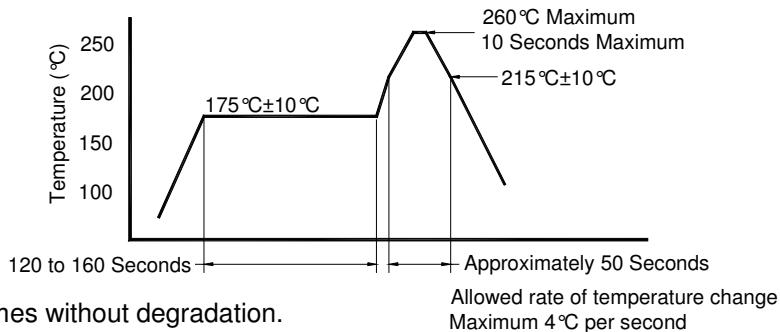


Layout and application information

For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.

Reflow Cycle (typical for lead free processing)



Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

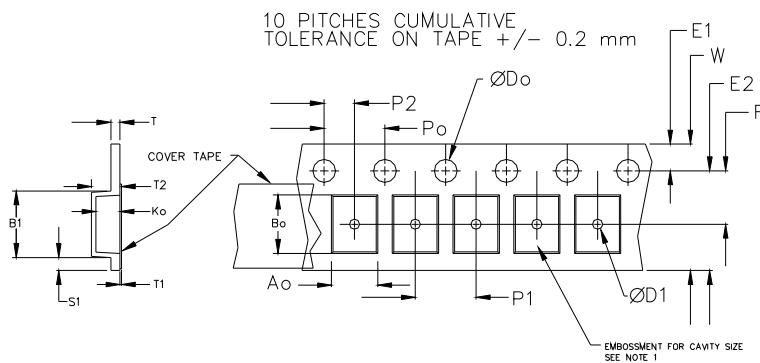
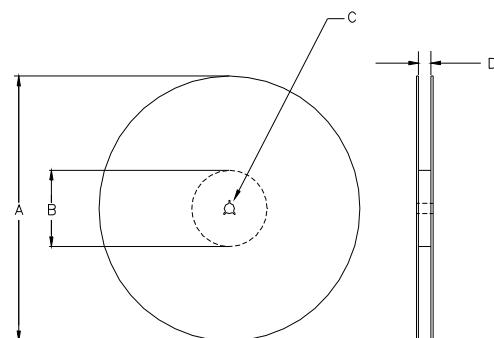
Constant Dimensions Table 1								
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max
8mm	1.5 +0.1 -0.0	1.0	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	0.6	0.6	0.1
12mm		1.5						
16mm		1.5						
24mm		1.5						

Variable Dimensions Table 2							
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko
16 mm	12.1	14.25	7.5 ±0.1	8.0 ±0.1	8.0	16.3	Note 1

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm

Not to scale



REEL DIMENSIONS				
A	inches	7.0	10.0	13.0
B	mm	177.8	254.0	330.2
C	mm	13.0 +0.5 / -0.2		
D	mm	16.4 +2.0 / -0.0	16.4 +2.0 / -0.0	16.4 +2.0 / -0.0

Reel dimensions may vary from the above

USER DIRECTION OF UNREELING →

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