

COMMON MODE CHOKE

For Fibre Channel, Gigabit Ethernet, and IEEE 1394 Applications



-  Common Mode Attenuation from 200 MHz to 2.5 GHz
-  Reduces radiated emissions and improves noise immunity
-  235°C Peak Reflow temperature rating
-  Surface mount, auto-insertable package
-  Industrial temperature range

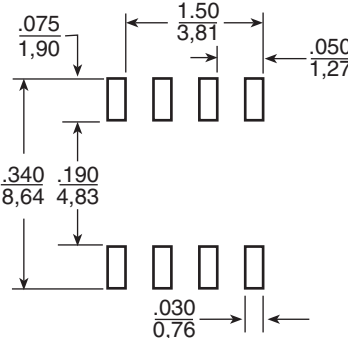
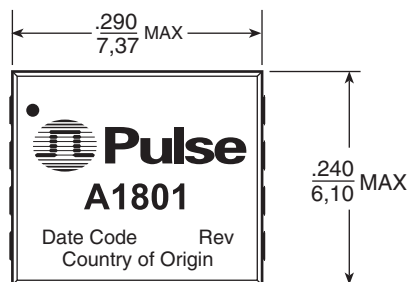
Electrical Specifications @ 25°C — Operating Temperature -40°C to +85°C

Part Number	Common Mode Attenuation (dB TYP)						Primary Inductance OCL (μH MIN)	DCR (Ω MAX)
	200 MHz	500MHz	1.0GHz	1.5GHz	2.0GHz	2.5GHz		
A1801	-11.0	-11.0	-11.0	-10.0	-9.0	-7.5	3.0	.15

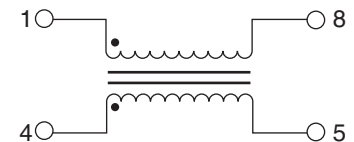
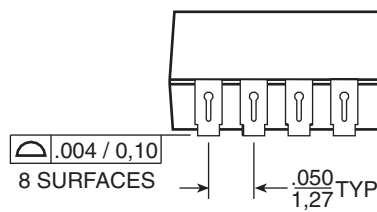
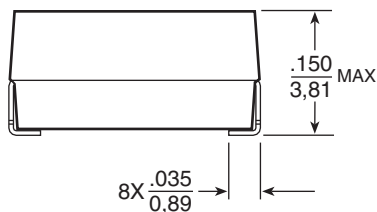
Mechanical

Schematic

A1801



SUGGESTED PAD LAYOUT



Dimensions: $\frac{\text{Inches}}{\text{mm}}$
Unless otherwise specified, all tolerances are $\pm \frac{.005}{0.13}$

Weight0.2 grams (max)
Tape & Reel1750/reel
Tube80/tube

1. Values for R1 and R2 are determined by system transmit amplitude requirements. A value of 0Ω for R1 and R2 will yield an output with maximum amplitude but distorted due to impedance mismatch. A value of 68Ω will provide an optimum waveform but have 6.0 dB insertion loss. If 6.0dB loss is too great, select at least 15Ω to obtain a reasonable compromise between insertion loss and waveshape. The typical value for C1, C2, C3, and C4 is $0.1\mu\text{F}$.
2. To maintain return loss and signal integrity, transmission line methods must be used when designing the PCB. For best performance the common mode choke should be located no more than 1" from the cable connector.
3. One example of a possible layout is shown below for a Fibre Channel application. Note that actual board layouts will vary based upon specific applications and EMC considerations.

The diagram illustrates the HSDCC or DB9 connector interface circuit. It features a SERDES CHIP SET connected to two A1801 comparators. The comparators are configured with resistors (R1, R2, R3, R4) and capacitors (C1, C2, C3, C4). The output of the comparators is connected to the TX+ and TX- pins of the HSDCC or DB9 connector. The RX- and RX+ pins of the connector are connected to the RX- and RX+ pins of the SERDES CHIP SET. The circuit is powered by Vcc and GND, with a 75 Ohm transmission line indicated.

The diagram shows a circuit for measuring the D.U.T. (Device Under Test). A dashed box labeled "D.U.T." contains a component with two terminals and two internal coils. This component is connected in series with a "75 Ω NETWORK ANALYZER". The circuit is a simple loop with two connection points on the left and right, indicated by circles with dots.

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