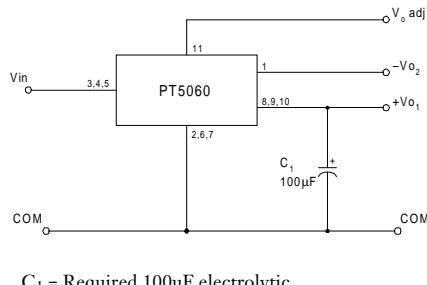


## Standard Application

C<sub>1</sub> = Required 100μF electrolytic

## Features

- Single Device: +5V Input
- Complimentary Dual Output: ±12V, ±15V
- Wide Input Voltage Range
- 85% Efficiency
- Adjustable Output Voltage
- Laser-trimmed

## Description

The PT5060 series of dual-output Integrated Switching Regulators (ISRs) provide a complimentary ±12V or ±15V from a single +5V input. Applications include systems that require power for analog interface circuitry, such as D/A and A/D converters, and Op Amps. The output voltage can be adjusted with an external resistor. These ISRs are made available in a 12-pin single in-line pin (SIP) package. Note that these modules are not short-circuit protected.

## Pin-Out Information

Pin	Function
1	-V <sub>o2</sub>
2	GND
3	V <sub>in</sub>
4	V <sub>in</sub>
5	V <sub>in</sub>
6	GND
7	GND
8	+V <sub>o1</sub>
9	+V <sub>o1</sub>
10	+V <sub>o1</sub>
11	V <sub>o</sub> Adj
12	Do Not Connect

## Ordering Information

PT5061□ = ±12 Volts  
PT5062□ = ±15 Volts

## PT Series Suffix (PT1234x)

Case/Pin Configuration	Order Suffix	Package Code *
Vertical	N	(ECD)
Horizontal	A	(ECA)
SMD	C	(ECC)
Vertical, Side Tabs	R	(ECE)
Horizontal, Side Tabs	G	(ECG)
SMD, Side Tabs	B	(ECK)

\* Previously known as package style 300.

(Reference the applicable package code drawing for the dimensions and PC board layout)

Specifications (Unless otherwise stated, T<sub>a</sub> = 25°C, V<sub>in</sub> = +5V, I<sub>o</sub> = I<sub>o</sub>max, C<sub>1</sub> = 100μF)

Characteristics	Symbol	Conditions	PT5060 SERIES				
			Min	Typ	Max	Units	
Output Current	I <sub>o</sub>	Over V <sub>in</sub> range	V <sub>o1</sub> = +12V V <sub>o2</sub> = -12V	0.05 0.05 (1)	— —	0.50 0.25	A
			V <sub>o1</sub> = +15V V <sub>o2</sub> = -15V	0.05 0.05 (1)	— —	0.40 0.20	A
Current Limit	I <sub>lim</sub>		—	150 (2)	—	%I <sub>o</sub> max	
Inrush Current	I <sub>ir</sub>	On start up	— —	5.5 (3) 2	— —	A mSec	
Input Voltage Range	V <sub>in</sub>	Over I <sub>o</sub> range	4.75	—	+V <sub>o</sub> -1	V	
Output Voltage Tolerance	ΔV <sub>o</sub>	Over V <sub>in</sub> and I <sub>o</sub> ranges T <sub>a</sub> = 0°C to SOA limit (3)	+V <sub>o1</sub> -V <sub>o2</sub>	— —	±1.5 ±5	±3.0 ±10	%V <sub>o</sub>
Line Regulation	R <sub>eg</sub> <sub>line</sub>	Over V <sub>in</sub> range	—	±0.5	±1.0	%V <sub>o</sub>	
Load Regulation	R <sub>eg</sub> <sub>load</sub>	0.1 ≤ I <sub>o</sub> ≤ I <sub>o</sub> max	—	±0.5	±1.0	%V <sub>o</sub>	
V <sub>o</sub> Ripple (pk-pk)	V <sub>n</sub>	20MHz bandwidth	+V <sub>o1</sub> -V <sub>o2</sub>	— —	±1.5 ±2	±3 ±3	%V <sub>o</sub>
Transient Response	t <sub>tr</sub> V <sub>os</sub>	25% load change V <sub>o</sub> over/undershoot	— —	100 3	— 5	μSec %V <sub>o</sub>	
Efficiency	η	I <sub>o</sub> =0.2A each output	—	85	—	%	
Switching Frequency	f <sub>s</sub>	Over V <sub>in</sub> and I <sub>o</sub> ranges	—	650	—	kHz	
Operating Temperature Range	T <sub>a</sub>	—	0	—	+85 (4)	°C	
Storage Temperature	T <sub>s</sub>	—	-40	—	+125	°C	
Mechanical Shock		Per Mil-STD-883D, Method 2002.3, 1 msec, Half Sine, mounted to a fixture	—	500	—	G's	
Mechanical Vibration		Per Mil-STD-883D, Method 2007.2 20-2000 Hz, Soldered in a PC board	—	15	—	G's	
Weight			—	6.5	—	grams	

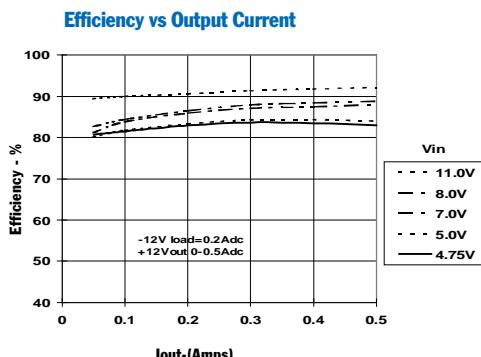
Notes: (1) Do not operate the negative output rail of these ISRs below the minimum load.

(2) ISRs based on a boost topology are not short-circuit protected.

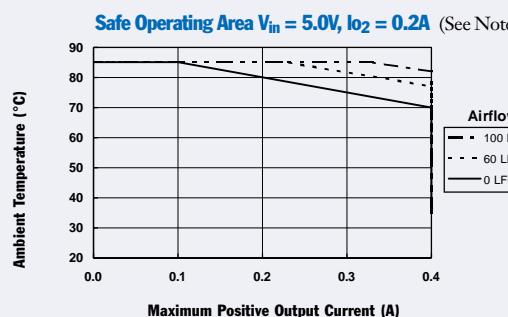
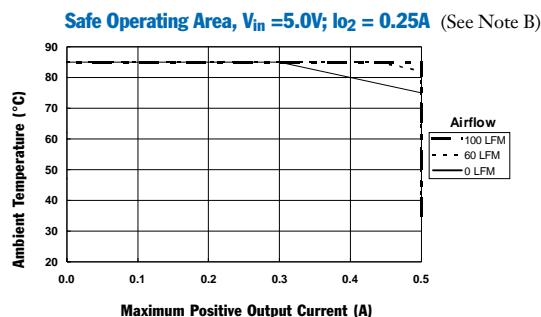
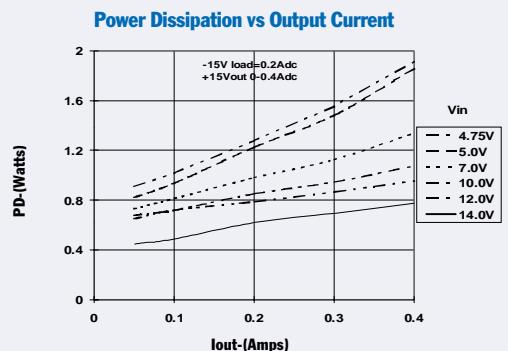
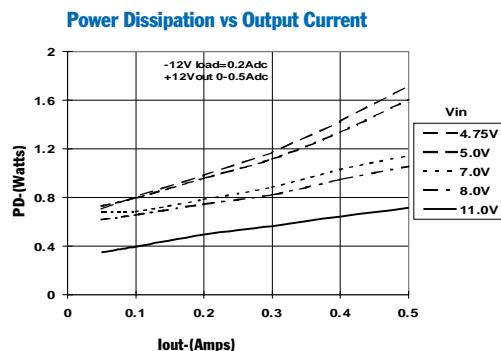
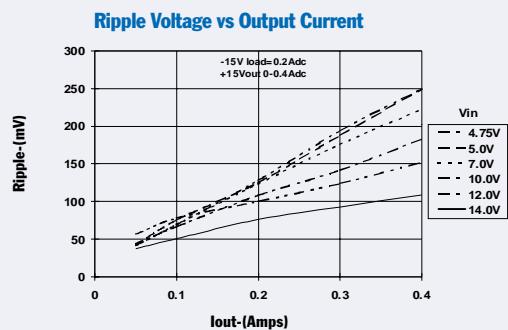
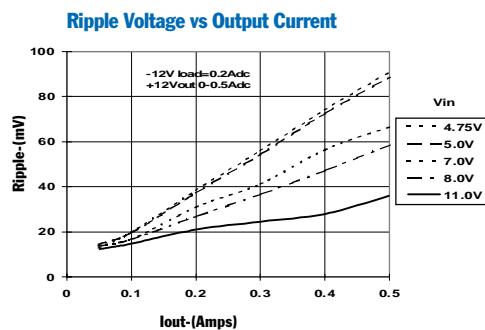
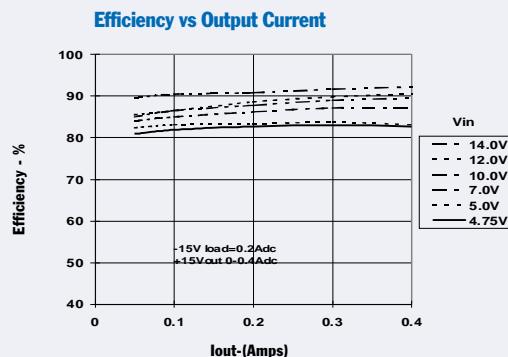
(3) The inrush current stated is above the normal input current for the associated output load.

(4) See Safe Operating Area curves or consult the factory for the appropriate derating.

## PT5061 +/- 12VDC (See Note A)



## PT5062 +/- 15V (See Note A)



**Note A:** Characteristic data has been developed from actual products tested at 25°C. This data is considered typical data for the Converter.  
**Note B:** Thermal derating graphs are developed in free-air convection cooling, which corresponds to approximately 40–60LFM of airflow.

### Adjusting the Output Voltage of the PT5060 Dual-Output Boost Converter Series

The dual output voltage of the PT5060 series modules can be adjusted higher or lower than the factory pre-set voltage with the addition of a single external resistor. Table 1 gives the applicable adjustment range for each model in the series as  $V_a$  (min) and  $V_a$  (max).

**Adjust Up:** An increase in the output voltage is obtained by adding a resistor  $R_2$ , between pin 11 ( $V_o$  adj) and pins 2, 6, or 7 (GND).

**Adjust Down:** Add a resistor ( $R_1$ ), between pin 11 ( $V_o$  adj) and pins 8, 9 or 10 ( $V_{o1}$ ).

Refer to Figure 1 and Table 2 for both the placement and value of the required resistor, either ( $R_1$ ) or  $R_2$  as appropriate.

#### Notes:

1. Both the positive and negative voltage outputs from the ISR are adjusted simultaneously.
2. Use only a single 1% resistor in either the ( $R_1$ ) or  $R_2$  location. Place the resistor as close to the ISR as possible.
3. Never connect capacitors from  $V_o$  adj to either GND or  $V_o$ . Any capacitance added to the  $V_o$  adjust pin will affect the stability of the ISR.
4. An increase in the output voltage must be accompanied by a corresponding reduction in the specified maximum current at each output. For  $V_{o1}$  and  $-V_{o2}$ , the revised maximum output current must be reduced to the equivalent of 6 watts and 3 watts respectively. i.e.

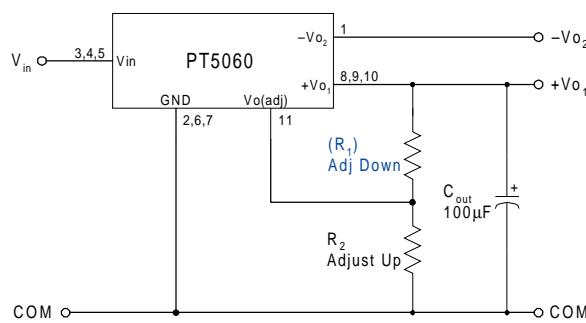
$$I_{o1} \text{ (max)} = \frac{6}{V_a} \text{ Adc}$$

and  $I_{o2} \text{ (max)} = \frac{3}{V_a} \text{ Adc,}$

where  $V_a$  is the adjusted output voltage.

5. Adjustments to the output voltage will also limit the maximum input voltage that can be applied to the ISR. The maximum input voltage that may be applied is limited to  $(V_o - 1)Vdc$  or 14Vdc, whichever is less.

**Figure 1**



The values of ( $R_1$ ) [adjust down], and  $R_2$  [adjust up], can also be calculated using the following formulas.

$$(R_1) = \frac{3.65 (V_a - 2.5)}{(V_o - V_a)} - 0.1 \text{ k}\Omega$$

$$R_2 = \frac{9.125}{V_a - V_o} - 0.1 \text{ k}\Omega$$

Where:  $V_o$  = Original output voltage  
 $V_a$  = Adjusted output voltage

**Table 1**

**PT5060 ADJUSTMENT AND FORMULA PARAMETERS**

Series Pt #	PT5061	PT5062
$V_o$ (nom)	$\pm 12.0V$	$\pm 15.0V$
$V_a$ (min)	$\pm 7.5V$	$\pm 7.5V$
$V_a$ (max)	$\pm 14.0V$	$\pm 20.0V$

**Table 2**

**PT5060 ADJUSTMENT RESISTOR VALUES**

Series Pt #	PT5061	PT5062
$V_o$ (nom)	$\pm 12.0Vdc$	$\pm 15.0Vdc$
$V_a$ (req'd)		
7.0		
7.5	(4.0)k $\Omega$	(2.3)k $\Omega$
8.0	(4.9)k $\Omega$	(2.8)k $\Omega$
8.5	(6.2)k $\Omega$	(3.3)k $\Omega$
9.0	(7.8)k $\Omega$	(3.9)k $\Omega$
9.5	(10.1)k $\Omega$	(4.6)k $\Omega$
10.0	(13.6)k $\Omega$	(5.4)k $\Omega$
10.5	(19.4)k $\Omega$	(6.4)k $\Omega$
11.0	(30.9)k $\Omega$	(7.7)k $\Omega$
11.5	(65.6)k $\Omega$	(9.3)k $\Omega$
12.0		(11.5)k $\Omega$
12.5	18.2k $\Omega$	(14.5)k $\Omega$
13.0	9.0k $\Omega$	(19.1)k $\Omega$
13.5	6.0k $\Omega$	(26.7)k $\Omega$
14.0	4.5k $\Omega$	(41.9)k $\Omega$
14.5		(87.5)k $\Omega$
15.0		
15.5		18.2k $\Omega$
16.0		9.0k $\Omega$
16.5		6.0k $\Omega$
17.0		4.5k $\Omega$
17.5		3.6k $\Omega$
18.0		2.9k $\Omega$
18.5		2.5k $\Omega$
19.0		2.2k $\Omega$
19.5		1.9k $\Omega$
20.0		1.7k $\Omega$

$R_1$  = (Blue)  $R_2$  = Black

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