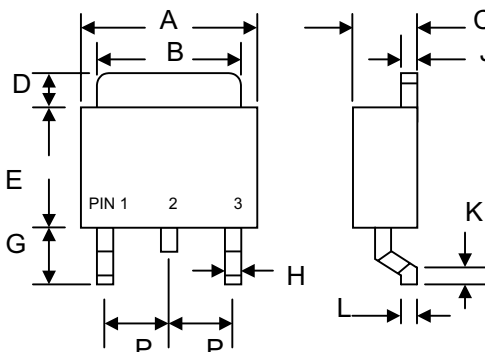


**Date Sheet 2840, Rev.-**

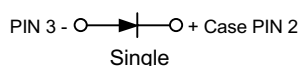
## Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications



## Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band
- Weight: 0.4 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- Standard Packaging: 16mm Tape (EIA-481)



D-PAK/TO-252AA				
Dim	Min	Max	Min	Max
A	6.40	6.80	0.252	0.268
B	5.00	5.40	0.197	0.213
C	2.35	2.75	0.093	0.108
D	—	1.60	—	0.063
E	5.30	5.70	0.209	0.224
G	2.30	2.70	0.091	0.106
H	0.40	0.80	0.016	0.031
J	0.40	0.60	0.016	0.024
K	0.30	0.70	0.012	0.028
L	0.50 Typical		0.020 Typical	
P	—	2.30	—	0.091
	In mm		In inch	

### Maximum Ratings and Electrical Characteristics @T<sub>A</sub>=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

[illegible]

Note: 1. Mounted on P.C. Board with 14mm<sup>2</sup> (0.13mm thick) copper pad.  
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

Data Sheet 2840, Rev. -

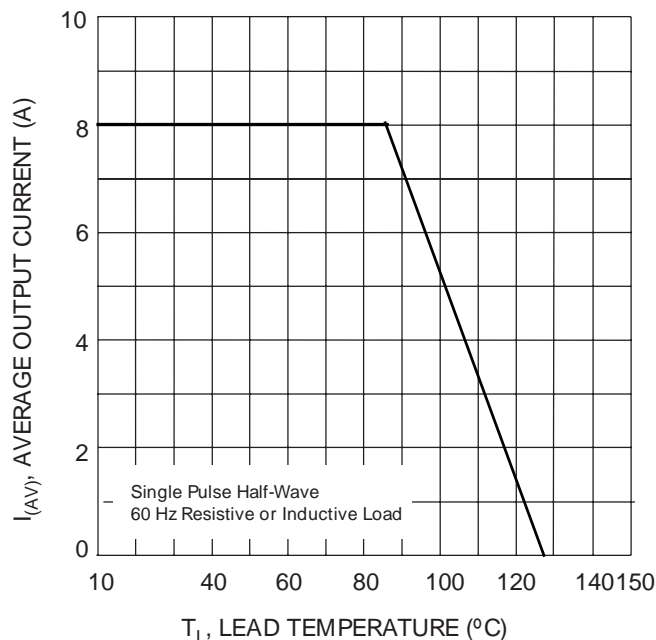


Fig. 1 Forward Current Derating Curve

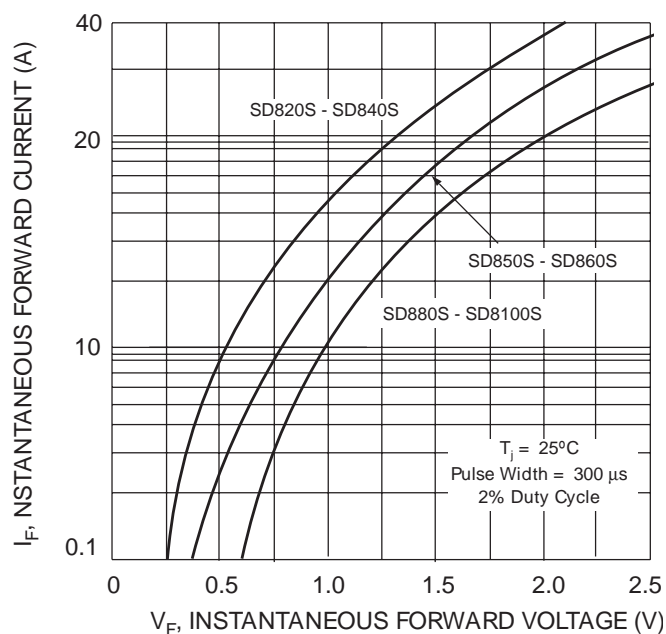


Fig. 2 Typical Forward Characteristics

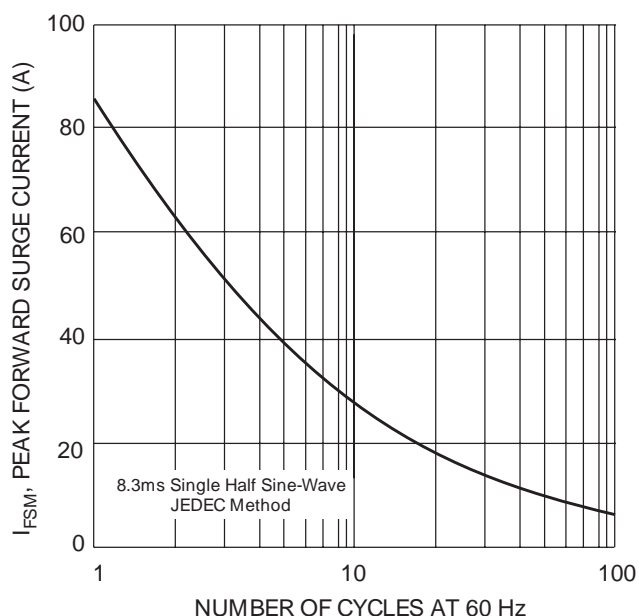


Fig. 3 Maximum Non-Repetitive Peak Fwd Surge Current

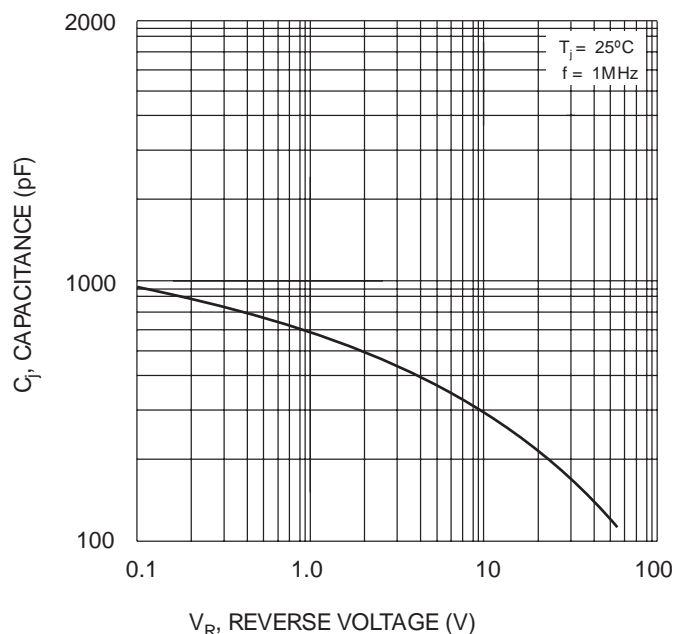


Fig. 4 Typical Junction Capacitance