

TLC227x, TLC227xA Advanced LinCMOS™ RAIL-TO-RAIL OPERATIONAL AMPLIFIERS

SLOS190F – FEBRUARY 1997 – REVISED MAY 2001

- Output Swing Includes Both Supply Rails
- Low Noise . . . 9 nV/√Hz Typ at f = 1 kHz
- Low Input Bias Current . . . 1 pA Typ
- Fully Specified for Both Single-Supply and Split-Supply Operation
- Common-Mode Input Voltage Range Includes Negative Rail
- High-Gain Bandwidth . . . 2.2 MHz Typ
- High Slew Rate . . . 3.6 V/μs Typ
- Low Input Offset Voltage
950 μV Max at T_A = 25°C
- Macromodel Included
- Performance Upgrades for the TS272, TS274, TLC272, and TLC274
- Available in Q-Temp Automotive HighRel Automotive Applications Configuration Control / Print Support Qualification to Automotive Standards

description

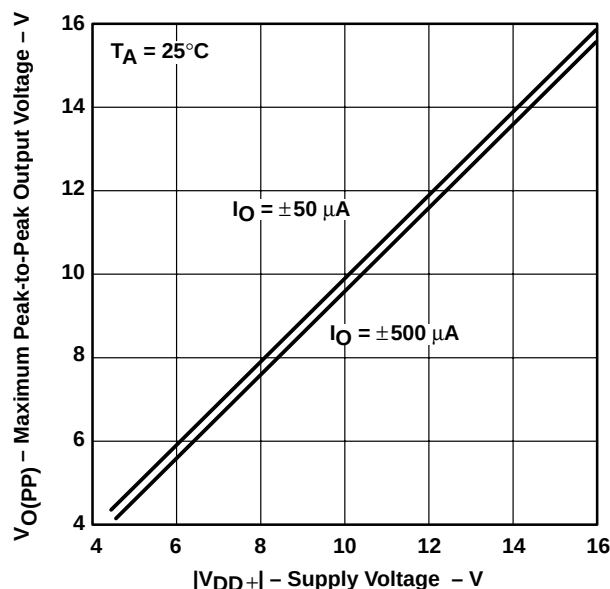
The TLC2272 and TLC2274 are dual and quadruple operational amplifiers from Texas Instruments. Both devices exhibit rail-to-rail output performance for increased dynamic range in single- or split-supply applications. The TLC227x family offers 2 MHz of bandwidth and 3 V/μs of slew rate for higher speed applications. These devices offer comparable ac performance while having better noise, input offset voltage, and power dissipation than existing CMOS operational amplifiers. The TLC227x has a noise voltage of 9 nV/√Hz, two times lower than competitive solutions.

The TLC227x, exhibiting high input impedance and low noise, is excellent for small-signal conditioning for high-impedance sources, such as piezoelectric transducers. Because of the micro-power dissipation levels, these devices work well in hand-held monitoring and remote-sensing applications. In addition, the rail-to-rail output feature, with single- or split-supplies, makes this family a great choice when interfacing with analog-to-digital converters (ADCs). For precision applications, the TLC227xA family is available with a maximum input offset voltage of 950 μV. This family is fully characterized at 5 V and ±5 V.

The TLC2272/4 also makes great upgrades to the TLC272/4 or TS272/4 in standard designs. They offer increased output dynamic range, lower noise voltage, and lower input offset voltage. This enhanced feature set allows them to be used in a wider range of applications. For applications that require higher output drive and wider input voltage range, see the TLV2432 and TLV2442 devices.

If the design requires single amplifiers, please see the TLV2211/21/31 family. These devices are single rail-to-rail operational amplifiers in the SOT-23 package. Their small size and low power consumption, make them ideal for high density, battery-powered equipment.

MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE
vs
SUPPLY VOLTAGE



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**TEXAS
INSTRUMENTS**

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On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

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TLC2272 AVAILABLE OPTIONS

T _A	V _{IO} max At 25°C	PACKAGED DEVICES		
		SMALL OUTLINE† (D)	PLASTIC DIP (P)	TSSOP‡ (PW)
0°C to 70°C	950 µV 2.5 mV	TLC2272ACD TLC2272CD	TLC2272ACP TLC2272CP	TLC2272ACPW TLC2272CPW
–40°C to 125°C	950 µV 2.5 mV	TLC2272AID TLC2272ID	TLC2272AIP TLC2272IP	— TLC2272IPW
	950 µV 2.5 mV	TLC2272AQD TLC2272QD	—	TLC2272AQPW TLC2272QPW
–55°C to 125°C	950 µV 2.5 mV	TLC2272AMD TLC2272MD	TLC2272AMP TLC2272MP	—

† The D packages are available taped and reeled. Add R suffix to the device type (e.g., TLC2272CDR).

‡ The PW package is available taped and reeled. Add R suffix to the device type (e.g., TLC2272PWR).

§ Chips are tested at 25°C.

TLC2274 AVAILABLE OPTIONS

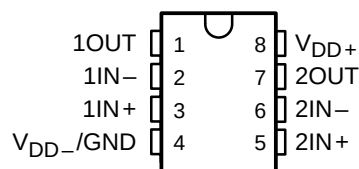
T _A	V _{IO} max AT 25°C	PACKAGED DEVICES				
		SMALL OUTLINE† (D)	CHIP CARRIER (FK)	CERAMIC DIP (J)	PLASTIC DIP (N)	TSSOP‡ (PW)
0°C to 70°C	950 µV 2.5 mV	TLC2274ACD TLC2274CD	—	—	TLC2274ACN TLC2274CN	TLC2274ACPW TLC2274CPW
–40°C to 125°C	950 µV 2.5 mV	TLC2274AID TLC2274ID	—	—	TLC2274AIN TLC2274IN	TLC2274AIPW TLC2274IPW
	950 µV 2.5 mV	TLC2274AQD TLC2274QD	—	—	—	—
–55°C to 125°C	950 µV 2.5 mV	TLC2274AMD TLC2274MD	TLC2274AMFK TLC2274MFK	TLC2274AMJ TLC2274MJ	TLC2274AMN TLC2274MN	—

† The D packages are available taped and reeled. Add R suffix to device type (e.g., TLC2274CDR).

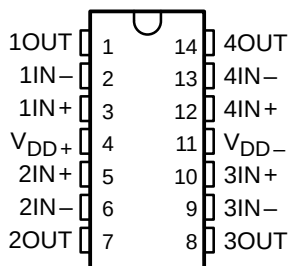
‡ The PW package is available taped and reeled.

§ Chips are tested at 25°C.

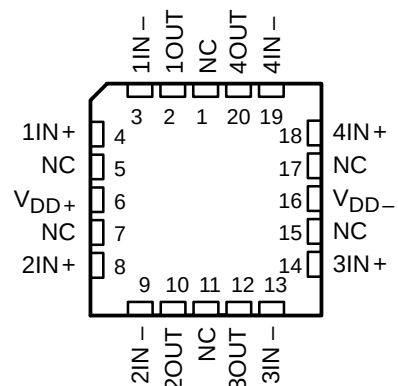
TLC2272
D, P, OR PW PACKAGE
(TOP VIEW)



TLC2274
D, J, N, OR PW PACKAGE
(TOP VIEW)

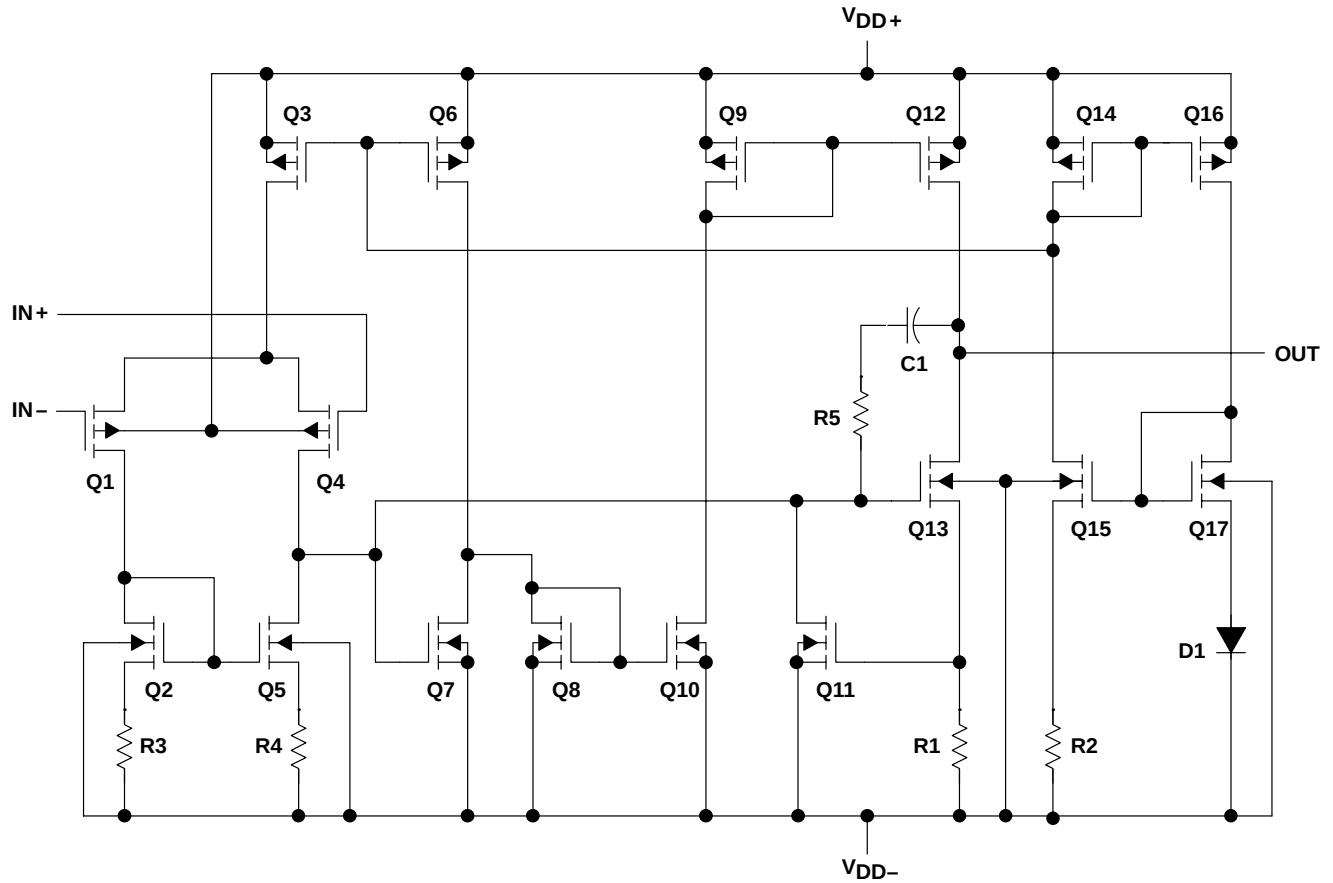


TLC2274
FK PACKAGE
(TOP VIEW)



NC – No internal connection

equivalent schematic (each amplifier)



ACTUAL DEVICE COMPONENT COUNT†		
COMPONENT	TLC2272	TLC2274
Transistors	38	76
Resistors	26	52
Diodes	9	18
Capacitors	3	6

† Includes both amplifiers and all ESD, bias, and trim circuitry

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V_{DD+} (see Note 1)	8 V
Supply voltage, V_{DD-} (see Note 1)	–8 V
Differential input voltage, V_{ID} (see Note 2)	±16 V
Input voltage range, V_I (any input, see Note 1)	$V_{DD-} - 0.3 \text{ V}$ to V_{DD+}
Input current, I_I (any input)	±5 mA
Output current, I_O	±50 mA
Total current into V_{DD+}	±50 mA
Total current out of V_{DD-}	±50 mA
Duration of short-circuit current at (or below) 25°C (see Note 3)	unlimited
Continuous total dissipation	See Dissipation Rating Table
Operating free-air temperature range, T_A : C suffix	0°C to 70°C
I, Q suffix	–40°C to 125°C
M suffix	–55°C to 125°C
Storage temperature range	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D, N, P or PW package	260°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: J package	300°C

[†] Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltages, are with respect to the midpoint between V_{DD+} and V_{DD-} .
2. Differential voltages are at $IN+$ with respect to $IN-$. Excessive current will flow if input is brought below $V_{DD-} - 0.3 \text{ V}$.
3. The output may be shorted to either supply. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.

DISSIPATION RATING TABLE

PACKAGE	$T_A \leq 25^\circ\text{C}$ POWER RATING	DERATING FACTOR ABOVE $T_A = 25^\circ\text{C}$	$T_A = 70^\circ\text{C}$ POWER RATING	$T_A = 85^\circ\text{C}$ POWER RATING	$T_A = 125^\circ\text{C}$ POWER RATING
D-8	725 mW	5.8 mW/°C	464 mW	337 mW	145 mW
D-14	950 mW	7.6 mW/°C	608 mW	494 mW	190 mW
FK	1375 mW	11.0 mW/°C	880 mW	715 mW	275 mW
J	1375 mW	11.0 mW/°C	880 mW	715 mW	275 mW
N	1150 mW	9.2 mW/°C	736 mW	598 mW	230 mW
P	1000 mW	8.0 mW/°C	640 mW	520 mW	200 mW
PW-8	525 mW	4.2 mW/°C	336 mW	273 mW	105 mW
PW-14	700 mW	5.6 mW/°C	448 mW	364 mW	—

recommended operating conditions

	C SUFFIX		I SUFFIX		Q SUFFIX		M SUFFIX		UNIT
	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
Supply voltage, $V_{DD\pm}$	±2.2	±8	±2.2	±8	±2.2	±8	±2.2	±8	V
Input voltage, V_I	V_{DD-}	$V_{DD+} - 1.5$	V_{DD-}	$V_{DD+} - 1.5$	V_{DD-}	$V_{DD+} - 1.5$	V_{DD-}	$V_{DD+} - 1.5$	V
Common-mode input voltage, V_{IC}	V_{DD-}	$V_{DD+} - 1.5$	V_{DD-}	$V_{DD+} - 1.5$	V_{DD-}	$V_{DD+} - 1.5$	V_{DD-}	$V_{DD+} - 1.5$	V
Operating free-air temperature, T_A	0	70	–40	125	–40	125	–55	125	°C



TLC2272C electrical characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2272C			TLC2272AC			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{IO} Input offset voltage	$V_{IC} = 0\text{ V}$, $V_{DD} \pm \pm 2.5\text{ V}$, $V_O = 0\text{ V}$, $R_S = 50\ \Omega$	25°C		300	2500		300	950	μV
		Full range			3000			1500	
α_{VIO} Temperature coefficient of input offset voltage		25°C to 70°C		2			2		$\mu\text{V}/^\circ\text{C}$
Input offset voltage long-term drift (see Note 4)		25°C		0.002			0.002		$\mu\text{V}/\text{mo}$
I_{IO} Input offset current		25°C		0.5	60		0.5	60	pA
		Full range			100			100	
I_{IB} Input bias current		25°C		1	60		1	60	pA
		Full range			100			100	
V_{ICR} Common-mode input voltage	$R_S = 50\ \Omega$, $ V_{IO} \leq 5\text{ mV}$	25°C	0 to 4	–0.3 to 4.2		0 to 4	–0.3 to 4.2		V
		Full range	0 to	3.5		0 to	3.5		
V_{OH} High-level output voltage	$I_{OH} = -20\ \mu\text{A}$	25°C		4.99			4.99		V
	$I_{OH} = -200\ \mu\text{A}$	25°C	4.85	4.93		4.85	4.93		
		Full range	4.85			4.85			
	$I_{OH} = -1\text{ mA}$	25°C	4.25	4.65		4.25	4.65		
		Full range	4.25			4.25			
V_{OL} Low-level output voltage	$V_{IC} = 2.5\text{ V}$, $I_{OL} = 50\ \mu\text{A}$	25°C		0.01			0.01		V
	$V_{IC} = 2.5\text{ V}$, $I_{OL} = 500\ \mu\text{A}$	25°C		0.09	0.15		0.09	0.15	
		Full range			0.15			0.15	
	$V_{IC} = 2.5\text{ V}$, $I_{OL} = 5\text{ mA}$	25°C		0.9	1.5		0.9	1.5	
		Full range			1.5			1.5	
A_{VD} Large-signal differential voltage amplification	$V_{IC} = 2.5\text{ V}$, $V_O = 1\text{ V to }4\text{ V}$	$R_L = 10\text{ k}\Omega^\ddagger$	25°C	15	35		15	35	V/mV
			Full range	15			15		
		$R_L = 1\text{ m}\Omega^\ddagger$	25°C		175			175	
r_{id} Differential input resistance		25°C		10^{12}			10^{12}		Ω
r_i Common-mode input resistance		25°C		10^{12}			10^{12}		Ω
c_i Common-mode input capacitance	$f = 10\text{ kHz}$, P package	25°C		8			8		pF
z_o Closed-loop output impedance	$f = 1\text{ MHz}$, $A_V = 10$	25°C		140			140		Ω
CMRR Common-mode rejection ratio	$V_{IC} = 0\text{ V to }2.7\text{ V}$, $V_O = 2.5\text{ V}$, $R_S = 50\ \Omega$	25°C	70	75		70	75		dB
		Full range	70			70			
k_{SVR} Supply-voltage rejection ratio ($\Delta V_{DD}/\Delta V_{IO}$)	$V_{DD} = 4.4\text{ V to }16\text{ V}$, $V_{IC} = V_{DD}/2$, No load	25°C	80	95		80	95		dB
		Full range	80			80			
I_{DD} Supply current	$V_O = 2.5\text{ V}$, No load	25°C		2.2	3		2.2	3	mA
		Full range			3			3	

† Full range is 0°C to 70°C.

‡ Referenced to 0 V

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2272C operating characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$

PARAMETER	TEST CONDITIONS	T_A †	TLC2272C			TLC2272AC			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain $V_O = 0.5\text{ V to }2.5\text{ V}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C	2.3	3.6		2.3	3.6		V/ μs
		Full range	1.7			1.7			
V_n	Equivalent input noise voltage	$f = 10\text{ Hz}$		50			50		nV/ $\sqrt{\text{Hz}}$
		$f = 1\text{ kHz}$		9			9		
V_{NPP}	Peak-to-peak equivalent input noise voltage	$f = 0.1\text{ Hz to }1\text{ Hz}$		1			1		μV
		$f = 0.1\text{ Hz to }10\text{ Hz}$		1.4			1.4		
I_n	Equivalent input noise current	25°C		0.6			0.6		fA/ $\sqrt{\text{Hz}}$
THD + N	Total harmonic distortion plus noise $V_O = 0.5\text{ V to }2.5\text{ V}$, $f = 20\text{ kHz}$, $R_L = 10\text{ k}\Omega^\ddagger$	$A_V = 1$		0.0013%			0.0013%		
		$A_V = 10$		0.004%			0.004%		
		$A_V = 100$		0.03%			0.03%		
	Gain-bandwidth product $f = 10\text{ kHz}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C		2.18			2.18		MHz
BOM	Maximum output-swing bandwidth $V_{O(PP)} = 2\text{ V}$, $A_V = 1$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C		1			1		MHz
t_s	Settling time $A_V = -1$, Step = $0.5\text{ V to }2.5\text{ V}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	To 0.1%		1.5			1.5		μs
		To 0.01%		2.6			2.6		
ϕ_m	Phase margin at unity gain $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C		50°			50°		
	Gain margin	25°C		10			10		dB

† Full range is 0°C to 70°C.

‡ Referenced to 0 V

TLC2272C electrical characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$ (unless otherwise specified)

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2272C			TLC2272AC			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
V _{IO}	Input offset voltage	V _{IC} = 0 V, V _O = 0 V, R _S = 50 Ω		25°C	300	2500		300	950	μV	
	Full range				3000		1500				
α _{VIO}	Temperature coefficient of input offset voltage			25°C to 70°C	2			2		μV/°C	
	Input offset voltage long-term drift (see Note 4)			25°C	0.002			0.002		μV/mo	
I _{IO}	Input offset current			25°C	0.5	60		0.5	60	pA	
				Full range		100		100			
I _{IB}	Input bias current			25°C	1	60		1	60	pA	
				Full range		100		100			
V _{ICR}	Common-mode input voltage	R _S = 50 Ω, V _{IO} ≤ 5 mV	25°C	-5 to 4	-5.3 to 4.2		-5 to 4	-5.3 to 4.2	V		
			Full range	-5 to 3.5			-5 to 3.5				
V _{OM+}	Maximum positive peak output voltage	I _O = -20 μA	25°C	4.99			4.99	V			
		I _O = -200 μA	25°C	4.85	4.93		4.85		4.93		
			Full range	4.85			4.85				
		I _O = -1 mA	25°C	4.25	4.65		4.25		4.65		
			Full range	4.25			4.25				
V _{OM-}	Maximum negative peak output voltage	V _{IC} = 0 V, I _O = 50 μA	25°C	-4.99			-4.99	V			
		V _{IC} = 0 V, I _O = 500 μA	25°C	-4.85	-4.91		-4.85		-4.91		
			Full range	-4.85			-4.85				
		V _{IC} = 0 V, I _O = 5 mA	25°C	-3.5	-4.1		-3.5		-4.1		
			Full range	-3.5			-3.5				
A _{VD}	Large-signal differential voltage amplification	V _O = ±4 V	R _L = 10 kΩ	25°C	25	50		25	50	V/mV	
				Full range	25			25			
			R _L = 1 mΩ	25°C	300			300			
r _{id}	Differential input resistance			25°C	10 ¹²			10 ¹²	Ω		
r _i	Common-mode input resistance			25°C	10 ¹²			10 ¹²	Ω		
c _i	Common-mode input capacitance	f = 10 kHz,	P package	25°C	8			8	pF		
z _o	Closed-loop output impedance	f = 1 MHz,	A _V = 10	25°C	130			130	Ω		
CMRR	Common-mode rejection ratio	V _{IC} = -5 V to 2.7 V, V _O = 0 V, R _S = 50 Ω		25°C	75	80		75	80	dB	
				Full range	75			75			
k _{SVR}	Supply-voltage rejection ratio (ΔV _{DD±} /ΔV _{IO})	V _{DD±} = 2.2 V to ±8 V, V _{IC} = 0 V, No load		25°C	80	95		80	95	dB	
				Full range	80			80			
I _{DD}	Supply current	V _O = 0 V No load		25°C	2.4	3		2.4	3	mA	
				Full range		3			3		

† Full range is 0°C to 70°C.

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2272C operating characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2272C			TLC2272AC			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain	V _O = ±2.3 V, R _L = 10 kΩ, C _L = 100 pF		25°C	2.3	3.6		2.3	3.6		V/μs
				Full range	1.7			1.7			
V _n	Equivalent input noise voltage	f = 10 Hz		25°C	50			50			nV/√Hz
		f = 1 kHz		25°C	9			9			
V _{NPP}	Peak-to-peak equivalent input noise voltage	f = 0.1 Hz to 1 Hz		25°C	1			1			μV
		f = 0.1 Hz to 10 Hz		25°C	1.4			1.4			
I _n	Equivalent input noise current			25°C	0.6			0.6			fA/√Hz
THD + N	Total harmonic distortion pulse duration	V _O = ±2.3 V, f = 20 kHz, R _L = 10 kΩ	A _V = 1	25°C	0.0011%			0.0011%			
			A _V = 10		0.004%			0.004%			
			A _V = 100		0.03%			0.03%			
	Gain-bandwidth product	f = 10 kHz, C _L = 100 pF	R _L = 10 kΩ,	25°C	2.25			2.25			MHz
B _{OM}	Maximum output-swing bandwidth	V _{O(PP)} = 4.6 V, R _L = 10 kΩ,	A _V = 1, C _L = 100 pF	25°C	0.54			0.54			MHz
t _s	Settling time	A _V = −1, Step = −2.3 V to 2.3 V, R _L = 10 kΩ, C _L = 100 pF	To 0.1%	25°C	1.5			1.5			μs
			To 0.01%		3.2			3.2			
ϕ _m	Phase margin at unity gain	R _L = 10 kΩ, C _L = 100 pF		25°C	52°			52°			
	Gain margin			25°C	10			10			dB

† Full range is 0°C to 70°C.

TLC2274C electrical characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2274C			TLC2274AC			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{IO} Input offset voltage	$V_{DD\pm} = \pm 2.5\text{ V}$, $V_{IC} = 0\text{ V}$, $V_O = 0\text{ V}$, $R_S = 50\ \Omega$	25°C	300	2500		300	950		μV
		Full range		3000			1500		
α_{VIO} Temperature coefficient of input offset voltage		25°C to 70°C	2			2			$\mu\text{V}/^\circ\text{C}$
Input offset voltage long-term drift (see Note 4)		25°C	0.002			0.002			$\mu\text{V}/\text{mo}$
I_{IO} Input offset current		25°C	0.5	60		0.5	60		pA
		Full range		100			100		
I_{IB} Input bias current		25°C	1	60		1	60		pA
		Full range		100			100		
V_{ICR} Common-mode input voltage	$R_S = 50\ \Omega$, $ V_{IO} \leq 5\text{ mV}$	25°C	0 to 4	–0.3 to 4.2		0 to 4	–0.3 to 4.2		V
		Full range	0 to 3.5			0 to 3.5			
V_{OH} High-level output voltage	$I_{OH} = -20\ \mu\text{A}$	25°C		4.99			4.99		V
	$I_{OH} = -200\ \mu\text{A}$	25°C	4.85	4.93		4.85	4.93		
		Full range	4.85			4.85			
	$I_{OH} = -1\text{ mA}$	25°C	4.25	4.65		4.25	4.65		
V_{OL} Low-level output voltage	$V_{IC} = 2.5\text{ V}$, $I_{OL} = 50\ \mu\text{A}$	25°C		0.01			0.01		V
	$V_{IC} = 2.5\text{ V}$, $I_{OL} = 500\ \mu\text{A}$	25°C	0.09	0.15		0.09	0.15		
		Full range		0.15			0.15		
	$V_{IC} = 2.5\text{ V}$, $I_{OL} = 5\text{ mA}$	25°C	0.9	1.5		0.9	1.5		
		Full range		1.5			1.5		
A_{VD} Large-signal differential voltage amplification	$V_{IC} = 2.5\text{ V}$, $V_O = 1\text{ V to }4\text{ V}$	$R_L = 10\text{ k}\Omega^\ddagger$	25°C	15	35		15	35	V/mV
			Full range	15			15		
		$R_L = 1\text{ m}\Omega^\ddagger$	25°C	175			175		
r_{id} Differential input resistance		25°C		10^{12}			10^{12}		Ω
r_i Common-mode input resistance		25°C		10^{12}			10^{12}		Ω
c_i Common-mode input capacitance	$f = 10\text{ kHz}$, N package	25°C		8			8		pF
z_o Closed-loop output impedance	$f = 1\text{ MHz}$, $A_V = 10$	25°C		140			140		Ω
CMRR Common-mode rejection ratio	$V_{IC} = 0\text{ V to }2.7\text{ V}$, $V_O = 2.5\text{ V}$, $R_S = 50\ \Omega$	25°C	70	75		70	75		dB
		Full range	70			70			
k_{SVR} Supply-voltage rejection ratio ($\Delta V_{DD}/\Delta V_{IO}$)	$V_{DD} = 4.4\text{ V to }16\text{ V}$, $V_{IC} = V_{DD}/2$, No load	25°C	80	95		80	95		dB
		Full range	80			80			
I_{DD} Supply current	$V_O = 2.5\text{ V}$, No load	25°C		4.4	6		4.4	6	mA
		Full range			6			6	

† Full range is 0°C to 70°C.

‡ Referenced to 0 V

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2274C operating characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$

PARAMETER		TEST CONDITIONS		T _A †	TLC2274C			TLC2274AC			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain	V _O = 0.5 V to 2.5 V, R _L = 10 kΩ [‡] , C _L = 100 pF [‡]		25°C	2.3	3.6		2.3	3.6		V/μs
				Full range	1.7			1.7			
V _n	Equivalent input noise voltage	f = 10 Hz		25°C	50			50			nV/√Hz
		f = 1 kHz		25°C	9			9			
V _{N(PP)}	Peak-to-peak equivalent input noise voltage	f = 0.1 Hz to 1 Hz		25°C	1			1			μV
		f = 0.1 Hz to 10 Hz		25°C	1.4			1.4			
I _n	Equivalent input noise current			25°C	0.6			0.6			fA/√Hz
THD + N	Total harmonic distortion plus noise	V _O = 0.5 V to 2.5 V, f = 20 kHz, R _L = 10 kΩ [‡]	A _V = 1	25°C	0.0013%			0.0013%			
			A _V = 10		0.004%			0.004%			
			A _V = 100		0.03%			0.03%			
	Gain-bandwidth product	f = 10 kHz, C _L = 100 pF [‡] , R _L = 10 kΩ [‡]		25°C	2.18			2.18			MHz
B _{OM}	Maximum output-swing bandwidth	V _{O(PP)} = 2 V, R _L = 10 kΩ [‡] , A _V = 1, C _L = 100 pF [‡]		25°C	1			1			MHz
t _s	Settling time	A _V = −1, Step = 0.5 V to 2.5 V, R _L = 10 kΩ [‡] , C _L = 100 pF [‡]	To 0.1%	25°C	1.5			1.5			μs
			To 0.01%		2.6			2.6			
ϕ _m	Phase margin at unity gain	R _L = 10 kΩ [‡] , C _L = 100 pF [‡]		25°C	50°			50°			
	Gain margin			25°C	10			10			dB

† Full range is 0°C to 70°C.

‡ Referenced to 0 V

TLC2274C electrical characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5$ V (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2274C			TLC2274AC			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{IO} Input offset voltage	$V_{IC} = 0$ V, $V_O = 0$ V, $R_S = 50$ Ω	25°C	300	2500		300	950		μ V
		Full range		3000			1500		
α_{VIO} Temperature coefficient of input offset voltage		25°C to 70°C	2			2			μ V/°C
Input offset voltage long-term drift (see Note 4)		25°C	0.002			0.002			μ V/mo
I_{IO} Input offset current		25°C	0.5	60		0.5	60		pA
		Full range		100			100		
I_{IB} Input bias current		25°C	1	60		1	60		pA
		Full range		100			100		
V_{ICR} Common-mode input voltage	$R_S = 50$ Ω , $ V_{IO} \leq 5$ mV	25°C	–5 to 4	–5.3 to 4.2		–5 to 4	–5.3 to 4.2		V
		Full range	–5 to 3.5			–5 to 3.5			
V_{OM+} Maximum positive peak output voltage	$I_O = -20$ μ A	25°C	4.99			4.99			V
	$I_O = -200$ μ A	25°C	4.85	4.93		4.85	4.93		
		Full range	4.85			4.85			
	$I_O = -1$ mA	25°C	4.25	4.65		4.25	4.65		
V_{OM-} Maximum negative peak output voltage	$V_{IC} = 0$ V, $I_O = 500$ μ A	25°C	–4.99			–4.99			V
		25°C	–4.8 to 5	–4.91		–4.85 to 5	–4.91		
	$V_{IC} = 0$ V, $I_O = -5$ mA	25°C	–3.5	–4.1		–3.5	–4.1		
		Full range	–3.5			–3.5			
A_{VD} Large-signal differential voltage amplification	$V_O = \pm 4$ V	$R_L = 10$ k Ω	25°C	25	50	25	50		V/mV
			Full range	25		25			
		$R_L = 1$ M Ω	25°C	300		300			
r_{id} Differential input resistance		25°C	10^{12}			10^{12}			Ω
r_i Common-mode input resistance		25°C	10^{12}			10^{12}			Ω
c_i Common-mode input capacitance	$f = 10$ kHz, N package	25°C	8			8			pF
z_o Closed-loop output impedance	$f = 1$ MHz, $A_V = 10$	25°C	130			130			Ω
CMRR Common-mode rejection ratio	$V_{IC} = -5$ V to 2.7 V, $V_O = 0$ V, $R_S = 50$ Ω	25°C	75	80		75	80		dB
		Full range	75			75			
k_{SVR} Supply-voltage rejection ratio ($\Delta V_{DD\pm}/\Delta V_{IO}$)	$V_{DD\pm} = \pm 2.2$ V to ± 8 V, $V_{IC} = 0$ V, No load	25°C	80	95		80	95		dB
		Full range	80			80			
I_{DD} Supply current	$V_O = 0$ V, No load	25°C	4.8	6		4.8	6		mA
		Full range		6			6		

† Full range is 0°C to 70°C.

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2274C operating characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2274C			TLC2274AC			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain $V_O = \pm 2.3\text{ V}$, $R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$	25°C	2.3	3.6		2.3	3.6		V/ μs
		Full range	1.7			1.7			
V_n	Equivalent input noise voltage	f = 10 Hz		50			50		nV/ $\sqrt{\text{Hz}}$
		f = 1 Hz		9			9		
$V_{N(PP)}$	Peak-to-peak equivalent input noise voltage	f = 0.1 Hz to 1 Hz		1			1		μV
		f = 0.1 Hz to 10 Hz		1.4			1.4		
I_n	Equivalent input noise current			0.6			0.6		fA/ $\sqrt{\text{Hz}}$
THD + N	Total harmonic distortion plus noise $V_O = \pm 2.3\text{ V}$, f = 20 kHz, $R_L = 10\text{ k}\Omega$	$A_V = 1$		0.0011%			0.0011%		
		$A_V = 10$		0.004%			0.004%		
		$A_V = 100$		0.03%			0.03%		
	Gain-bandwidth product	f = 10 kHz, $R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$		2.25			2.25		MHz
B_{OM}	Maximum output-swing bandwidth	$V_{O(PP)} = 4.6\text{ V}$, $A_V = 1$, $R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$		0.54			0.54		MHz
t_s	Settling time $A_V = -1$, Step = -2.3 V to 2.3 V , $R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$	To 0.1%		1.5			1.5		μs
		To 0.01%		3.2			3.2		
ϕ_m	Phase margin at unity gain	$R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$		52°			52°		
	Gain margin			10			10		dB

† Full range is 0°C to 70°C.

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TLC2272I electrical characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2272I			TLC2272AI			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
V _{IO}	Input offset voltage	V _{IC} = 0 V, V _O = 0 V, V _{DD±} = ±2.5 V R _S = 50 Ω		25°C	300	2500		300	950	μV	
	Full range			3000			1500				
α _{VIO}	Temperature coefficient of input offset voltage			25°C to 85°C	2			2			μV/°C
	Input offset voltage long-term drift (see Note 4)			25°C	0.002			0.002			μV/mo
I _{IO}	Input offset current			25°C	0.5	60		0.5	60	pA	
				–40°C to 85°C	150			150			
				Full range	800			800			
I _{IB}	Input bias current			25°C	1	60		1	60	pA	
				–40°C to 85°C	150			150			
				Full range	800			800			
V _{ICR}	Common-mode input voltage	R _S = 50 Ω, V _{IO} ≤ 5 mV	25°C	0 to 4	–0.3 to 4.2		0 to 4	–0.3 to 4.2	V		
			Full range	0 to 3.5		0 to 3.5					
V _{OH}	High-level output voltage	I _{OH} = –20 μA	25°C	4.99			4.99			V	
		I _{OH} = –200 μA	25°C	4.85	4.93		4.85	4.93			
			Full range	4.85			4.85				
		I _{OH} = –1 mA	25°C	4.25	4.65		4.25	4.65			
			Full range	4.25			4.25				
V _{OL}	Low-level output voltage	V _{IC} = 2.5 V, I _{OL} = 50 μA	25°C	0.01			0.01			V	
		V _{IC} = 2.5 V, I _{OL} = 500 μA	25°C	0.09	0.15		0.09	0.15			
			Full range	0.15			0.15				
		V _{IC} = 2.5 V, I _{OL} = 5 mA	25°C	0.9	1.5		0.9	1.5			
			Full range	1.5			1.5				
A _{VD}	Large-signal differential voltage amplification	V _{IC} = 2.5 V, V _O = 1 V to 4 V	R _L = 10 kΩ [‡]	25°C	15	35		15	35	V/mV	
				Full range	15			15			
			R _L = 1 mΩ [‡]	25°C	175			175			
r _{id}	Differential input resistance			25°C	10 ¹²			10 ¹²			Ω
r _i	Common-mode input resistance			25°C	10 ¹²			10 ¹²			Ω
c _i	Common-mode input capacitance	f = 10 kHz,	P package	25°C	8			8			pF
z _o	Closed-loop output impedance	f = 1 MHz,	A _V = 10	25°C	140			140			Ω
CMRR	Common-mode rejection ratio	V _{IC} = 0 V to 2.7 V, V _O = 2.5 V, R _S = 50 Ω		25°C	70	75		70	75	dB	
				Full range	70			70			
k _{SVR}	Supply-voltage rejection ratio (ΔV _{DD} /ΔV _{IO})	V _{DD} = 4.4 V to 16 V, V _{IC} = V _{DD} /2, No load		25°C	80	95		80	95	dB	
				Full range	80			80			
I _{DD}	Supply current	V _O = 2.5 V, No load		25°C	2.2	3		2.2	3	mA	
				Full range		3			3		

† Full range is –40°C to 125°C.

‡ Referenced to 0 V

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.



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TLC2272I operating characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2272I			TLC2272AI			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain $V_O = 0.5\text{ V to }2.5\text{ V}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C	2.3	3.6		2.3	3.6		V/ μs
		Full range	1.7			1.7			
V_n	Equivalent input noise voltage	$f = 10\text{ Hz}$		50			50		nV/ $\sqrt{\text{Hz}}$
		$f = 1\text{ kHz}$		9			9		
V_{NPP}	Peak-to-peak equivalent input noise voltage	$f = 0.1\text{ Hz to }1\text{ Hz}$		1			1		μV
		$f = 0.1\text{ Hz to }10\text{ Hz}$		1.4			1.4		
I_n	Equivalent input noise current			0.6			0.6		fA/ $\sqrt{\text{Hz}}$
THD + N	Total harmonic distortion plus noise $V_O = 0.5\text{ V to }2.5\text{ V}$, $f = 20\text{ kHz}$, $R_L = 10\text{ k}\Omega^\ddagger$	$A_V = 1$		0.0013%			0.0013%		
		$A_V = 10$		0.004%			0.004%		
		$A_V = 100$		0.03%			0.03%		
	Gain-bandwidth product	$f = 10\text{ kHz}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$		2.18			2.18		MHz
BOM	Maximum output-swinging bandwidth	$V_{O(PP)} = 2\text{ V}$, $R_L = 10\text{ k}\Omega^\ddagger$, $A_V = 1$, $C_L = 100\text{ pF}^\ddagger$		1			1		MHz
t_s	Settling time $A_V = -1$, Step = 0.5 V to 2.5 V, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	To 0.1%		1.5			1.5		μs
		To 0.01%		2.6			2.6		
ϕ_m	Phase margin at unity gain	$R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$		50°			50°		
	Gain margin			10			10		dB

† Full range is -40°C to 125°C .

‡ Referenced to 0 V

TLC2272I electrical characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2272I			TLC2272AI			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
V _{IO}	Input offset voltage	V _{IC} = 0 V, V _O = 0 V, R _S = 50 Ω		25°C	300	2500	300	950	μV		
	Full range			3000			1500				
α _{VIO}	Temperature coefficient of input offset voltage			25°C to 85°C	2			2		μV/°C	
	Input offset voltage long-term drift (see Note 4)			25°C	0.002			0.002		μV/mo	
I _{IO}	Input offset current			25°C	0.5	60	0.5	60	pA		
				–40°C to 85°C	150			150			
				Full range	800			800			
I _{IB}	Input bias current			25°C	1	60	1	60	pA		
				–40°C to 85°C	150			150			
				Full range	800			800			
V _{ICR}	Common-mode input voltage			R _S = 50 Ω, V _{IO} ≤ 5 mV	25°C	–5 to 4	–5.3 to 4.2	–5 to 4	–5.3 to 4.2	V	
					Full range	–5 to 3.5		–5 to 3.5			
V _{OM+}	Maximum positive peak output voltage	I _O = –20 μA	25°C	4.99		4.99		V			
		I _O = –200 μA	25°C	4.85	4.93	4.85	4.93				
			Full range	4.85		4.85					
		I _O = –1 mA	25°C	4.25	4.65	4.25	4.65				
			Full range	4.25		4.25					
V _{OM–}	Maximum negative peak output voltage	V _{IC} = 0 V, I _O = 50 μA	25°C	–4.99		–4.99		V			
		V _{IC} = 0 V, I _O = 500 μA	25°C	–4.85	–4.91	–4.85	–4.91				
			Full range	–4.85		–4.85					
		V _{IC} = 0 V, I _O = 5 mA	25°C	–3.5	–4.1	–3.5	–4.1				
			Full range	–3.5		–3.5					
A _{VD}	Large-signal differential voltage amplification	V _O = ±4 V	R _L = 10 kΩ	25°C	25	50	25	50	V/mV		
			R _L = 1 mΩ	Full range	25		25				
				25°C	300		300				
r _{id}	Differential input resistance			25°C	10 ¹²		10 ¹²		Ω		
r _i	Common-mode input resistance			25°C	10 ¹²		10 ¹²		Ω		
c _i	Common-mode input capacitance	f = 10 kHz,	P package	25°C	8		8		pF		
z _o	Closed-loop output impedance	f = 1 MHz,	A _V = 10	25°C	130		130		Ω		
CMRR	Common-mode rejection ratio	V _{IC} = –5 V to 2.7 V, V _O = 0 V, R _S = 50 Ω		25°C	75	80	75	80	dB		
				Full range	75		75				
k _{SVR}	Supply-voltage rejection ratio (ΔV _{DD±} /ΔV _{IO})	V _{DD} = 4.4 V to 16 V, V _{IC} = V _{DD} /2, No load		25°C	80	95	80	95	dB		
				Full range	80		80				
I _{DD}	Supply current	V _O = 0 V, No load		25°C	2.4	3	2.4	3	mA		
				Full range	3		3				

† Full range is –40°C to 125°C.

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2272I operating characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2272I			TLC2272AI			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain	V _O = ±2.3 V, C _L = 100 pF	R _L = 10 kΩ,	25°C	2.3	3.6		2.3	3.6		V/μs
				Full range	1.7			1.7			
V _n	Equivalent input noise voltage	f = 10 Hz		25°C	50			50			nV/√Hz
		f = 1 kHz		25°C	9			9			
V _{NPP}	Peak-to-peak equivalent input noise voltage	f = 0.1 Hz to 1 Hz		25°C	1			1			μV
		f = 0.1 Hz to 10 Hz		25°C	1.4			1.4			
I _n	Equivalent input noise current			25°C	0.6			0.6			fA√Hz
THD + N	Total harmonic distortion plus noise	V _O = ±2.3 V R _L = 10 kΩ, f = 20 kHz	A _V = 1	25°C	0.0011%			0.0011%			
			A _V = 10		0.004%			0.004%			
			A _V = 100		0.03%			0.03%			
	Gain-bandwidth product	f = 10 kHz, C _L = 100 pF	R _L = 10 kΩ,	25°C	2.25			2.25			MHz
B _{OM}	Maximum output-swing bandwidth	V _{O(PP)} = 4.6 V, R _L = 10 kΩ,	A _V = 1, C _L = 100 pF	25°C	0.54			0.54			MHz
t _s	Settling time	A _V = −1, Step = −2.3 V to 2.3 V, R _L = 10 kΩ, C _L = 100 pF	To 0.1%	25°C	1.5			1.5			μs
			To 0.01%		3.2			3.2			
ϕ _m	Phase margin at unity gain	R _L = 10 kΩ, C _L = 100 pF		25°C	52°			52°			
	Gain margin			25°C	10			10			dB

† Full range is $-40^\circ\text{C to }125^\circ\text{C}$.

TLC2274I electrical characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2274I			TLC2274AI			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{IO} Input offset voltage	$V_{DD\pm} = \pm 2.5\text{ V}, V_{IC} = 0\text{ V}, V_O = 0\text{ V}, R_S = 50\ \Omega$	25°C	300	2500		300	950		μV
		Full range		3000			1500		
α_{VIO} Temperature coefficient of input offset voltage		25°C to 85°C	2			2			$\mu\text{V}/^\circ\text{C}$
Input offset voltage long-term drift (see Note 4)		25°C	0.002			0.002			$\mu\text{V}/\text{mo}$
I_{IO} Input offset current		25°C	0.5	60		0.5	60		pA
		–40°C to 85°C		150			150		
		Full range		800			800		
I_{IB} Input bias current		25°C	1	60		1	60		pA
		–40°C to 85°C		150			150		
		Full range		800			800		
V_{ICR} Common-mode input voltage	$R_S = 50\ \Omega, V_{IO} \leq 5\text{ mV}$	25°C	0 to 4	–0.3 to 4.2		0 to 4	–0.3 to 4.2		V
		Full range	0 to 3.5			0 to 3.5			
V_{OH} High-level output voltage	$I_{OH} = -20\ \mu\text{A}$	25°C		4.99			4.99		V
	$I_{OH} = -200\ \mu\text{A}$	25°C	4.85	4.93		4.85	4.93		
		Full range	4.85			4.85			
	$I_{OH} = -1\text{ mA}$	25°C	4.25	4.65		4.25	4.65		
		Full range	4.25			4.25			
V_{OL} Low-level output voltage	$V_{IC} = 2.5\text{ V}, I_{OL} = 50\ \mu\text{A}$	25°C		0.01			0.01		V
	$V_{IC} = 2.5\text{ V}, I_{OL} = 500\ \mu\text{A}$	25°C		0.09 0.15			0.09 0.15		
		Full range		0.15			0.15		
	$V_{IC} = 2.5\text{ V}, I_{OL} = 5\text{ mA}$	25°C		0.9 1.5			0.9 1.5		
		Full range		1.5			1.5		
A_{VD} Large-signal differential voltage amplification	$V_{IC} = 2.5\text{ V}, V_O = 1\text{ V to }4\text{ V}$	$R_L = 10\text{ k}\Omega^\ddagger$	25°C	15 35		15 35			V/mV
			Full range	15		15			
		$R_L = 1\text{ M}\Omega^\ddagger$	25°C	175		175			
r_{id} Differential input resistance		25°C		10^{12}			10^{12}		Ω
r_i Common-mode input resistance		25°C		10^{12}			10^{12}		Ω
c_i Common-mode input capacitance	$f = 10\text{ kHz}, \text{ N package}$	25°C		8			8		pF
z_o Closed-loop output impedance	$f = 1\text{ MHz}, A_V = 10$	25°C		140			140		Ω
CMRR Common-mode rejection ratio	$V_{IC} = 0\text{ V to }2.7\text{ V}, V_O = 2.5\text{ V}, R_S = 50\ \Omega$	25°C	70	75		70	75		dB
		Full range	70			70			
k_{SVR} Supply-voltage rejection ratio ($\Delta V_{DD} / \Delta V_{IO}$)	$V_{DD} = 4.4\text{ V to }16\text{ V}, V_{IC} = V_{DD}/2, \text{ No load}$	25°C	80	95		80	95		dB
		Full range	80			80			
I_{DD} Supply current	$V_O = 2.5\text{ V}, \text{ No load}$	25°C		4.4 6			4.4 6		mA
		Full range		6			6		

† Full range is –40°C to 125°C.

‡ Referenced to 0 V

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2274I operating characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2274I			TLC2274AI			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain $V_O = 0.5\text{ V to }2.5\text{ V}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C	2.3	3.6		2.3	3.6		V/ μs
		Full range	1.7			1.7			
V_n	Equivalent input noise voltage	25°C		50			50		nV/ $\sqrt{\text{Hz}}$
	$f = 1\text{ kHz}$	25°C		9			9		
$V_{N(PP)}$	Peak-to-peak equivalent input noise voltage	25°C		1			1		μV
	$f = 0.1\text{ Hz to }10\text{ Hz}$	25°C		1.4			1.4		
I_n	Equivalent input noise current	25°C		0.6			0.6		fA/ $\sqrt{\text{Hz}}$
THD + N	Total harmonic distortion plus noise $V_O = 0.5\text{ V to }2.5\text{ V}$, $f = 20\text{ kHz}$, $R_L = 10\text{ k}\Omega^\ddagger$	$A_V = 1$		0.0013%			0.0013%		
		$A_V = 10$		0.004%			0.004%		
		$A_V = 100$		0.03%			0.03%		
	Gain-bandwidth product $f = 10\text{ kHz}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C		2.18			2.18		MHz
BOM	Maximum output-swing bandwidth $V_{O(PP)} = 2\text{ V}$, $A_V = 1$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C		1			1		MHz
t_s	Settling time $A_V = -1$, Step = $0.5\text{ V to }2.5\text{ V}$, $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	To 0.1%		1.5			1.5		μs
		To 0.01%		2.6			2.6		
ϕ_m	Phase margin at unity gain $R_L = 10\text{ k}\Omega^\ddagger$, $C_L = 100\text{ pF}^\ddagger$	25°C		50°			50°		
	Gain margin	25°C		10			10		dB

† Full range is $-40^\circ\text{C to }125^\circ\text{C}$.

‡ Referenced to 0 V

TLC2274I electrical characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2274I			TLC2274AI			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{IO} Input offset voltage	$V_{IC} = 0\text{ V}, V_O = 0\text{ V}, R_S = 50\ \Omega$	25°C		300	2500		300	950	μV
		Full range			3000			1500	
α_{VIO} Temperature coefficient of input offset voltage		25°C to 85°C		2			2		$\mu\text{V}/^\circ\text{C}$
Input offset voltage long-term drift (see Note 4)		25°C		0.002			0.002		$\mu\text{V}/\text{mo}$
I_{IO} Input offset current		25°C		0.5	60		0.5	60	pA
		–40°C to 85°C			150			150	
		Full range			800			800	
I_{IB} Input bias current		25°C		1	60		1	60	pA
		–40°C to 85°C			150			150	
		Full range			800			800	
V_{ICR} Common-mode input voltage	$R_S = 50\ \Omega, V_{IO} \leq 5\text{ mV}$	25°C	–5 to 4	–5.3 to 4.2		–5 to 4	–5.3 to 4.2		V
		Full range	–5 to 3.5			–5 to 3.5			
V_{OM+} Maximum positive peak output voltage	$I_O = -20\ \mu\text{A}$	25°C		4.99			4.99		V
	$I_O = -200\ \mu\text{A}$	25°C		4.85	4.93		4.85	4.93	
		Full range		4.85			4.85		
	$I_O = -1\text{ mA}$	25°C		4.25	4.65		4.25	4.65	
V_{OM-} Maximum negative peak output voltage	$V_{IC} = 0\text{ V}, I_O = 50\ \mu\text{A}$	25°C		–4.99			–4.99		V
		Full range		–4.85	–4.91		–4.85	–4.91	
	$V_{IC} = 0\text{ V}, I_O = 500\ \mu\text{A}$	25°C		–4.85	–4.91		–4.85	–4.91	
		Full range		–4.85			–4.85		
	$V_{IC} = 0\text{ V}, I_O = 5\text{ mA}$	25°C		–3.5	–4.1		–3.5	–4.1	
		Full range		–3.5			–3.5		
A_{VD} Large-signal differential voltage amplification	$V_O = \pm 4\text{ V}$	$R_L = 10\text{ k}\Omega$	25°C	25	50		25	50	V/mV
			Full range	25			25		
		$R_L = 1\text{ M}\Omega$	25°C		300			300	
r_{id} Differential input resistance		25°C		10^{12}			10^{12}		Ω
r_i Common-mode input resistance		25°C		10^{12}			10^{12}		Ω
c_i Common-mode input capacitance	$f = 10\text{ kHz}, \text{ N package}$	25°C		8			8		pF
z_o Closed-loop output impedance	$f = 1\text{ MHz}, A_V = 10$	25°C		130			130		Ω
CMRR Common-mode rejection ratio	$V_{IC} = -5\text{ V to } 2.7\text{ V}, V_O = 0\text{ V}, R_S = 50\ \Omega$	25°C		75	80		75	80	dB
		Full range		75			75		
k_{SVR} Supply-voltage rejection ratio ($\Delta V_{DD\pm}/\Delta V_{IO}$)	$V_{DD\pm} = \pm 2.2\text{ V to } \pm 8\text{ V}, V_{IC} = 0\text{ V}, \text{ No load}$	25°C		80	95		80	95	dB
		Full range		80			80		
I_{DD} Supply current	$V_O = 0\text{ V}, \text{ No load}$	25°C		4.8	6		4.8	6	mA
		Full range			6			6	

† Full range is –40°C to 125°C.

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2274I operating characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$

PARAMETER		TEST CONDITIONS		$T_A^\dagger$	TLC2274I			TLC2274AI			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain	$V_O = \pm 2.3\text{ V}$, $C_L = 100\text{ pF}$	$R_L = 10\text{ k}\Omega$	25°C	2.3	3.6		2.3	3.6		V/ μs
				Full range	1.7			1.7			
V_n	Equivalent input noise voltage	$f = 10\text{ Hz}$		25°C		50			50		nV/ $\sqrt{\text{Hz}}$
		$f = 1\text{ kHz}$		25°C		9			9		
$V_{N(PP)}$	Peak-to-peak equivalent input noise voltage	$f = 0.1\text{ Hz to }1\text{ Hz}$		25°C		1			1		μV
		$f = 0.1\text{ Hz to }10\text{ Hz}$		25°C		1.4			1.4		
I_n	Equivalent input noise current			25°C		0.6			0.6		fA/ $\sqrt{\text{Hz}}$
THD + N	Total harmonic distortion plus noise	$V_O = \pm 2.3\text{ V}$, $R_L = 10\text{ k}\Omega$, $f = 20\text{ kHz}$	$A_V = 1$	25°C		0.0011%			0.0011%		
			$A_V = 10$			0.004%			0.004%		
			$A_V = 100$			0.03%			0.03%		
	Gain-bandwidth product	$f = 10\text{ kHz}$, $C_L = 100\text{ pF}$	$R_L = 10\text{ k}\Omega$	25°C		2.25			2.25		MHz
B_{OM}	Maximum output-swing bandwidth	$V_{O(PP)} = 4.6\text{ V}$, $R_L = 10\text{ k}\Omega$	$A_V = 1$, $C_L = 100\text{ pF}$	25°C		0.54			0.54		MHz
t_s	Settling time	$A_V = -1$, Step = $-2.3\text{ V to }2.3\text{ V}$, $R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$	To 0.1%	25°C		1.5			1.5		μs
			To 0.01%			3.2			3.2		
ϕ_m	Phase margin at unity gain	$R_L = 10\text{ k}\Omega$, $C_L = 100\text{ pF}$		25°C		52°			52°		
	Gain margin			25°C		10			10		dB

† Full range is $-40^\circ\text{C to }125^\circ\text{C}$.

TLC2272Q and TLC2272M electrical characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2272Q, TLC2272M			TLC2272AQ, TLC2272AM			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{IO} Input offset voltage	$V_{IC} = 0\text{ V}, V_O = 0\text{ V}, V_{DD\pm} = \pm 2.5\text{ V}, R_S = 50\ \Omega$	25°C	300	2500		300	950		μV
		Full range		3000			1500		
α_{VIO} Temperature coefficient of input offset voltage		25°C to 125°C	2			2			$\mu\text{V}/^\circ\text{C}$
Input offset voltage long-term drift (see Note 4)		25°C	0.002			0.002			$\mu\text{V}/\text{mo}$
I_{IO} Input offset current		25°C	0.5	60		0.5	60		pA
		Full range		800			800		
I_{IB} Input bias current		25°C	1	60		1	60		pA
		Full range		800			800		
V_{ICR} Common-mode input voltage	$R_S = 50\ \Omega, V_{IO} \leq 5\text{ mV}$	25°C	0 to 4	–0.3 to 4.2		0 to 4	–0.3 to 4.2		V
		Full range	0 to 3.5			0 to 3.5			
V_{OH} High-level output voltage	$I_{OH} = -20\ \mu\text{A}$	25°C	4.99			4.99			V
	$I_{OH} = -200\ \mu\text{A}$	25°C	4.85	4.93		4.85	4.93		
		Full range	4.85			4.85			
	$I_{OH} = -1\text{ mA}$	25°C	4.25	4.65		4.25	4.65		
V_{OL} Low-level output voltage	$V_{IC} = 2.5\text{ V}, I_{OL} = 50\ \mu\text{A}$	25°C	0.01			0.01			V
	$V_{IC} = 2.5\text{ V}, I_{OL} = 500\ \mu\text{A}$	25°C	0.09	0.15		0.09	0.15		
		Full range		0.15			0.15		
	$V_{IC} = 2.5\text{ V}, I_{OL} = 5\text{ mA}$	25°C	0.9	1.5		0.9	1.5		
		Full range		1.5			1.5		
A_{VD} Large-signal differential voltage amplification	$V_{IC} = 2.5\text{ V}, V_O = 1\text{ V to }4\text{ V}$	$R_L = 10\text{ k}\Omega^\ddagger$	25°C	10	35	10	35		V/mV
			Full range	10		10			
		$R_L = 1\text{ m}\Omega^\ddagger$	25°C	175		175			
r_{id} Differential input resistance		25°C	10^{12}			10^{12}			Ω
r_i Common-mode input resistance		25°C	10^{12}			10^{12}			Ω
c_i Common-mode input capacitance	$f = 10\text{ kHz}, \text{ P package}$	25°C	8			8			pF
z_o Closed-loop output impedance	$f = 1\text{ MHz}, A_V = 10$	25°C	140			140			Ω
CMRR Common-mode rejection ratio	$V_{IC} = 0\text{ V to }2.7\text{ V}, V_O = 2.5\text{ V}, R_S = 50\ \Omega$	25°C	70	75		70	75		dB
		Full range	70			70			
k_{SVR} Supply-voltage rejection ratio ($\Delta V_{DD}/\Delta V_{IO}$)	$V_{DD} = 4.4\text{ V to }16\text{ V}, V_{IC} = V_{DD}/2, \text{ No load}$	25°C	80	95		80	95		dB
		Full range	80			80			
I_{DD} Supply current	$V_O = 2.5\text{ V}, \text{ No load}$	25°C	2.2	3		2.2	3		mA
		Full range		3			3		

† Full range is -40°C to 125°C for Q level part, -55°C to 125°C for M level part.

‡ Referenced to 2.5 V

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2272Q and TLC2272M operating characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$

PARAMETER	TEST CONDITIONS	T_A †	TLC2272Q, TLC2272M			TLC2272AQ, TLC2272AM			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain	$V_O = 1.25\text{ V to }2.75\text{ V},$ $R_L = 10\text{ k}\Omega^\ddagger,$ $C_L = 100\text{ pF}^\ddagger$	25°C	2.3	3.6	2.3	3.6		V/ μs
			Full range	1.7		1.7			
V_n	Equivalent input noise voltage	$f = 10\text{ Hz}$	25°C	50		50			nV/ $\sqrt{\text{Hz}}$
		$f = 1\text{ kHz}$	25°C	9		9			
V_{NPP}	Peak-to-peak equivalent input noise voltage	$f = 0.1\text{ Hz to }1\text{ Hz}$	25°C	1		1			μV
		$f = 0.1\text{ Hz to }10\text{ Hz}$	25°C	1.4		1.4			
I_n	Equivalent input noise current		25°C	0.6		0.6			fA/ $\sqrt{\text{Hz}}$
THD + N	Total harmonic distortion plus noise	$V_O = 0.5\text{ V to }2.5\text{ V},$ $f = 20\text{ kHz},$ $R_L = 10\text{ k}\Omega^\ddagger,$	25°C	$A_V = 1$	0.0013%	$A_V = 1$	0.0013%		
				$A_V = 10$	0.004%	$A_V = 10$	0.004%		
				$A_V = 100$	0.03%	$A_V = 100$	0.03%		
	Gain-bandwidth product	$f = 10\text{ kHz},$ $R_L = 10\text{ k}\Omega^\ddagger,$ $C_L = 100\text{ pF}^\ddagger$	25°C	2.18		2.18			MHz
B_{OM}	Maximum output-swing bandwidth	$V_{O(PP)} = 2\text{ V},$ $R_L = 10\text{ k}\Omega^\ddagger,$ $A_V = 1,$ $C_L = 100\text{ pF}^\ddagger$	25°C	1		1			MHz
t_s	Settling time	$A_V = -1,$ Step = $0.5\text{ V to }2.5\text{ V},$ $R_L = 10\text{ k}\Omega^\ddagger,$ $C_L = 100\text{ pF}^\ddagger$	25°C	To 0.1%	1.5	To 0.1%	1.5		μs
				To 0.01%	2.6	To 0.01%	2.6		
ϕ_m	Phase margin at unity gain	$R_L = 10\text{ k}\Omega^\ddagger,$ $C_L = 100\text{ pF}^\ddagger$	25°C	50°		50°			
	Gain margin		25°C	10		10			dB

† Full range is $-40^\circ\text{C to }125^\circ\text{C}$ for Q level part, $-55^\circ\text{C to }125^\circ\text{C}$ for M level part.

‡ Referenced to 2.5 V

TLC2272Q and TLC2272M electrical characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2272Q, TLC2272M			TLC2272AQ, TLC2272AM			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
V _{IO}	Input offset voltage	V _{IC} = 0 V, V _O = 0 V, R _S = 50 Ω		25°C	300	2500	300	950	μV		
	Full range			3000			1500				
α _{VIO}	Temperature coefficient of input offset voltage			25°C to 125°C	2			2			μV/°C
	Input offset voltage long-term drift (see Note 4)			25°C	0.002			0.002			μV/mo
I _{IO}	Input offset current			25°C	0.5	60	0.5	60	pA		
				Full range	800			800			
I _{IB}	Input bias current			25°C	1	60	1	60	pA		
				Full range	800			800			
V _{ICR}	Common-mode input voltage	R _S = 50 Ω, V _{IO} ≤ 5 mV		25°C	−5 to 4	−5.3 to 4.2	−5 to 4	−5.3 to 4.2	V		
				Full range	−5 to 3.5		−5 to 3.5				
V _{OM+}	Maximum positive peak output voltage	I _O = −20 μA		25°C	4.99			4.99	V		
		I _O = −200 μA		25°C	4.85	4.93	4.85	4.93			
				Full range	4.85			4.85			
		I _O = −1 mA		25°C	4.25	4.65	4.25	4.65			
Full range	4.25			4.25							
V _{OM−}	Maximum negative peak output voltage	V _{IC} = 0 V, I _O = 50 μA		25°C	−4.99			−4.99	V		
		V _{IC} = 0 V, I _O = 500 μA		25°C	−4.85	−4.91	−4.85	−4.91			
				Full range	−4.85			−4.85			
		V _{IC} = 0 V, I _O = 5 mA		25°C	−3.5	−4.1	−3.5	−4.1			
				Full range	−3.5			−3.5			
A _{VD}	Large-signal differential voltage amplification	V _O = ±4 V	R _L = 10 kΩ	25°C	20	50	20	50	V/mV		
				Full range	20		20				
			R _L = 1 mΩ	25°C	300			300			
r _{id}	Differential input resistance			25°C	10 ¹²			10 ¹²		Ω	
r _i	Common-mode input resistance			25°C	10 ¹²			10 ¹²		Ω	
c _i	Common-mode input capacitance	f = 10 kHz,	P package	25°C	8			8		pF	
z _o	Closed-loop output impedance	f = 1 MHz,	A _V = 10	25°C	130			130		Ω	
CMRR	Common-mode rejection ratio	V _{IC} = −5 V to 2.7 V, V _O = 0 V, R _S = 50 Ω		25°C	75	80	75	80	dB		
				Full range	75		75				
k _{SVR}	Supply-voltage rejection ratio (ΔV _{DD±} /ΔV _{IO})	V _{DD} = ±2.2 V to ±8 V, V _{IC} = 0 V, No load		25°C	80	95	80	95	dB		
				Full range	80		80				
I _{DD}	Supply current	V _O = 2.5 V, No load		25°C	2.4	3	2.4	3	mA		
				Full range	3			3			

† Full range is –40°C to 125°C for Q level part, –55°C to 125°C for M level part.

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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**TLC2272Q and TLC2272M operating characteristics at specified free-air temperature,
 $V_{DD\pm} = \pm 5\text{ V}$**

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2272Q, TLC2272M			TLC2272AQ, TLC2272AM			UNIT	
					MIN	TYP	MAX	MIN	TYP	MAX		
SR	Slew rate at unity gain	V _O = ±1 V, C _L = 100 pF R _L = 10 kΩ,		25°C	2.3	3.6		2.3	3.6		V/μs	
				Full range	1.7			1.7				
V _n	Equivalent input noise voltage	f = 10 Hz		25°C	50			50			nV/√Hz	
		f = 1 kHz		25°C	9			9				
V _{NPP}	Peak-to-peak equivalent input noise voltage	f = 0.1 Hz to 1 Hz		25°C	1			1			μV	
		f = 0.1 Hz to 10 Hz		25°C	1.4			1.4				
I _n	Equivalent input noise current			25°C	0.6			0.6			fA/√Hz	
THD + N	Total harmonic distortion plus noise	V _O = ±2.3 V R _L = 10 kΩ, f = 20 kHz	A _V = 1	25°C	0.0011%			0.0011%				
			A _V = 10		0.004%			0.004%				
			A _V = 100		0.03%			0.03%				
	Gain-bandwidth product	f = 10 kHz, C _L = 100 pF R _L = 10 kΩ,		25°C	2.25			2.25			MHz	
B _{OM}	Maximum output-swing bandwidth	V _{O(PP)} = 4.6 V, R _L = 10 kΩ,		A _V = 1, C _L = 100 pF	25°C	0.54			0.54			MHz
t _s	Settling time	A _V = −1, Step = −2.3 V to 2.3 V, R _L = 10 kΩ, C _L = 100 pF	To 0.1%	25°C	1.5			1.5			μs	
			To 0.01%		3.2			3.2				
ϕ _m	Phase margin at unity gain	R _L = 10 kΩ, C _L = 100 pF		25°C	52°			52°				
	Gain margin			25°C	10			10			dB	

† Full range is -40°C to 125°C for Q level part, -55°C to 125°C for M level part.

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TLC2274Q and TLC2274M electrical characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2274Q, TLC2274M			TLC2274AQ, TLC2274AM			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
V _{IO}	Input offset voltage	V _{DD±} = ±2.5 V, V _{IC} = 0 V, V _O = 0 V, R _S = 50 Ω		25°C	300	2500		300	950	μV	
	Full range				3000			1500			
α _{VIO}	Temperature coefficient of input offset voltage			25°C to 125°C	2			2		μV/°C	
	Input offset voltage long-term drift (see Note 4)			25°C	0.002			0.002		μV/mo	
I _{IO}	Input offset current			25°C	0.5	60		0.5	60	pA	
				Full range		800			800		
I _{IB}	Input bias current			25°C	1	60		1	60	pA	
				Full range		800			800		
V _{ICR}	Common-mode input voltage	R _S = 50 Ω, V _{IO} ≤ 5 mV	25°C	0 to 4	–0.3 to 4.2		0 to 4	–0.3 to 4.2	V		
			Full range	0 to 3.5			0 to 3.5				
V _{OH}	High-level output voltage	I _{OH} = –20 μA	25°C		4.99			4.99	V		
		I _{OH} = –200 μA	25°C	4.85	4.93		4.85	4.93			
			Full range	4.85			4.85				
		I _{OH} = –1 mA	25°C	4.25	4.65		4.25	4.65			
			Full range	4.25			4.25				
V _{OL}	Low-level output voltage	V _{IC} = 2.5 V, I _{OL} = 50 μA	25°C		0.01			0.01	V		
		V _{IC} = 2.5 V, I _{OL} = 500 μA	25°C	0.09	0.15		0.09	0.15			
			Full range		0.15			0.15			
		V _{IC} = 2.5 V, I _{OL} = 5 mA	25°C	0.9	1.5		0.9	1.5			
			Full range		1.5			1.5			
		A _{VD}	Large-signal differential voltage amplification	V _{IC} = 2.5 V, V _O = 1 V to 4 V	R _L = 10 kΩ [‡]	25°C	10	35			10
	Full range				10			10			
R _L = 1 MΩ [‡]	25°C					175			175		
r _{id}	Differential input resistance			25°C		10 ¹²			10 ¹²	Ω	
r _i	Common-mode input resistance			25°C		10 ¹²			10 ¹²	Ω	
c _i	Common-mode input capacitance	f = 10 kHz,	N package	25°C		8			8	pF	
z _o	Closed-loop output impedance	f = 1 MHz,	A _V = 10	25°C		140			140	Ω	
CMRR	Common-mode rejection ratio	V _{IC} = 0 V to 2.7 V, V _O = 2.5 V, R _S = 50 Ω		25°C	70	75		70	75	dB	
				Full range	70			70			
K _{SVR}	Supply-voltage rejection ratio (ΔV _{DD} /ΔV _{IO})	V _{DD} = 4.4 V to 16 V, V _{IC} = V _{DD} /2, No load		25°C	80	95		80	95	dB	
				Full range	80			80			
I _{DD}	Supply current	V _O = 2.5 V, No load		25°C	4.4	6		4.4	6	mA	
				Full range		6			6		

[†] Full range is -40°C to 125°C for Q level part, -55°C to 125°C for M level part.

[‡] Referenced to 2.5 V

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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TLC2274Q and TLC2274M operating characteristics at specified free-air temperature, $V_{DD} = 5\text{ V}$

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2274Q, TLC2274M			TLC2274AQ, TLC2274AM			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain	V _O = 0.5 V to 2.5 V, R _L = 10 kΩ [‡] , C _L = 100 pF [‡]		25°C	2.3	3.6		2.3	3.6		V/μs
				Full range	1.7			1.7			
V _n	Equivalent input noise voltage	f = 10 Hz		25°C	50			50			nV/√Hz
		f = 1 kHz		25°C	9			9			
V _{N(PP)}	Peak-to-peak equivalent input noise voltage	f = 0.1 Hz to 1 Hz		25°C	1			1			μV
		f = 0.1 Hz to 10 Hz		25°C	1.4			1.4			
I _n	Equivalent input noise current			25°C	0.6			0.6			fA/√Hz
THD + N	Total harmonic distortion plus noise	V _O = 0.5 V to 2.5 V, f = 20 kHz, R _L = 10 kΩ [‡]	A _V = 1	25°C	0.0013%			0.0013%			
			A _V = 10		0.004%			0.004%			
			A _V = 100		0.03%			0.03%			
	Gain-bandwidth product	f = 10 kHz, C _L = 100 pF [‡]		R _L = 10 kΩ [‡] , 25°C	2.18			2.18			MHz
B _{OM}	Maximum out-put-swing band-width	V _{O(PP)} = 2 V, R _L = 10 kΩ [‡] ,		A _V = 1, C _L = 100 pF [‡] , 25°C	1			1			MHz
t _s	Settling time	A _V = −1, Step = 0.5 V to 2.5 V, R _L = 10 kΩ [‡] , C _L = 100 pF [‡]	To 0.1%	25°C	1.5			1.5			μs
			To 0.01%		2.6			2.6			
ϕ _m	Phase margin at unity gain	R _L = 10 kΩ [‡] , C _L = 100 pF [‡]		25°C	50°			50°			
	Gain margin			25°C	10			10			dB

† Full range is -40°C to 125°C for Q level part, -55°C to 125°C for M level part.

‡ Referenced to 2.5 V

TLC2274Q and TLC2274M electrical characteristics at specified free-air temperature, $V_{DD\pm} = \pm 5\text{ V}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLC2274Q, TLC2274M			TLC2274AQ, TLC2274AM			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{IO} Input offset voltage	$V_{IC} = 0\text{ V}, V_O = 0\text{ V}, R_S = 50\ \Omega$	25°C	300	2500		300	950		μV
		Full range		3000			1500		
α_{VIO} Temperature coefficient of input offset voltage		25°C to 125°C	2			2			$\mu\text{V}/^\circ\text{C}$
Input offset voltage long-term drift (see Note 4)		25°C	0.002			0.002			$\mu\text{V}/\text{mo}$
I_{IO} Input offset current		25°C	0.5	60		0.5	60		pA
		Full range		800			800		
I_{IB} Input bias current		25°C	1	60		1	60		pA
		Full range		800			800		
V_{ICR} Common-mode input voltage	$R_S = 50\ \Omega, V_{IO} \leq 5\text{ mV}$	25°C	–5 to 4	–5.3 to 4.2		–5 to 4	–5.3 to 4.2		V
		Full range	–5 to 3.5			–5 to 3.5			
V_{OM+} Maximum positive peak output voltage	$I_O = -20\ \mu\text{A}$	25°C	4.99			4.99			V
	$I_O = -200\ \mu\text{A}$	25°C	4.85	4.93		4.85	4.93		
		Full range	4.85			4.85			
	$I_O = -1\text{ mA}$	25°C	4.25	4.65		4.25	4.65		
V_{OM-} Maximum negative peak output voltage	$V_{IC} = 0\text{ V}, I_O = 50\ \mu\text{A}$	25°C	–4.99			–4.99			V
		25°C	–4.85	–4.91		–4.85	–4.91		
	$V_{IC} = 0\text{ V}, I_O = 500\ \mu\text{A}$	Full range	–4.85			–4.85			
		25°C	–3.5	–4.1		–3.5	–4.1		
	$V_{IC} = 0\text{ V}, I_O = 5\text{ mA}$	Full range	–3.5			–3.5			
A_{VD} Large-signal differential voltage amplification	$V_O = \pm 4\text{ V}$	$R_L = 10\text{ k}\Omega$	25°C	20	50	20	50		V/mV
			Full range	20		20			
		$R_L = 1\text{ M}\Omega$	25°C	300		300			
r_{id} Differential input resistance		25°C	10^{12}			10^{12}			Ω
r_i Common-mode input resistance		25°C	10^{12}			10^{12}			Ω
c_i Common-mode input capacitance	$f = 10\text{ kHz}, \text{ N package}$	25°C	8			8			pF
z_o Closed-loop output impedance	$f = 1\text{ MHz}, A_V = 10$	25°C	130			130			Ω
CMRR Common-mode rejection ratio	$V_{IC} = -5\text{ V to } 2.7\text{ V}, V_O = 0\text{ V}, R_S = 50\ \Omega$	25°C	75	80		75	80		dB
		Full range	75			75			
k_{SVR} Supply-voltage rejection ratio ($\Delta V_{DD\pm}/\Delta V_{IO}$)	$V_{DD\pm} = \pm 2.2\text{ V to } \pm 8\text{ V}, V_{IC} = 0\text{ V}, \text{ No load}$	25°C	80	95		80	95		dB
		Full range	80			80			
I_{DD} Supply current	$V_O = 0\text{ V}, \text{ No load}$	25°C	4.8	6		4.8	6		mA
		Full range		6			6		

† Full range is -40°C to 125°C for Q level part, -55°C to 125°C for M level part.

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at $T_A = 150^\circ\text{C}$ extrapolated to $T_A = 25^\circ\text{C}$ using the Arrhenius equation and assuming an activation energy of 0.96 eV.

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**TLC2274Q and TLC2274M operating characteristics at specified free-air temperature,
 $V_{DD\pm} = \pm 5\text{ V}$**

PARAMETER		TEST CONDITIONS		T _A [†]	TLC2274Q, TLC2274M			TLC2274AQ, TLC2274AM			UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
SR	Slew rate at unity gain	V _O = ±2.3 V, R _L = 10 kΩ, C _L = 100 pF		25°C	2.3	3.6		2.3	3.6		V/μs
				Full range	1.7			1.7			
V _n	Equivalent input noise voltage	f = 10 Hz		25°C	50			50			nV/√Hz
		f = 1 kHz		25°C	9			9			
V _{N(PP)}	Peak-to-peak equivalent input noise voltage	f = 0.1 Hz to 1 Hz		25°C	1			1			μV
		f = 0.1 Hz to 10 Hz		25°C	1.4			1.4			
I _n	Equivalent input noise current			25°C	0.6			0.6			fA/√Hz
THD + N	Total harmonic distortion plus noise	V _O = ±2.3 V, R _L = 10 kΩ, f = 20 kHz	A _V = 1	25°C	0.0011%			0.0011%			
			A _V = 10		0.004%			0.004%			
			A _V = 100		0.03%			0.03%			
	Gain-bandwidth product	f = 10 kHz, R _L = 10 kΩ, C _L = 100 pF		25°C	2.25			2.25			MHz
B _{OM}	Maximum output-swing bandwidth	V _{O(PP)} = 4.6 V, R _L = 10 kΩ, A _V = 1, C _L = 100 pF		25°C	0.54			0.54			MHz
t _s	Settling time	A _V = −1, Step = −2.3 V to 2.3 V, R _L = 10 kΩ, C _L = 100 pF	To 0.1%	25°C	1.5			1.5			μs
			To 0.01%		3.2			3.2			
ϕ _m	Phase margin at unit gain	R _L = 10 kΩ, C _L = 100 pF		25°C	52°			52°			
	Gain margin			25°C	10			10			dB

† Full range is $-40^\circ\text{C to }125^\circ\text{C}$ for Q level part, $-55^\circ\text{C to }125^\circ\text{C}$ for M level part.

TYPICAL CHARACTERISTICS

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		vs Free-air temperature	32
k_{SVR}	Supply-voltage rejection ratio	vs Frequency	33, 34
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THD + N	Total harmonic distortion plus noise	vs Frequency	54
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		vs Free-air temperature	56
ϕ_m	Phase margin	vs Load capacitance	57
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NOTE: For all graphs where $V_{DD} = 5$ V, all loads are referenced to 2.5 V.

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TYPICAL CHARACTERISTICS

DISTRIBUTION OF TLC2272
INPUT OFFSET VOLTAGE

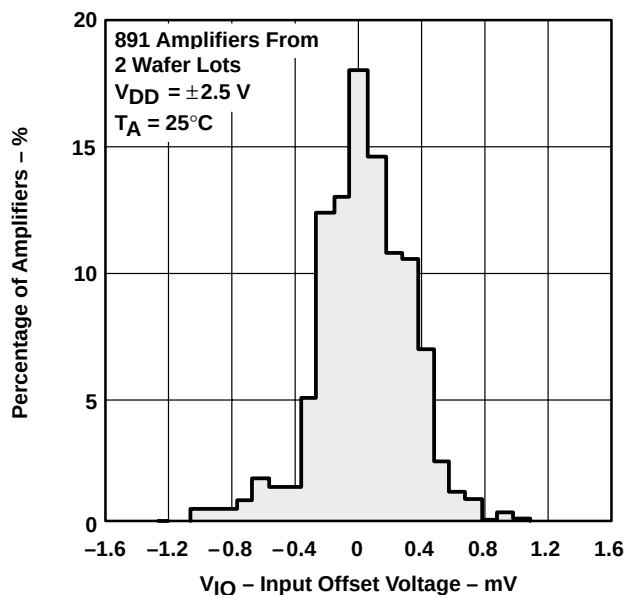


Figure 1

DISTRIBUTION OF TLC2272
INPUT OFFSET VOLTAGE

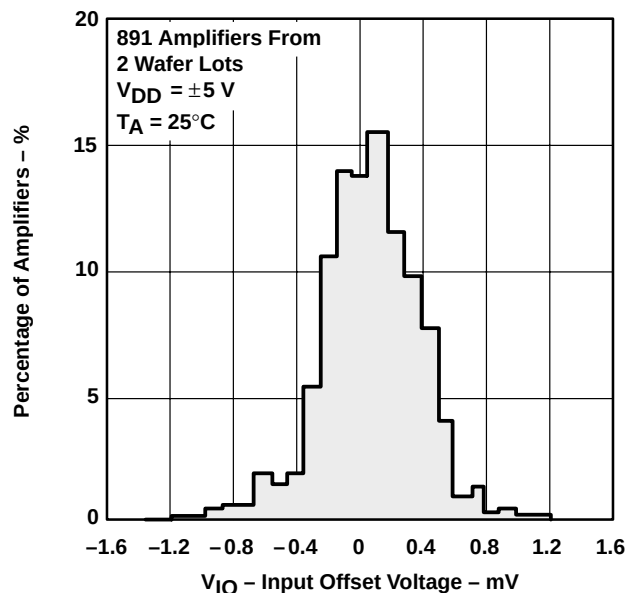


Figure 2

DISTRIBUTION OF TLC2274
INPUT OFFSET VOLTAGE

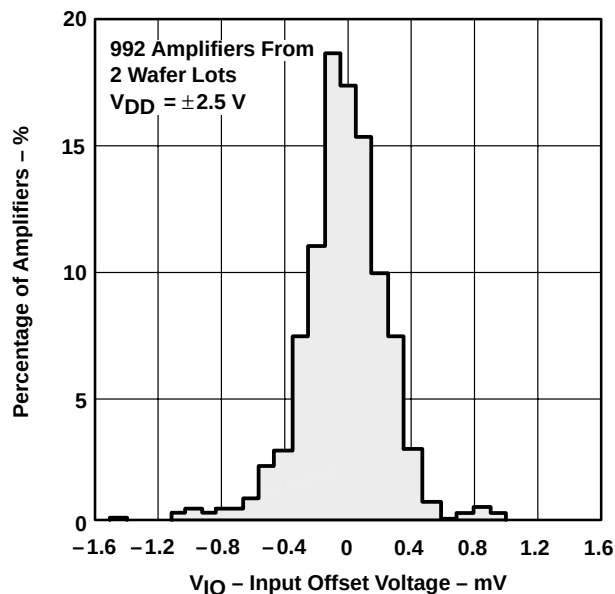


Figure 3

DISTRIBUTION OF TLC2274
INPUT OFFSET VOLTAGE

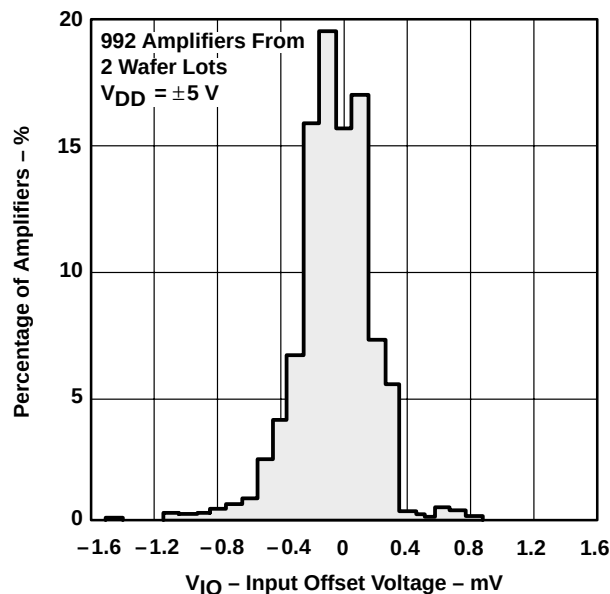


Figure 4

TYPICAL CHARACTERISTICS

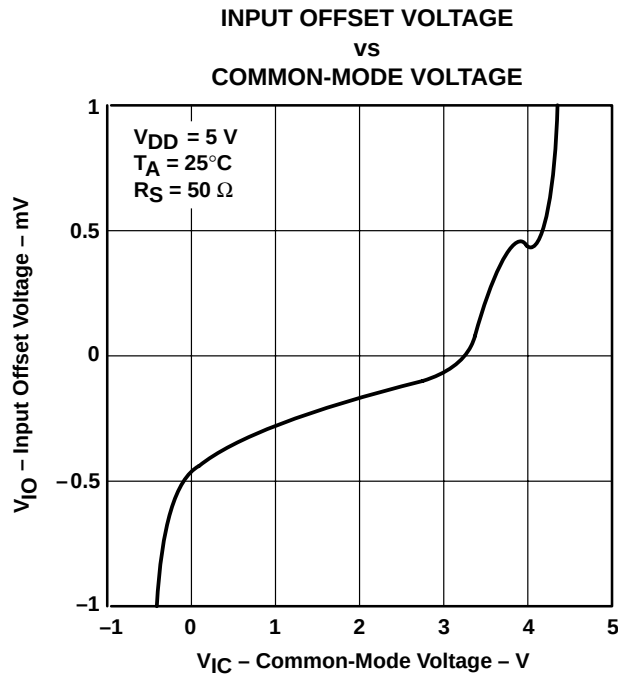


Figure 5

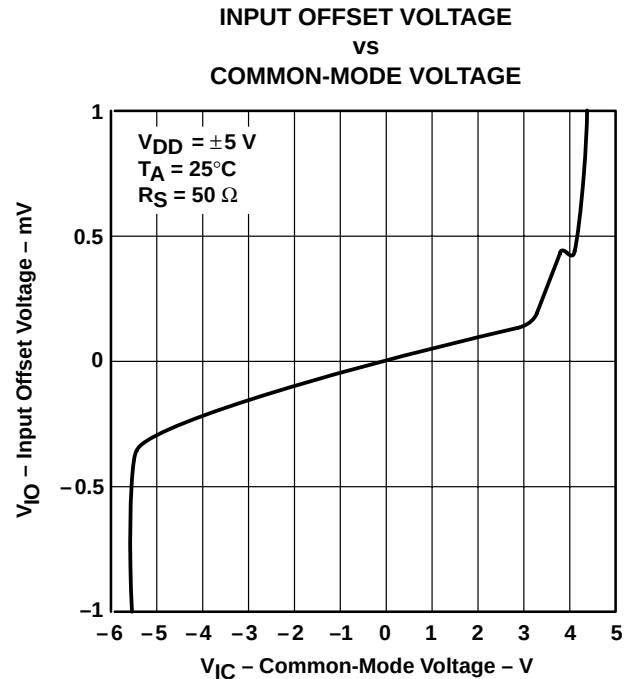


Figure 6

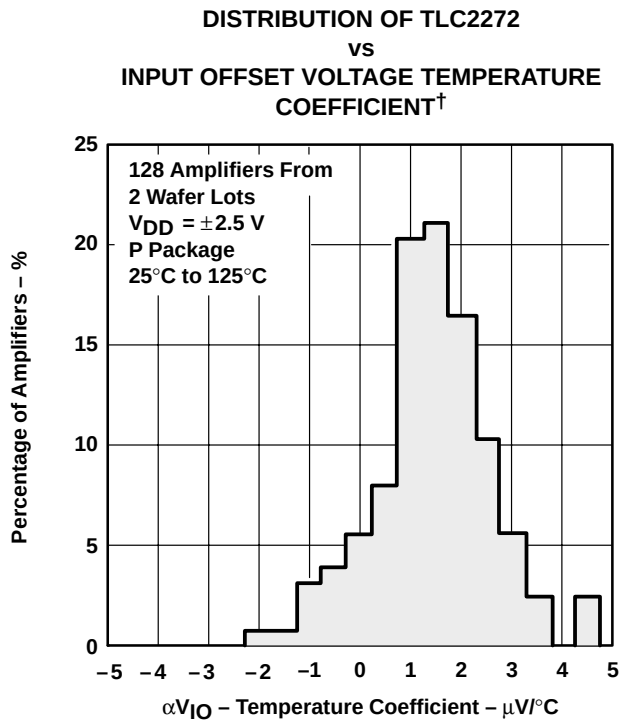


Figure 7

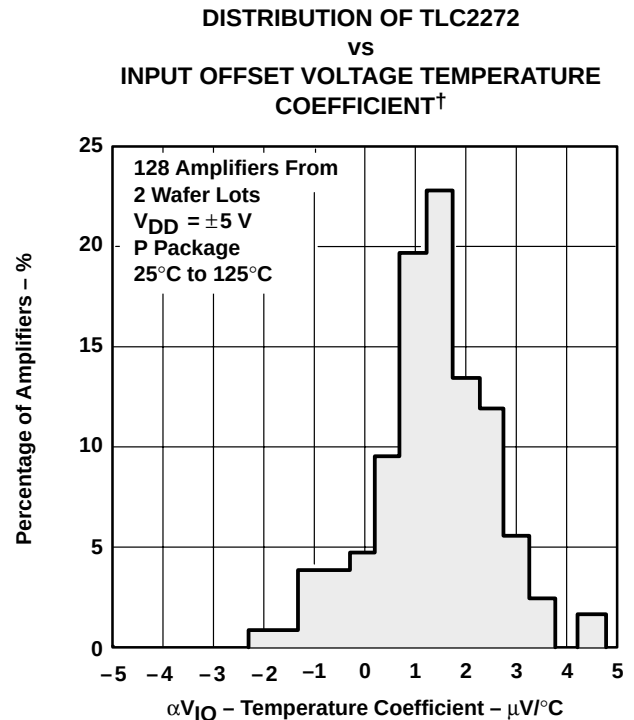
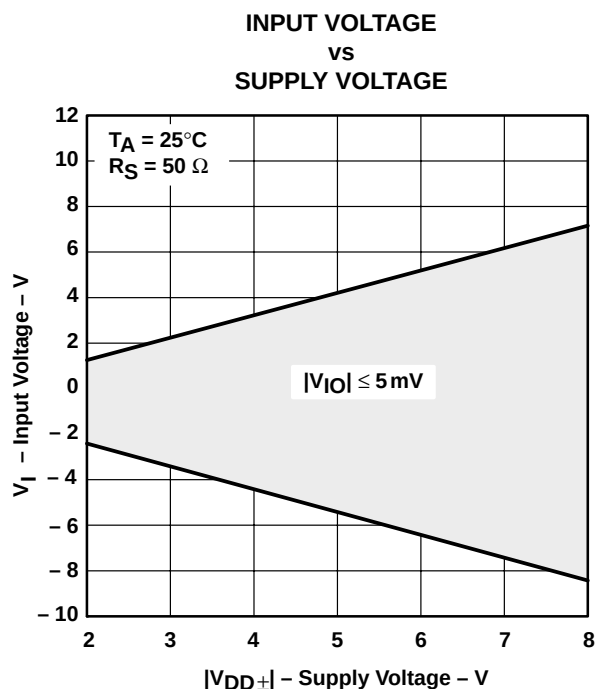
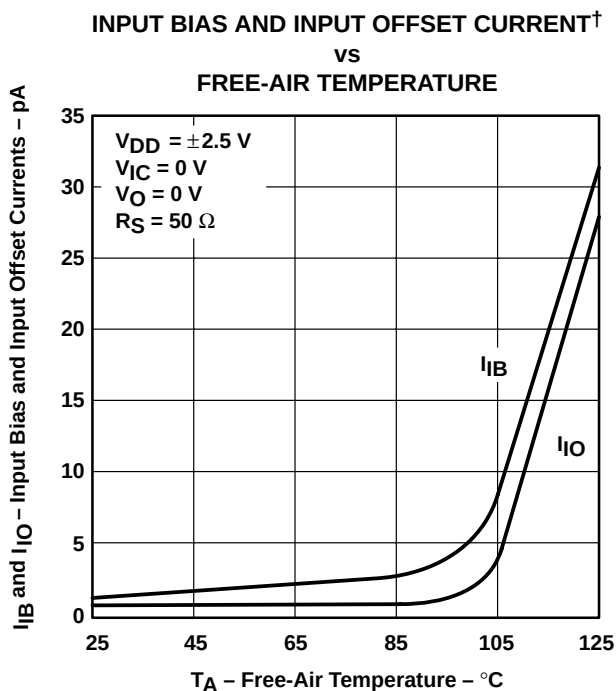
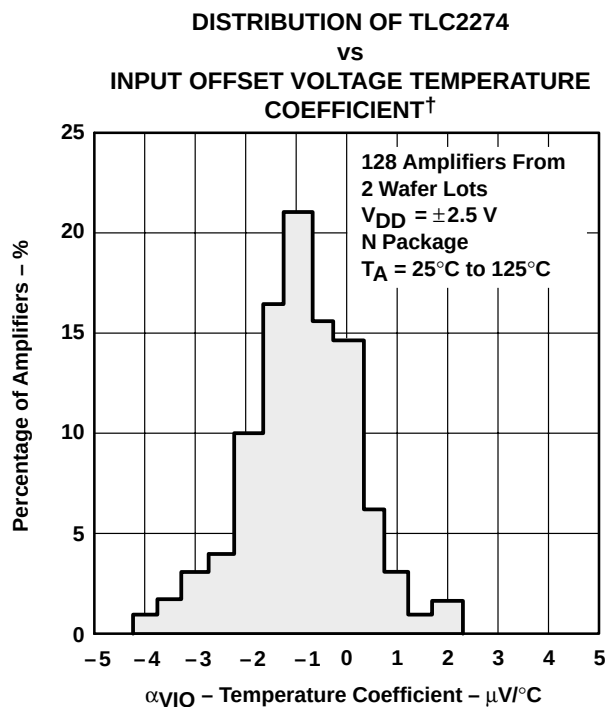
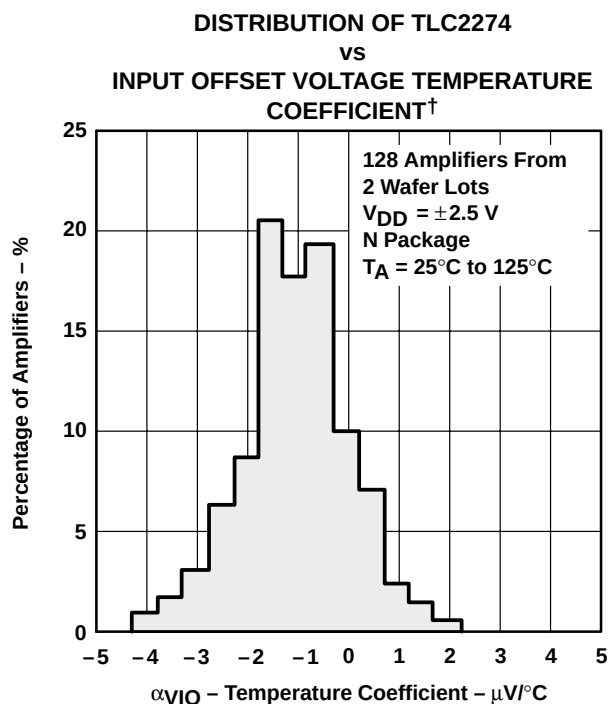


Figure 8

† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

TYPICAL CHARACTERISTICS



† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

TYPICAL CHARACTERISTICS

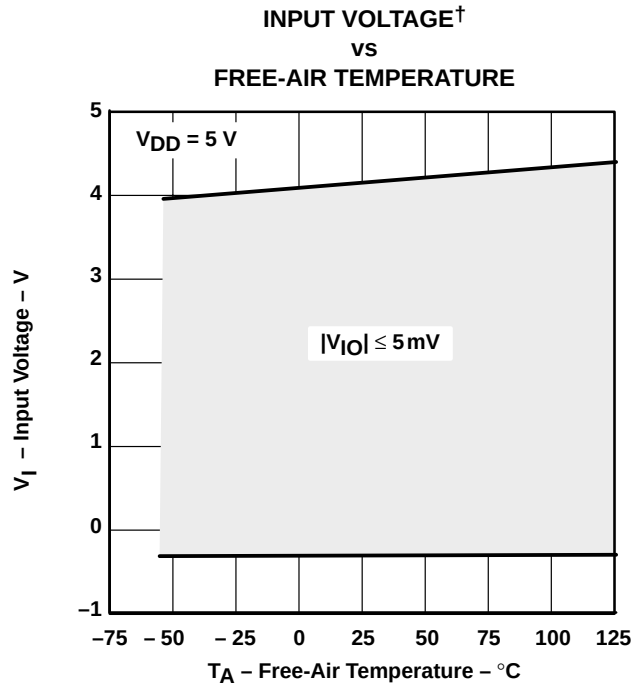


Figure 13

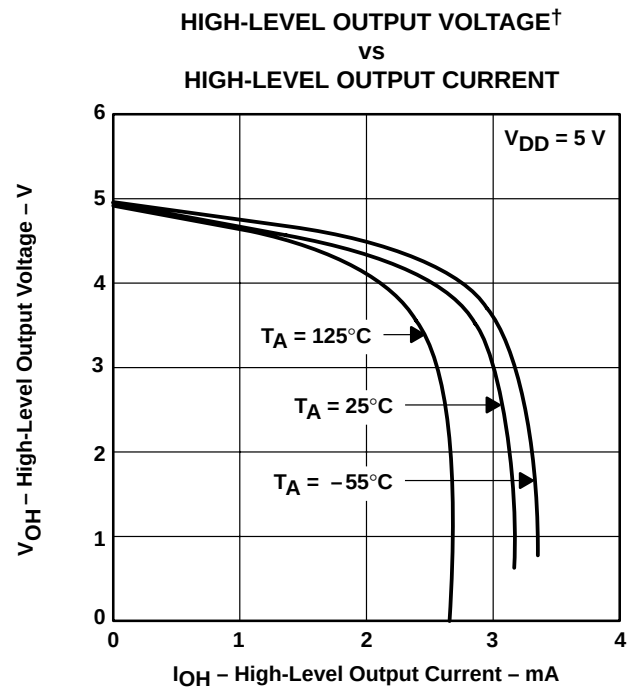


Figure 14

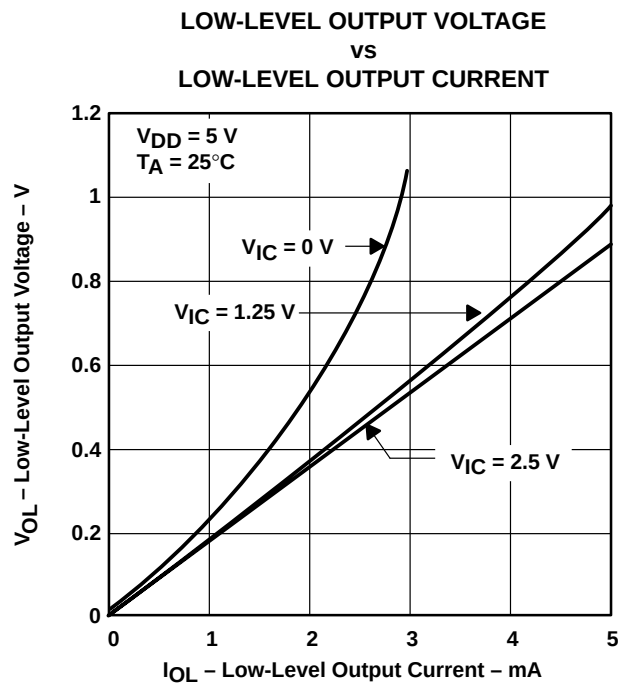


Figure 15

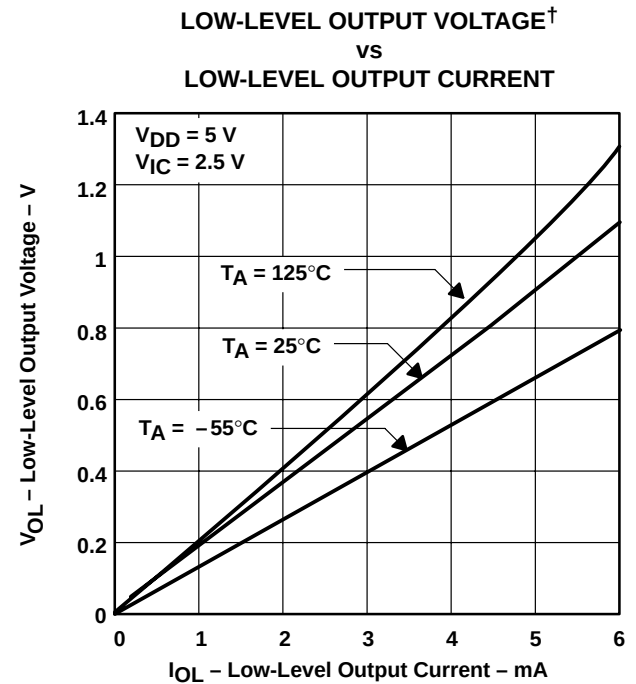


Figure 16

[†] Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

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TYPICAL CHARACTERISTICS

MAXIMUM POSITIVE PEAK OUTPUT VOLTAGE†
VS
OUTPUT CURRENT

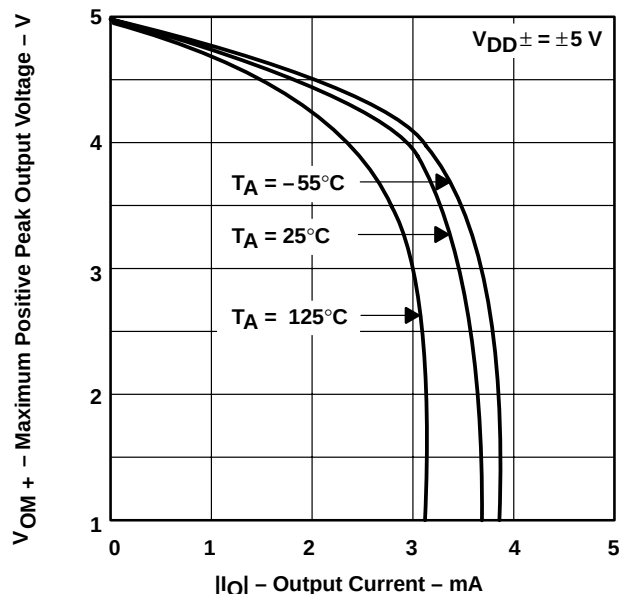


Figure 17

MAXIMUM NEGATIVE PEAK OUTPUT VOLTAGE†
VS
OUTPUT CURRENT

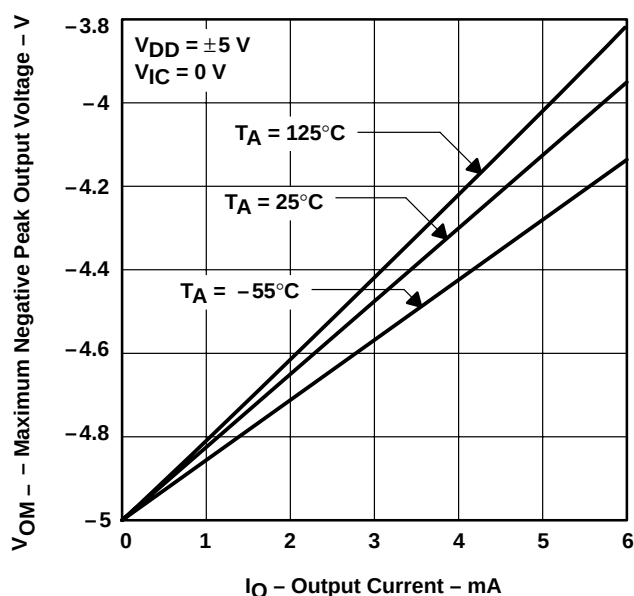


Figure 18

MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE
VS
FREQUENCY

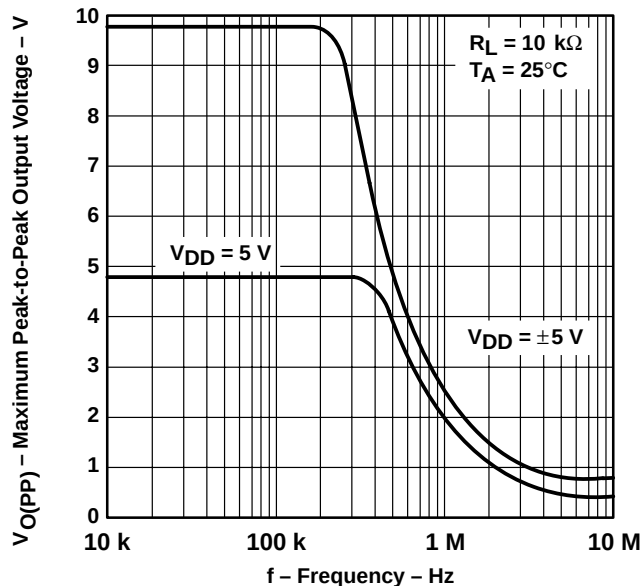


Figure 19

SHORT-CIRCUIT OUTPUT CURRENT
VS
SUPPLY VOLTAGE

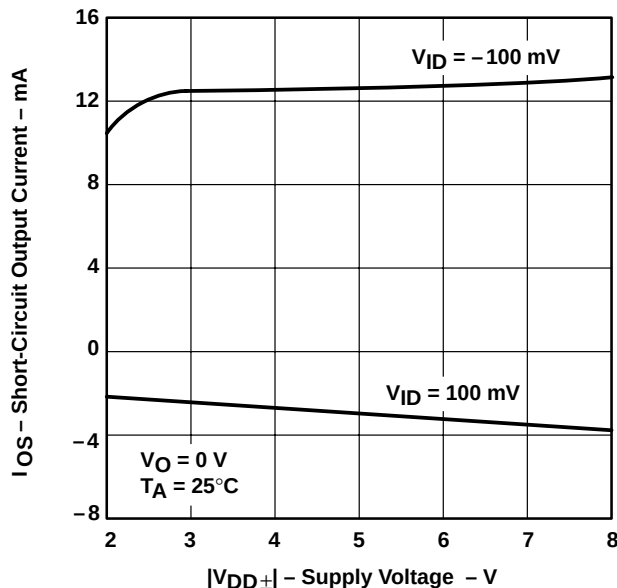
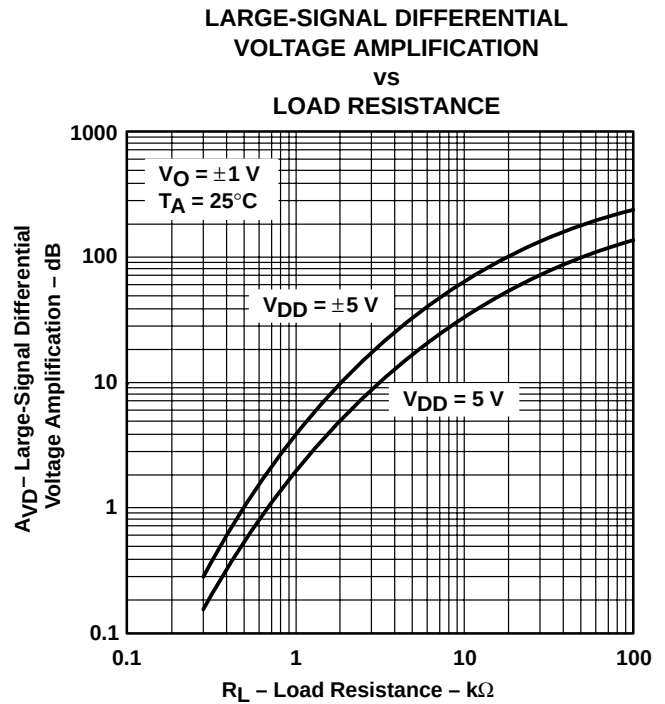
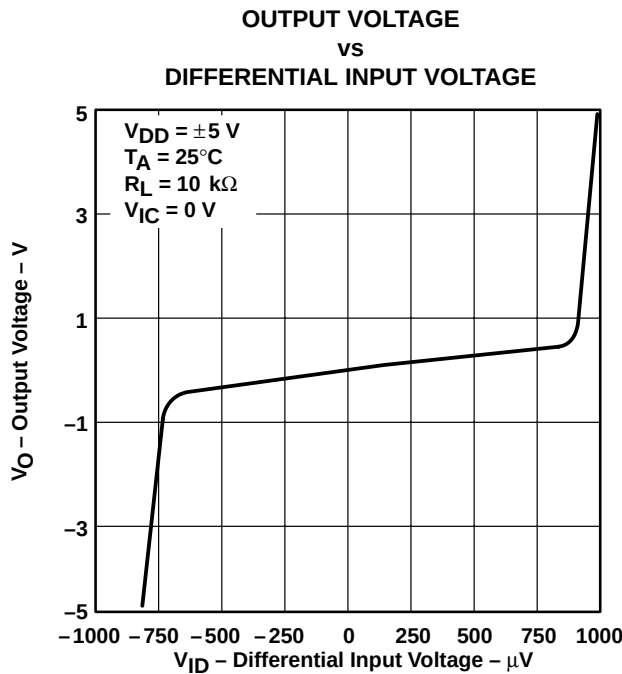
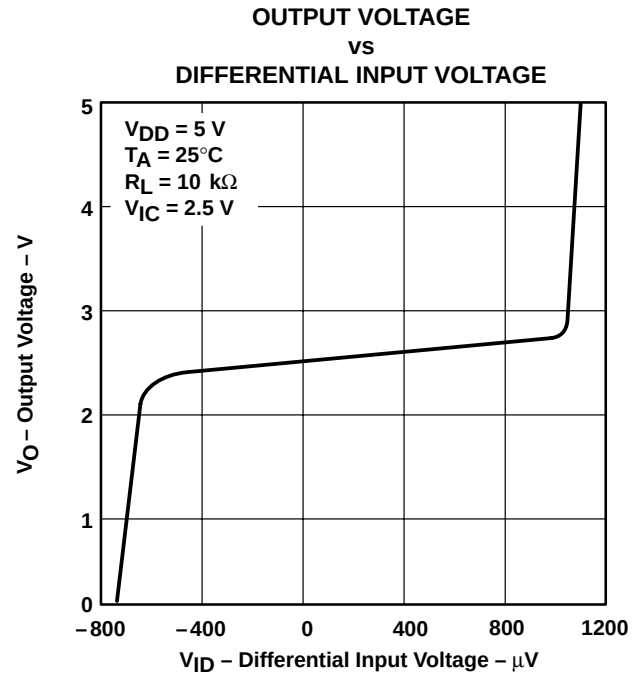
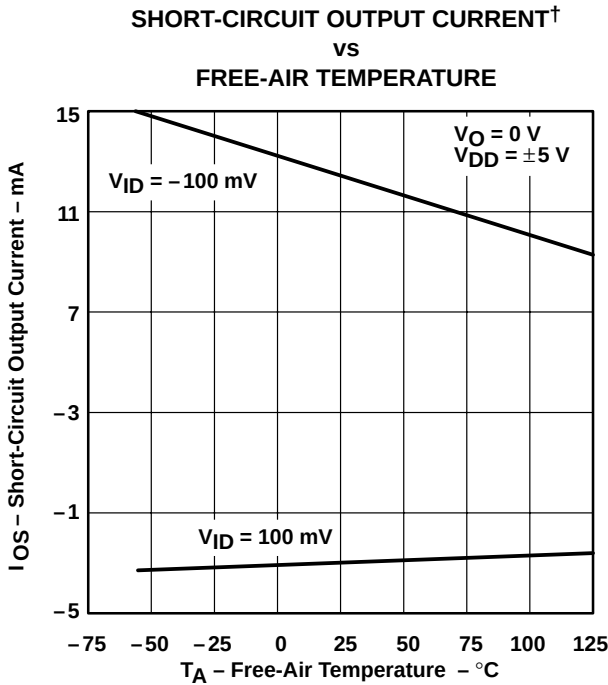


Figure 20

† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

TYPICAL CHARACTERISTICS



† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

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TYPICAL CHARACTERISTICS

**LARGE-SIGNAL DIFFERENTIAL VOLTAGE
AMPLIFICATION AND PHASE MARGIN**

**vs
FREQUENCY**

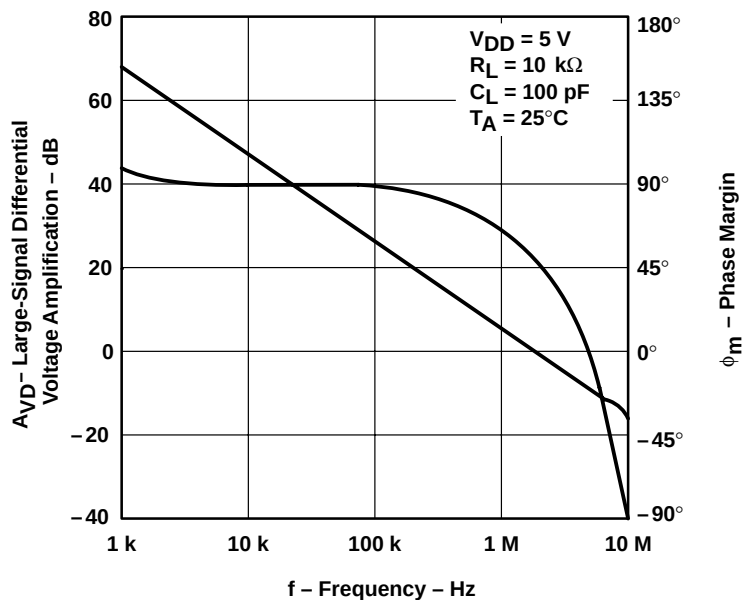


Figure 25

**LARGE-SIGNAL DIFFERENTIAL VOLTAGE
AMPLIFICATION AND PHASE MARGIN**

**vs
FREQUENCY**

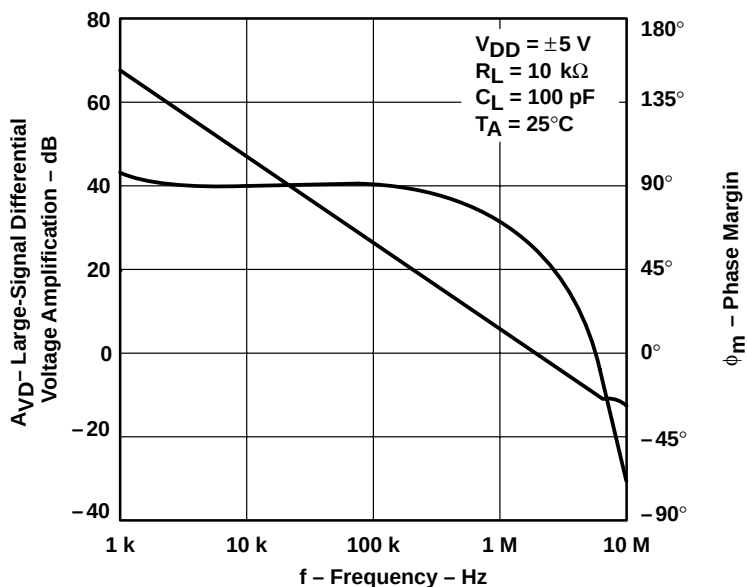


Figure 26



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TYPICAL CHARACTERISTICS

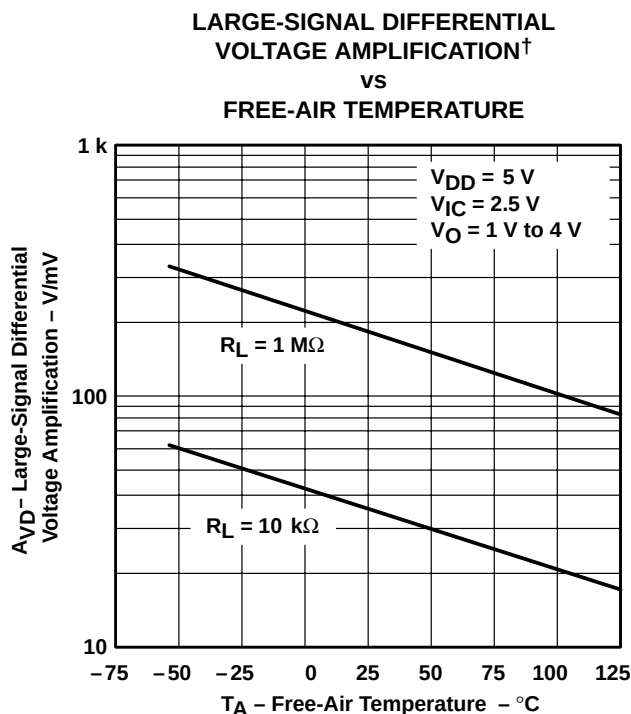


Figure 27

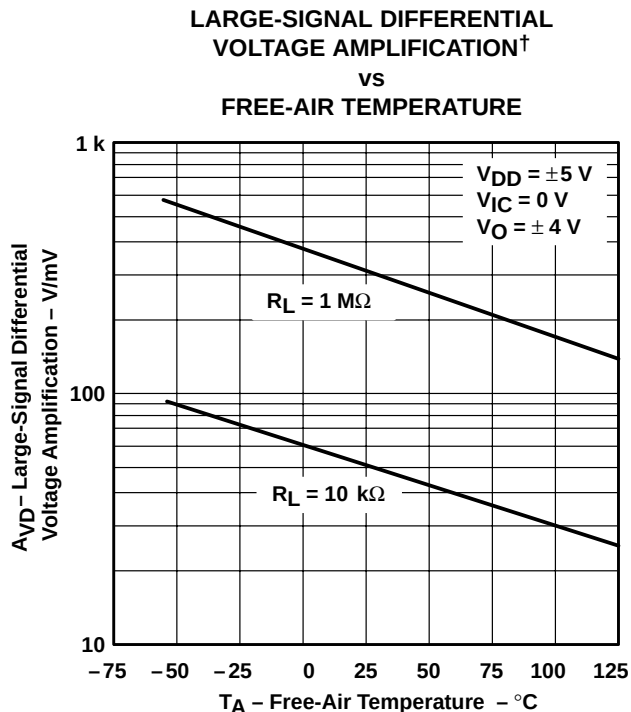


Figure 28

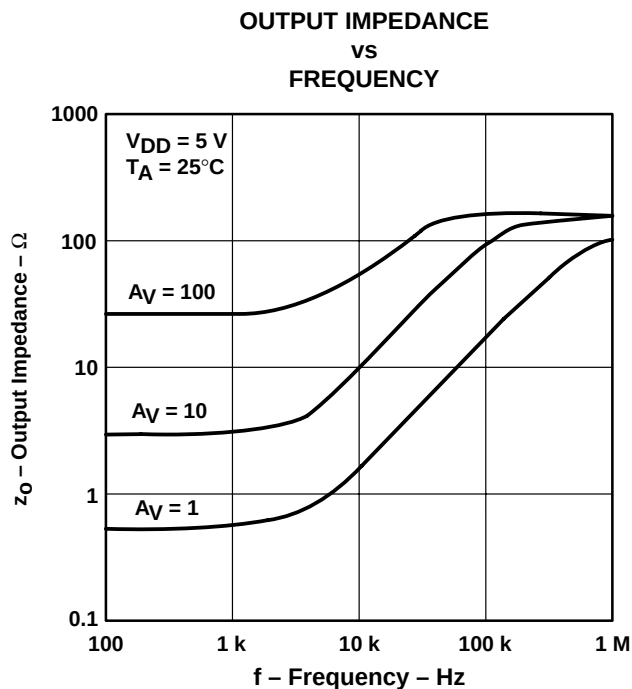


Figure 29

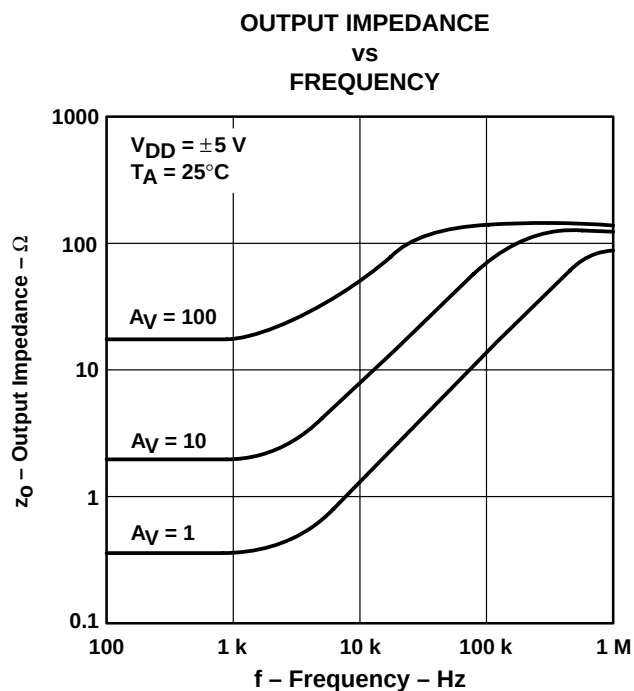


Figure 30

† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

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TYPICAL CHARACTERISTICS

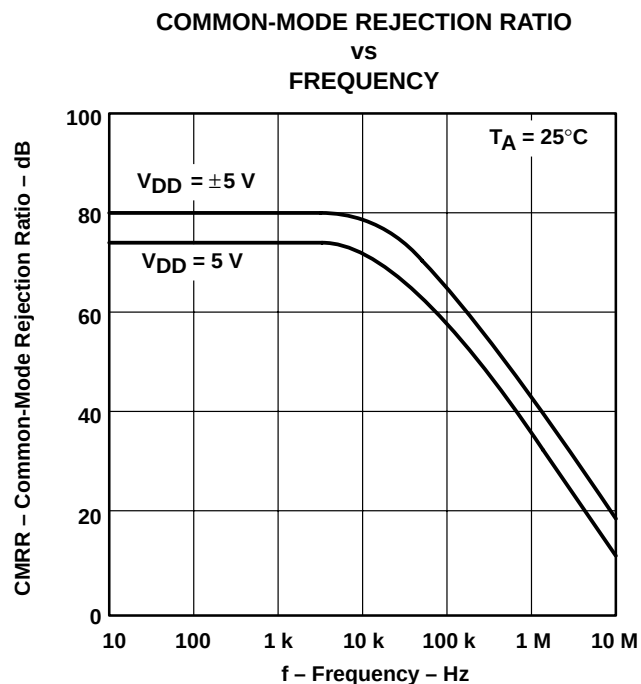


Figure 31

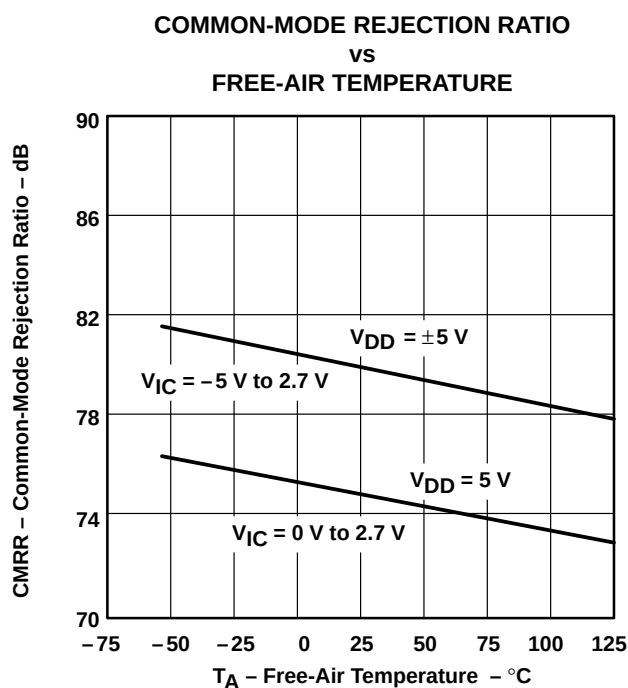


Figure 32

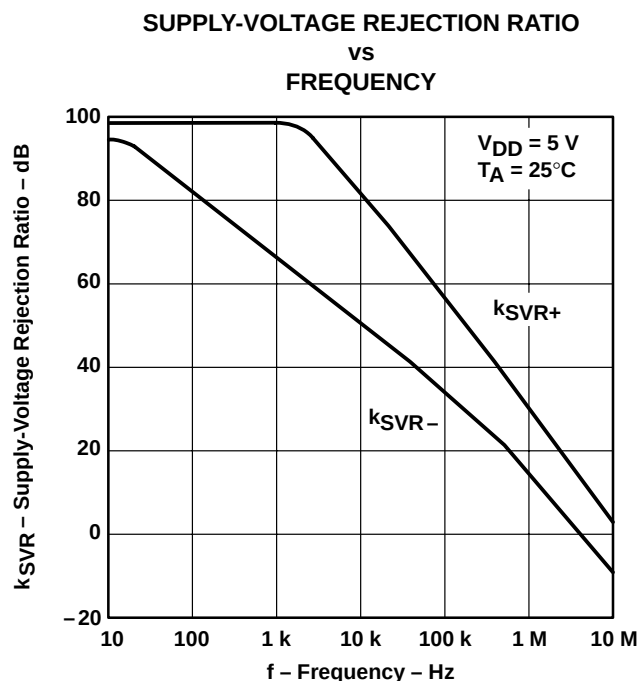


Figure 33

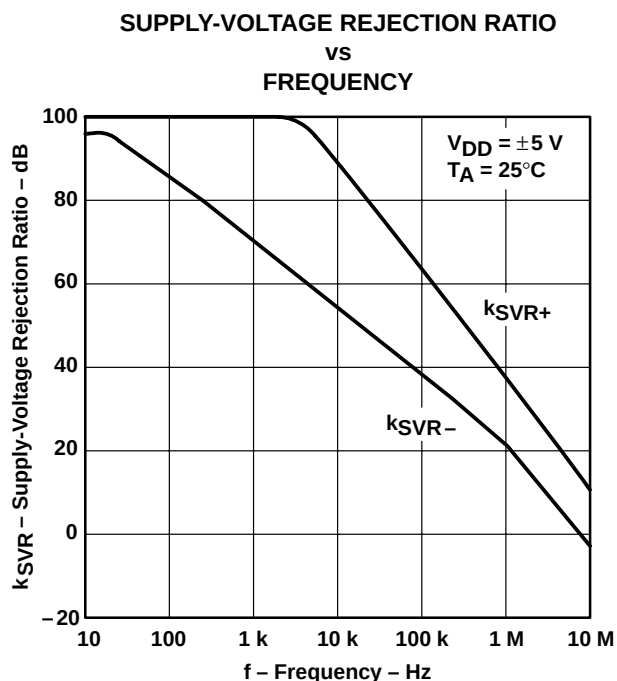


Figure 34

TYPICAL CHARACTERISTICS

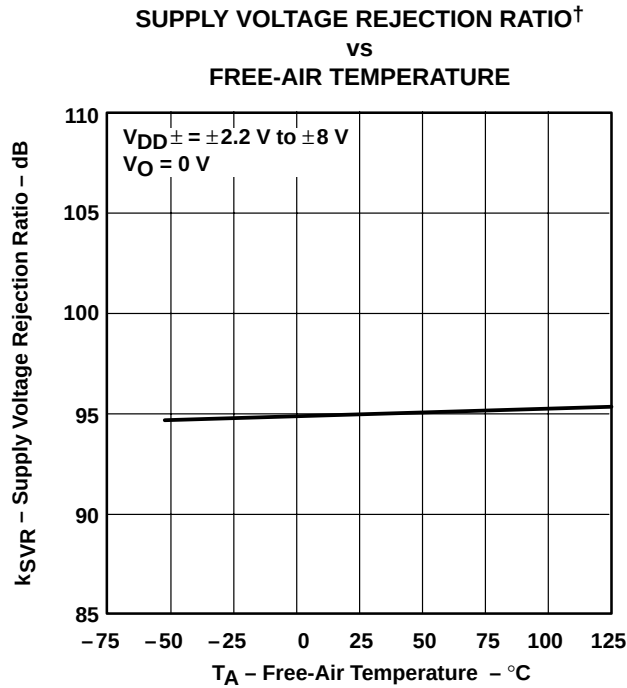


Figure 35

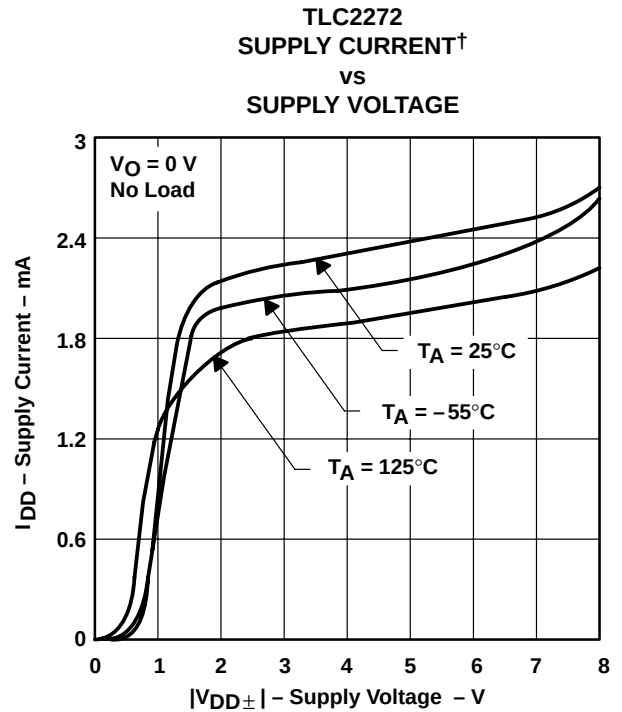


Figure 36

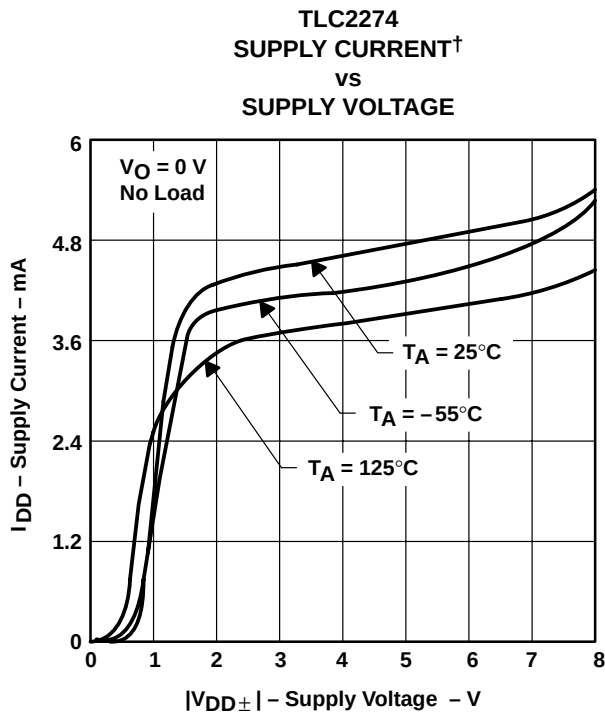


Figure 37

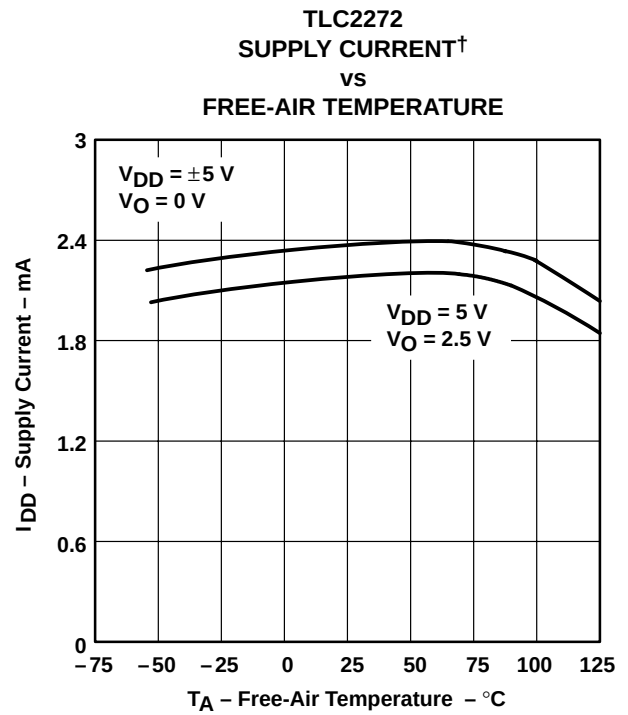


Figure 38

† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

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TYPICAL CHARACTERISTICS

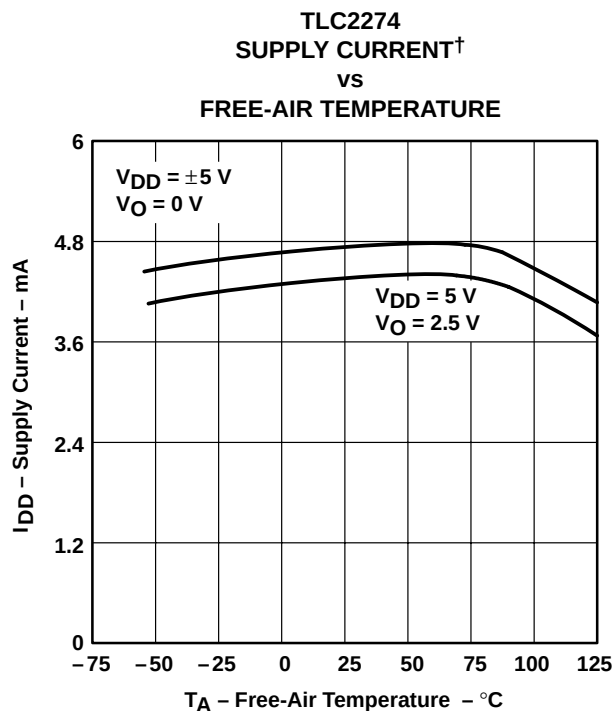


Figure 39

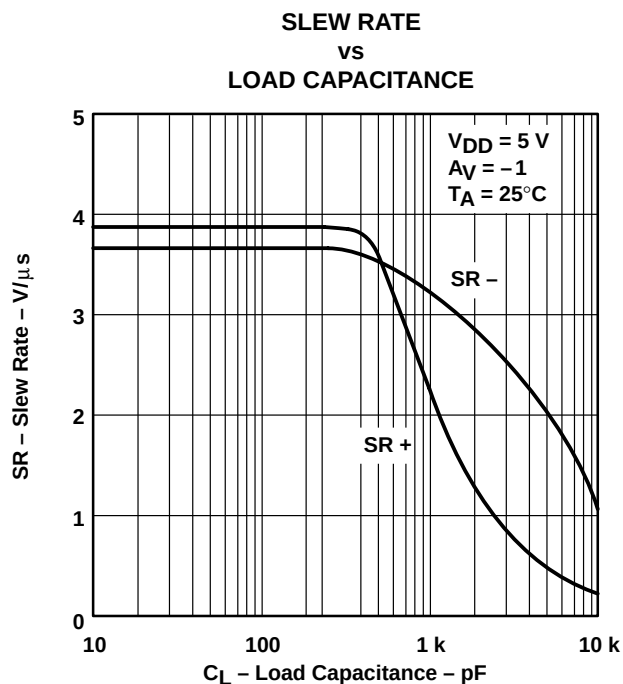


Figure 40

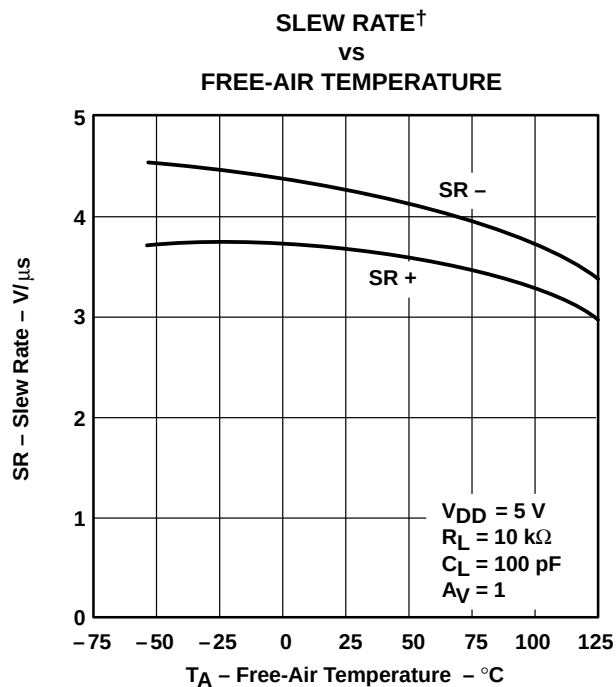


Figure 41

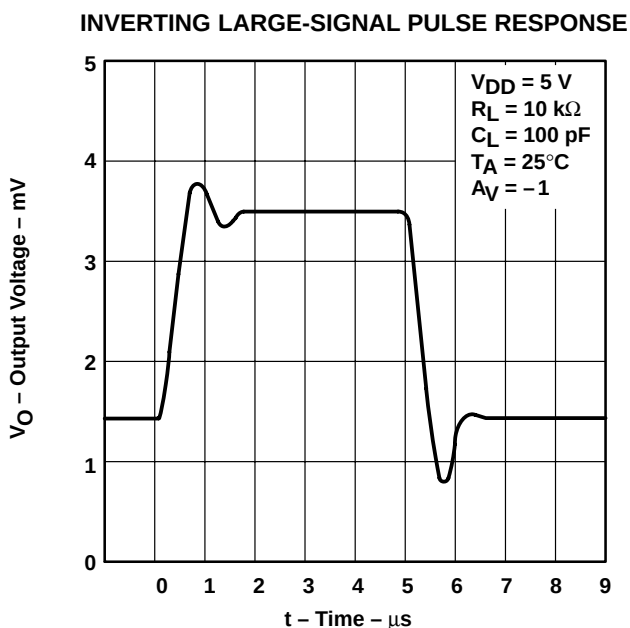


Figure 42

† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

TYPICAL CHARACTERISTICS

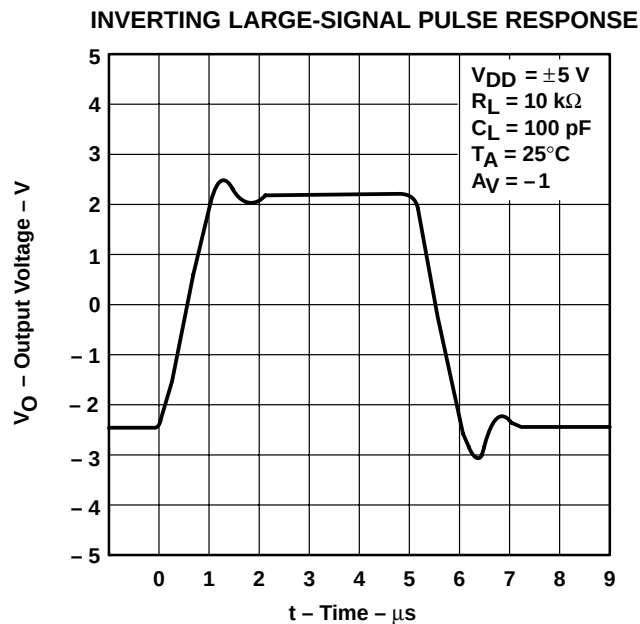


Figure 43

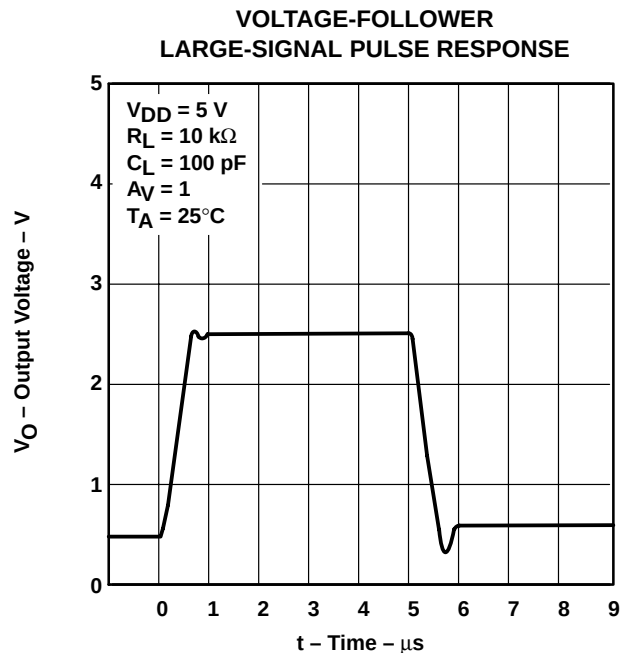


Figure 44

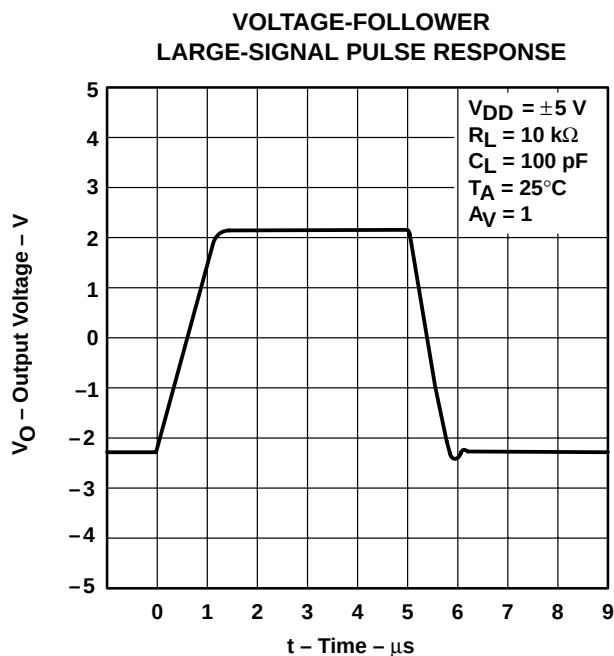


Figure 45

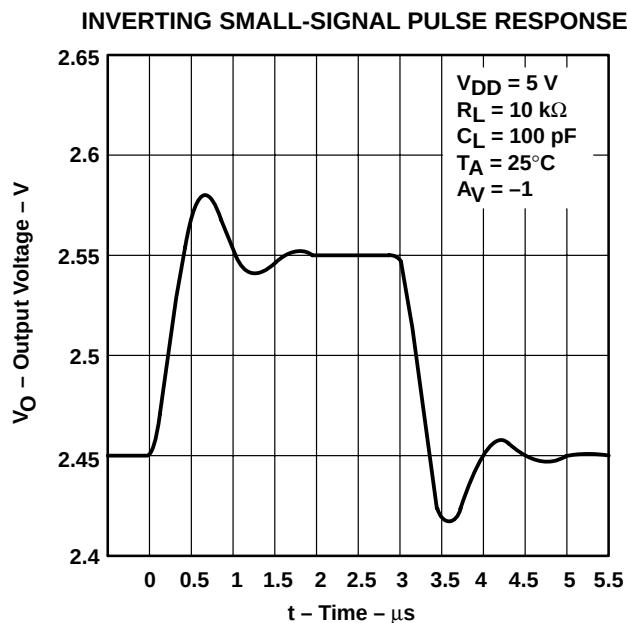


Figure 46

TLC227x, TLC227xA

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TYPICAL CHARACTERISTICS

INVERTING SMALL-SIGNAL PULSE RESPONSE

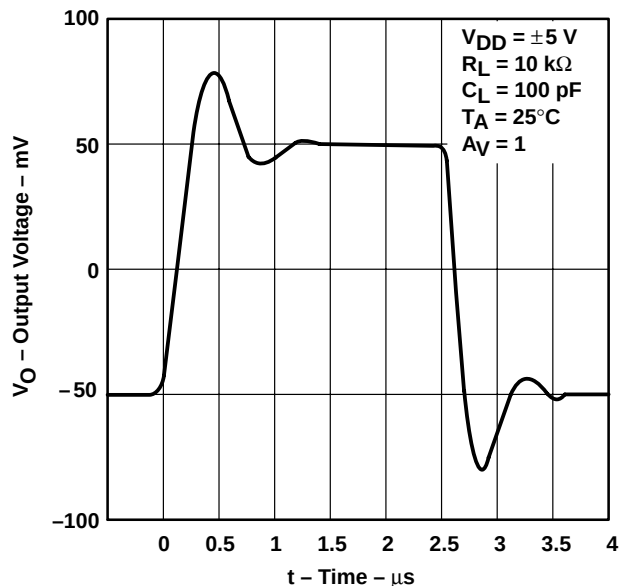


Figure 47

VOLTAGE-FOLLOWER
SMALL-SIGNAL PULSE RESPONSE

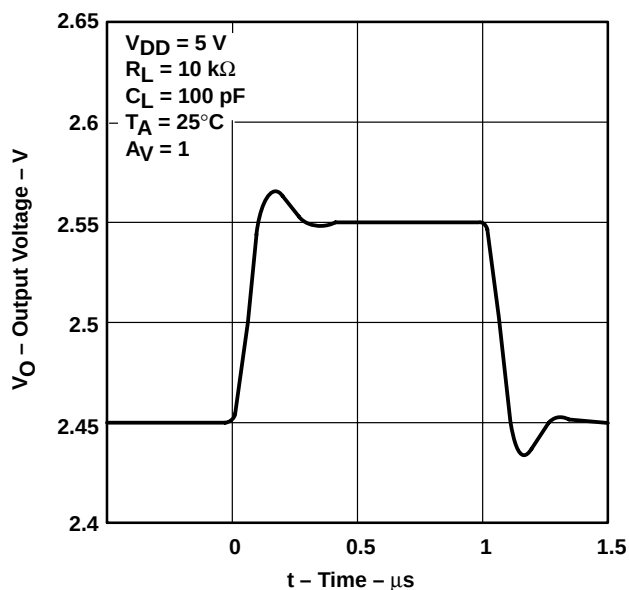


Figure 48

VOLTAGE-FOLLOWER
SMALL-SIGNAL PULSE RESPONSE

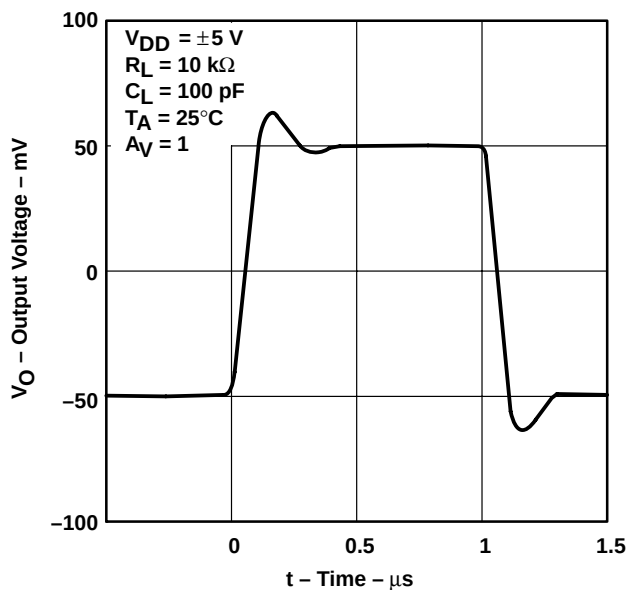


Figure 49

EQUIVALENT INPUT NOISE VOLTAGE
VS
FREQUENCY

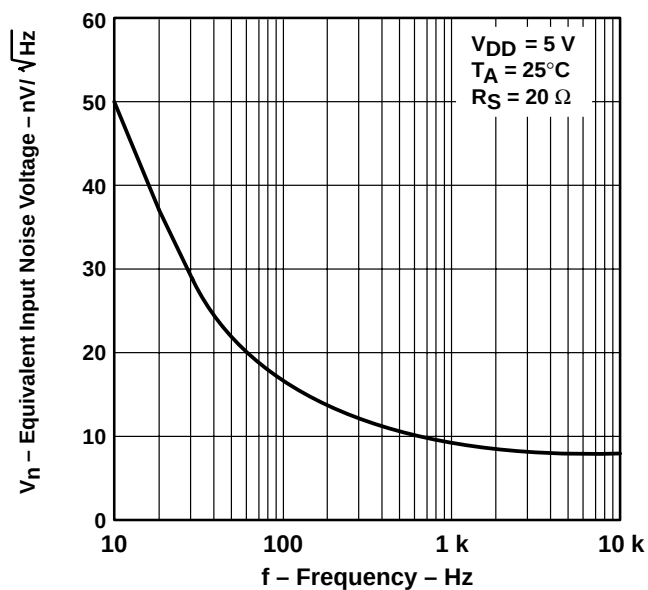


Figure 50

TYPICAL CHARACTERISTICS

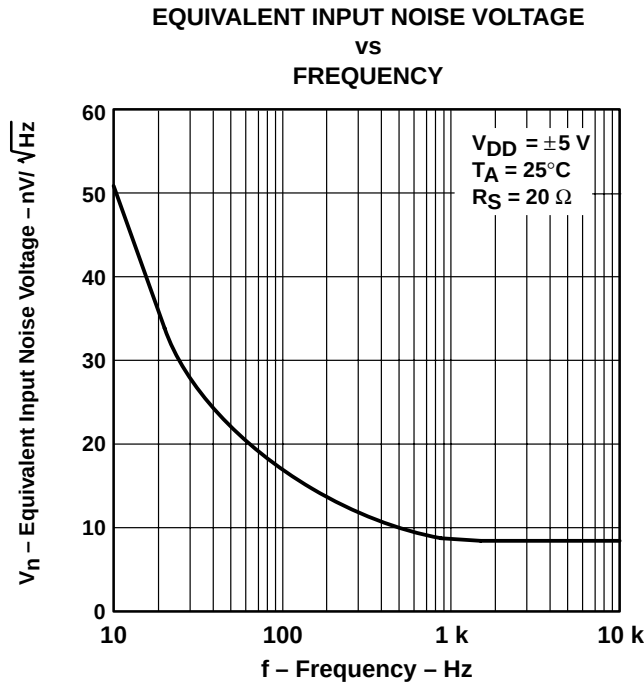


Figure 51

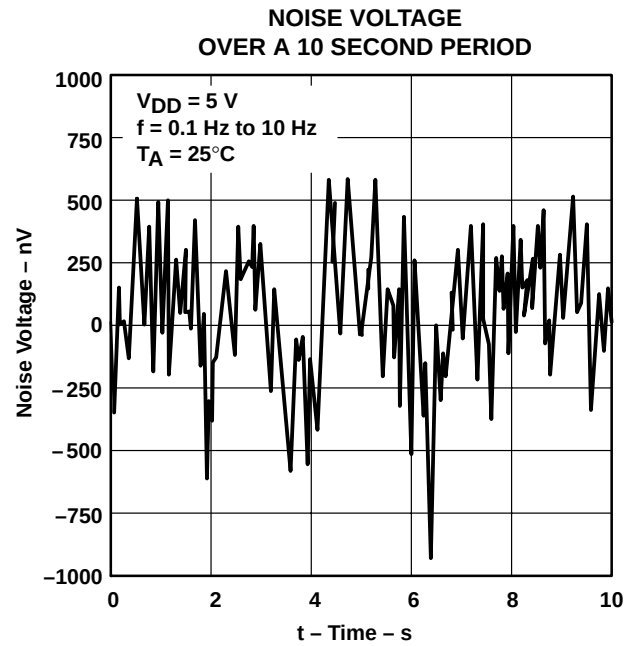


Figure 52

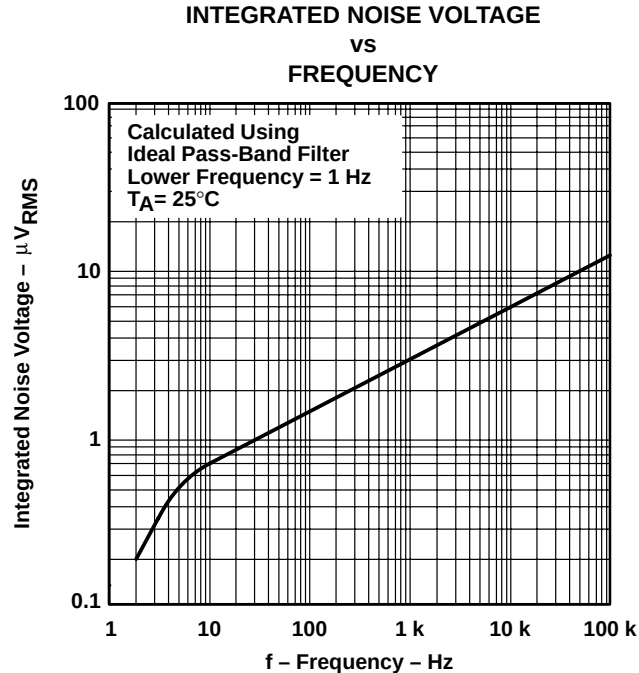


Figure 53

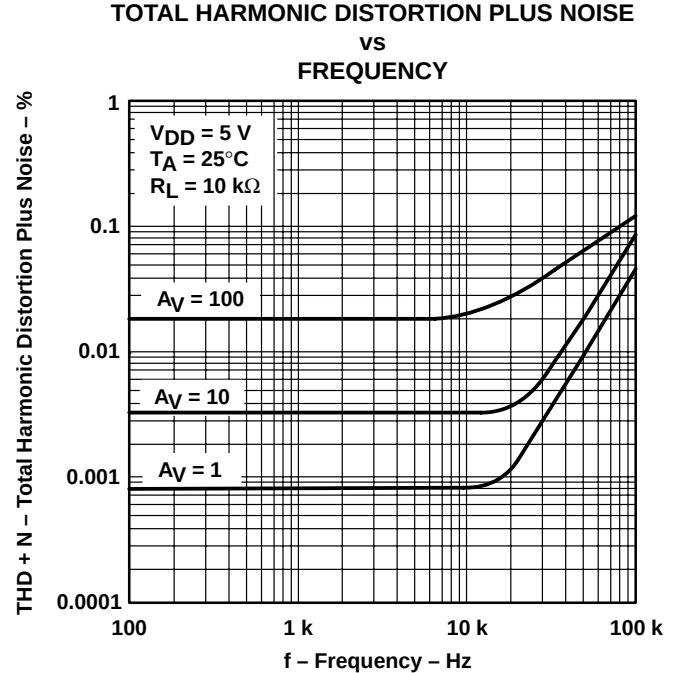


Figure 54

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TYPICAL CHARACTERISTICS

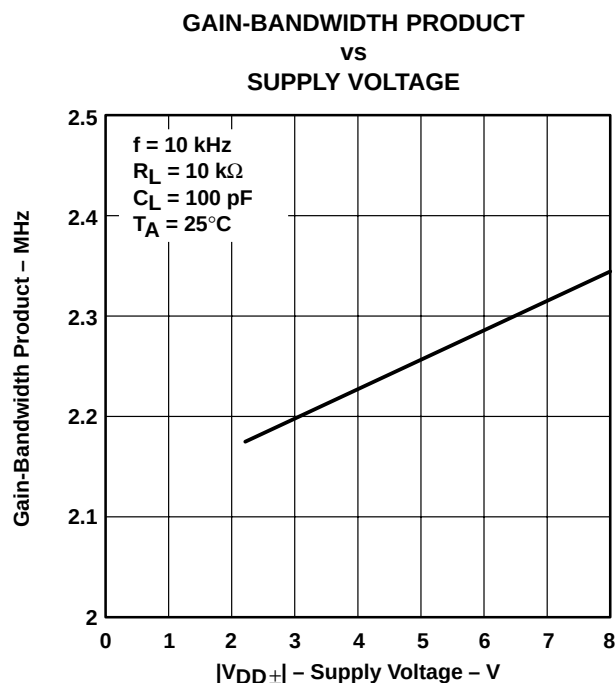


Figure 55

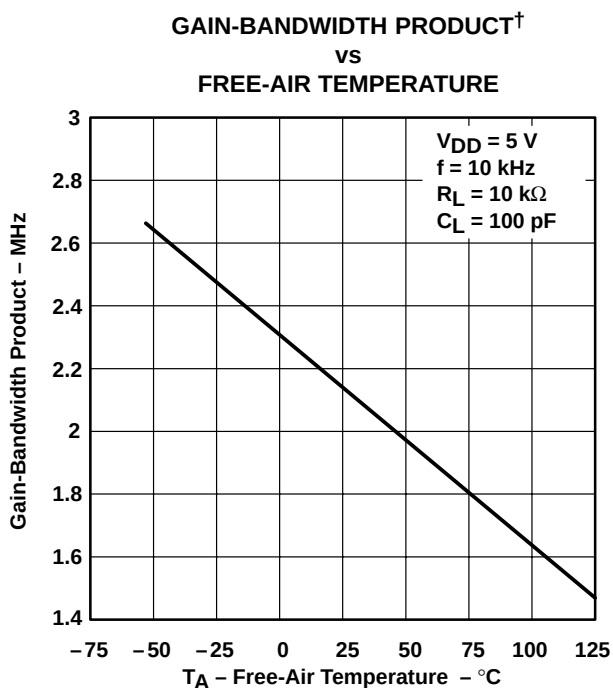


Figure 56

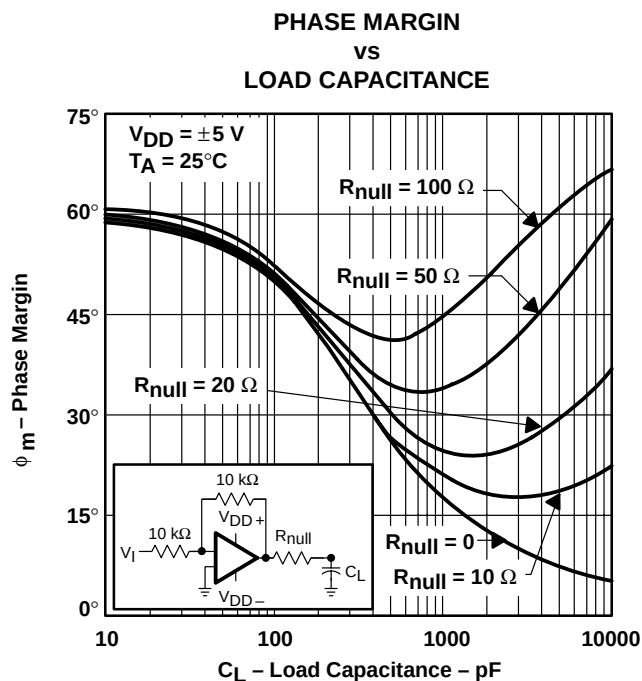


Figure 57

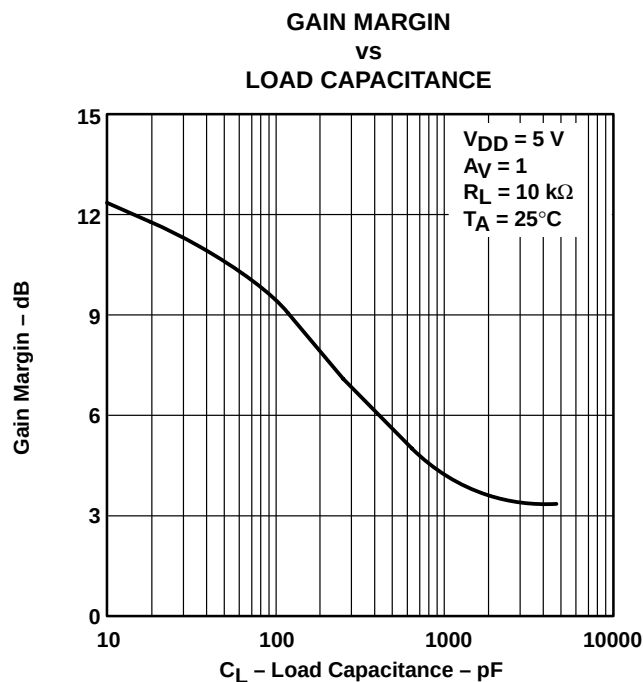


Figure 58

† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

APPLICATION INFORMATION

macromodel information

Macromodel information provided was derived using Microsim *Parts*™, the model generation software used with Microsim *PSpice*™. The Boyle macromodel (see Note 5) and subcircuit in Figure 59 were generated using the TLC227x typical electrical and operating characteristics at $T_A = 25^\circ\text{C}$. Using this information, output simulations of the following key parameters can be generated to a tolerance of 20% (in most cases):

- Maximum positive output voltage swing
- Maximum negative output voltage swing
- Slew rate
- Quiescent power dissipation
- Input bias current
- Open-loop voltage amplification
- Unity gain frequency
- Common-mode rejection ratio
- Phase margin
- DC output resistance
- AC output resistance
- Short-circuit output current limit

NOTE 5: G. R. Boyle, B. M. Cohn, D. O. Pederson, and J. E. Solomon, "Macromodeling of Integrated Circuit Operational Amplifiers", *IEEE Journal of Solid-State Circuits*, SC-9, 353 (1974).

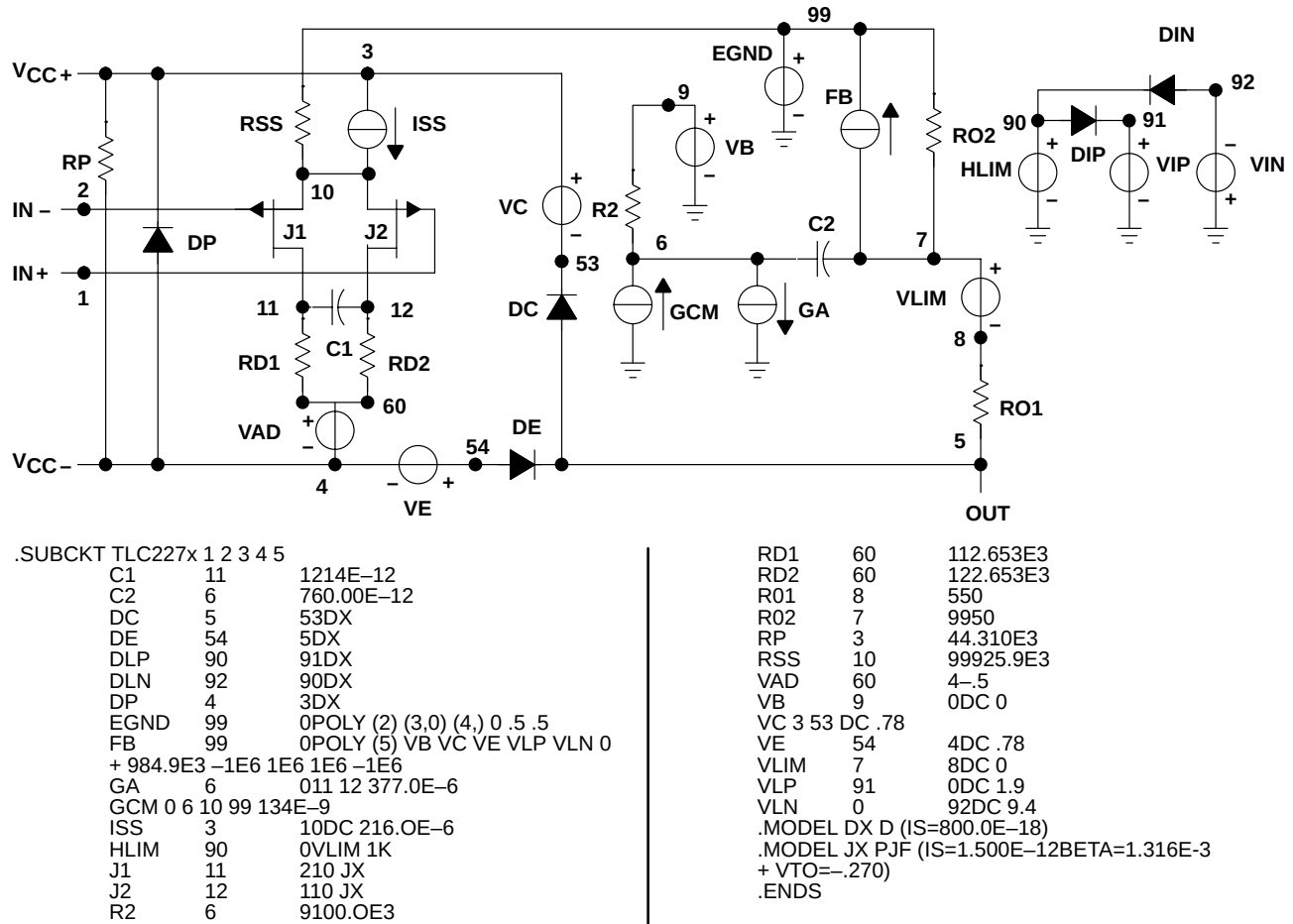


Figure 59. Boyle Macromodel and Subcircuit

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Macromodels, simulation models, or other models provided by TI, directly or indirectly, are not warranted by TI as fully representing all of the specification and operating characteristics of the semiconductor product to which the model relates.



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OPERATIONAL AMPLIFIERS

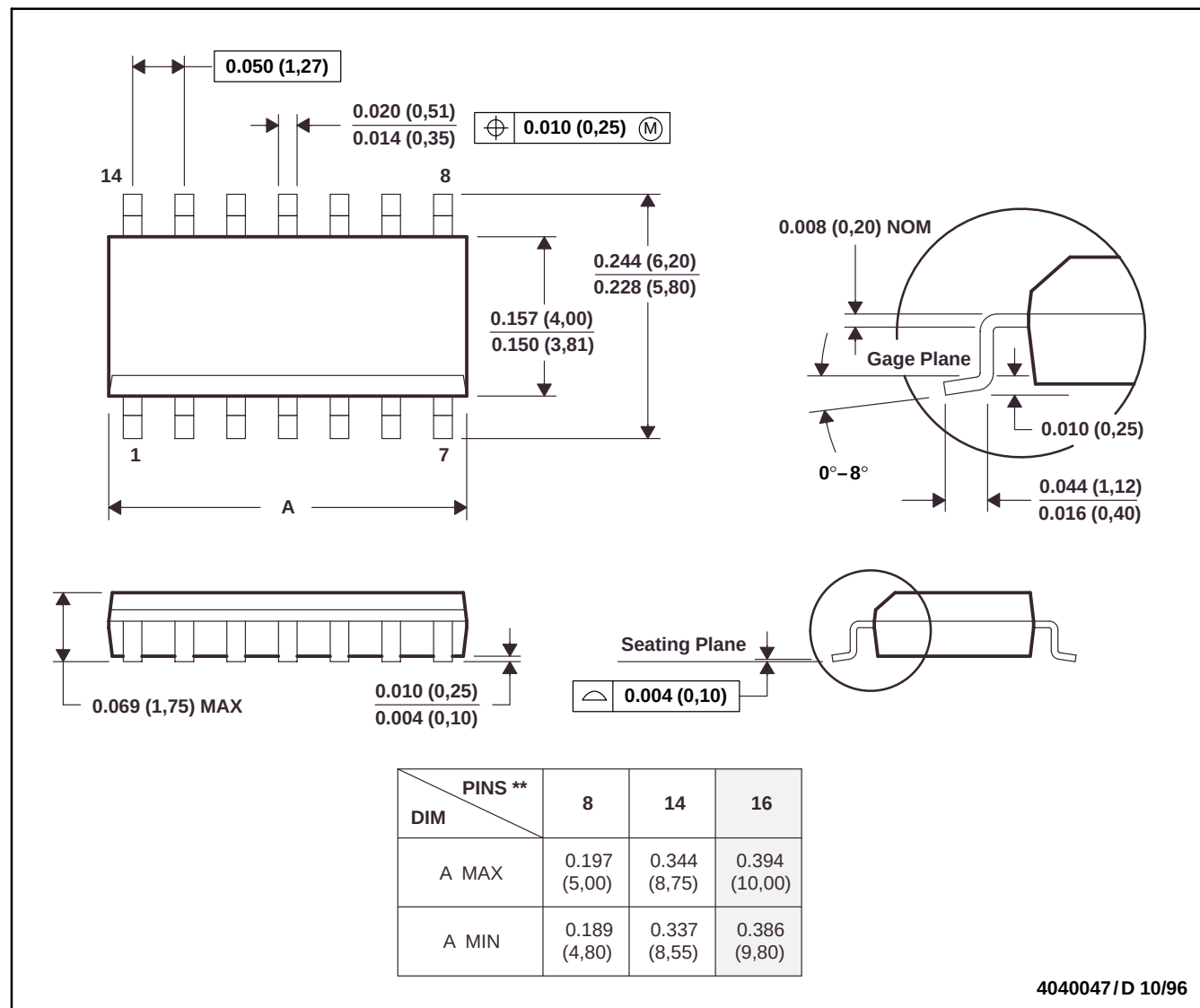
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MECHANICAL DATA

D (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PIN SHOWN



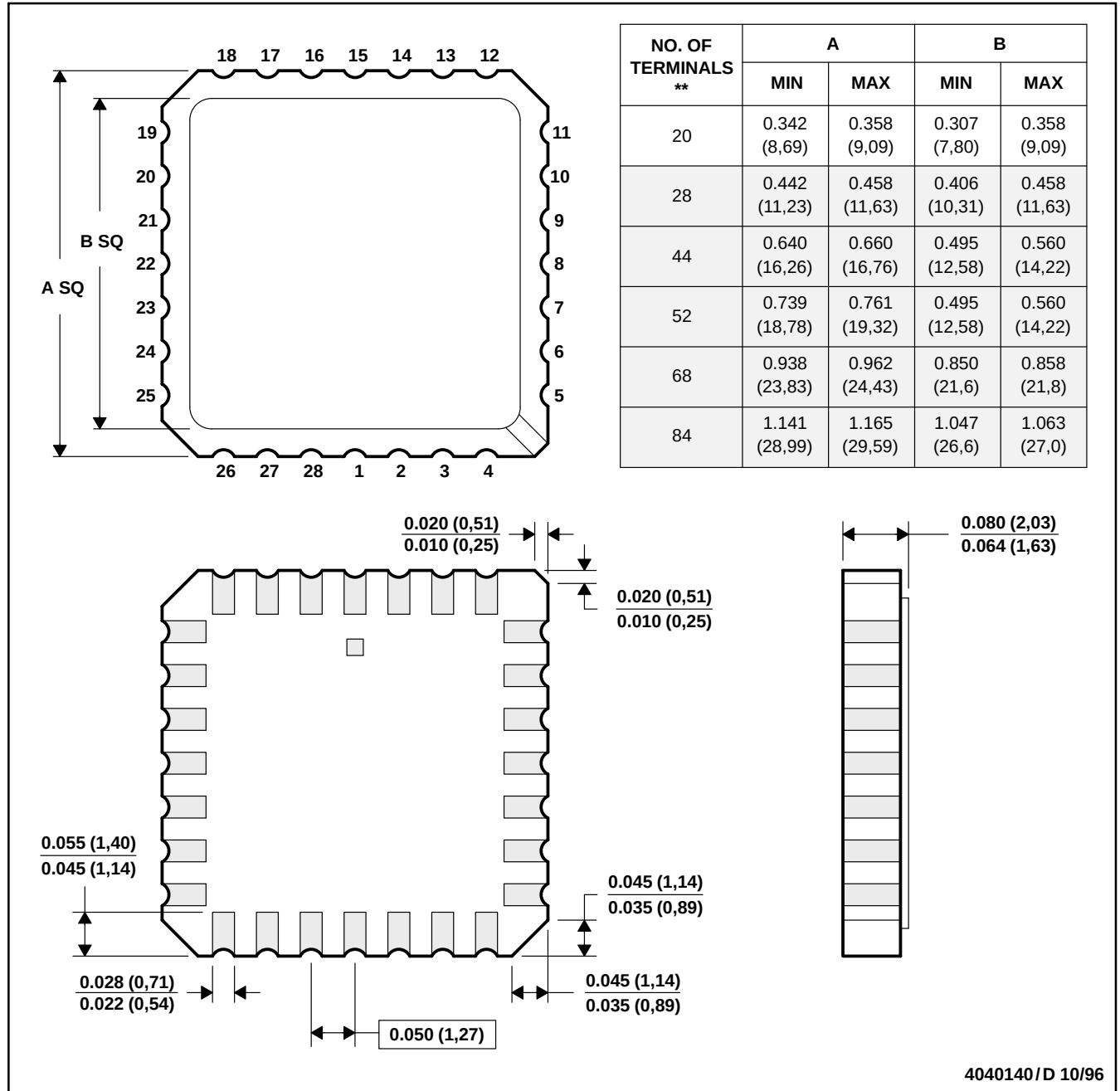
- NOTES:
- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).
 - D. Falls within JEDEC MS-012

MECHANICAL DATA

FK (S-CQCC-N)**

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



- NOTES:
- A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. This package can be hermetically sealed with a metal lid.
 - D. The terminals are gold plated.
 - E. Falls within JEDEC MS-004

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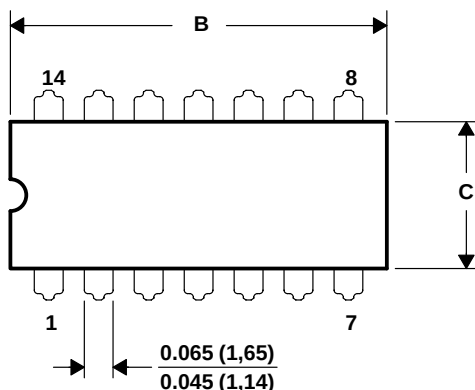
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MECHANICAL DATA

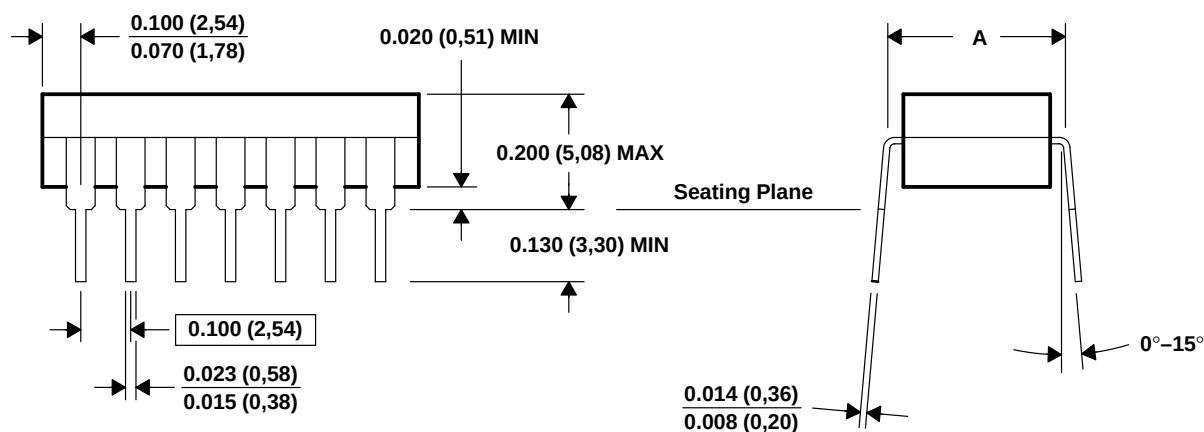
J (R-GDIP-T**)

CERAMIC DUAL-IN-LINE PACKAGE

14 PIN SHOWN



DIM \ PINS **	14	16	18	20
A MAX	0.310 (7,87)	0.310 (7,87)	0.310 (7,87)	0.310 (7,87)
A MIN	0.290 (7,37)	0.290 (7,37)	0.290 (7,37)	0.290 (7,37)
B MAX	0.785 (19,94)	0.785 (19,94)	0.910 (23,10)	0.975 (24,77)
B MIN	0.755 (19,18)	0.755 (19,18)	—	0.930 (23,62)
C MAX	0.300 (7,62)	0.300 (7,62)	0.300 (7,62)	0.300 (7,62)
C MIN	0.245 (6,22)	0.245 (6,22)	0.245 (6,22)	0.245 (6,22)



4040083/D 08/98

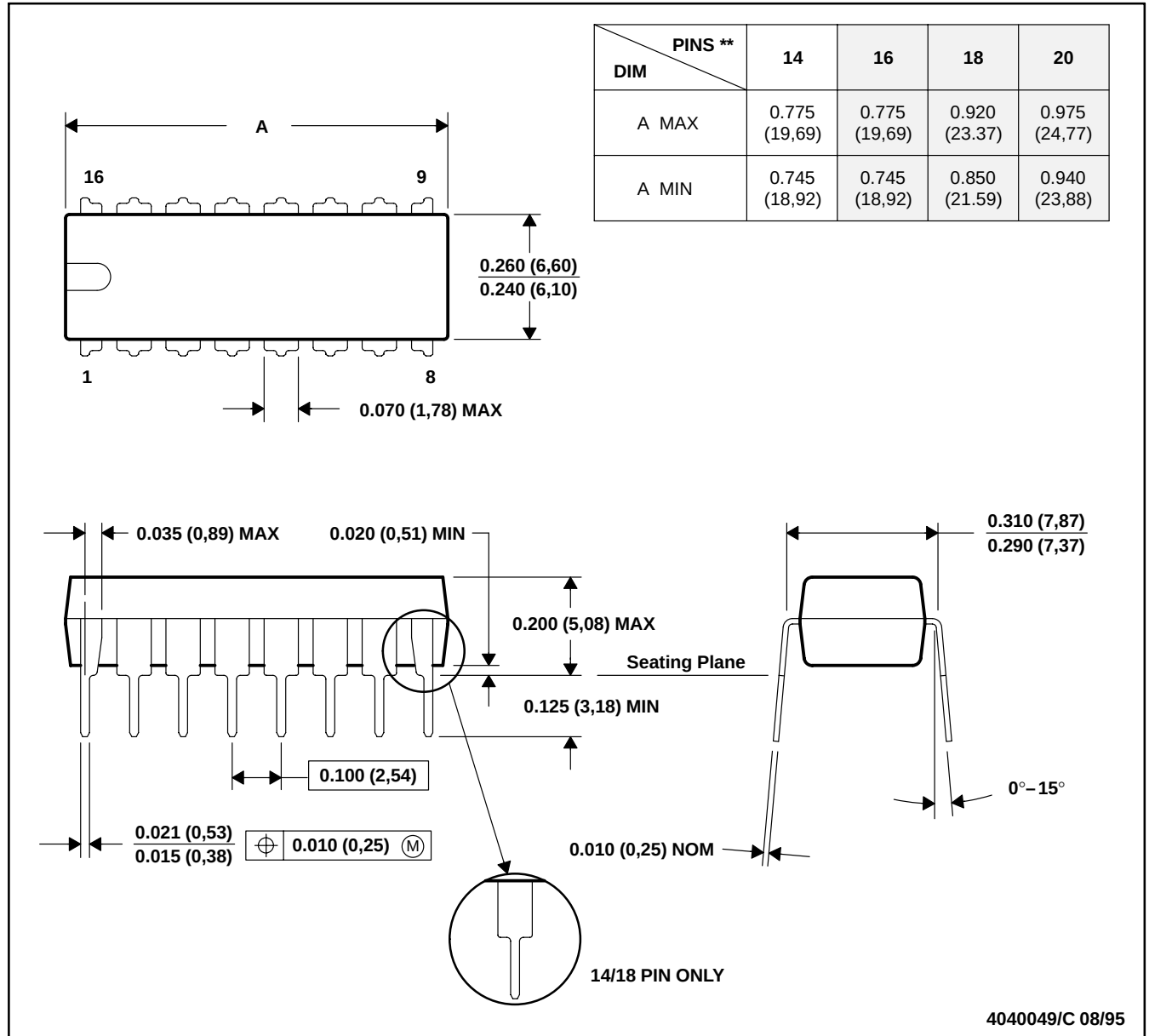
- NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. This package can be hermetically sealed with a ceramic lid using glass frit.
 D. Index point is provided on cap for terminal identification on press ceramic glass frit seal.
 E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18, GDIP1-T20, and GDIP1-T22.

MECHANICAL DATA

N (R-PDIP-T)**

PLASTIC DUAL-IN-LINE PACKAGE

16 PIN SHOWN



- NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Falls within JEDEC MS-001 (20 pin package is shorter than MS-001.)

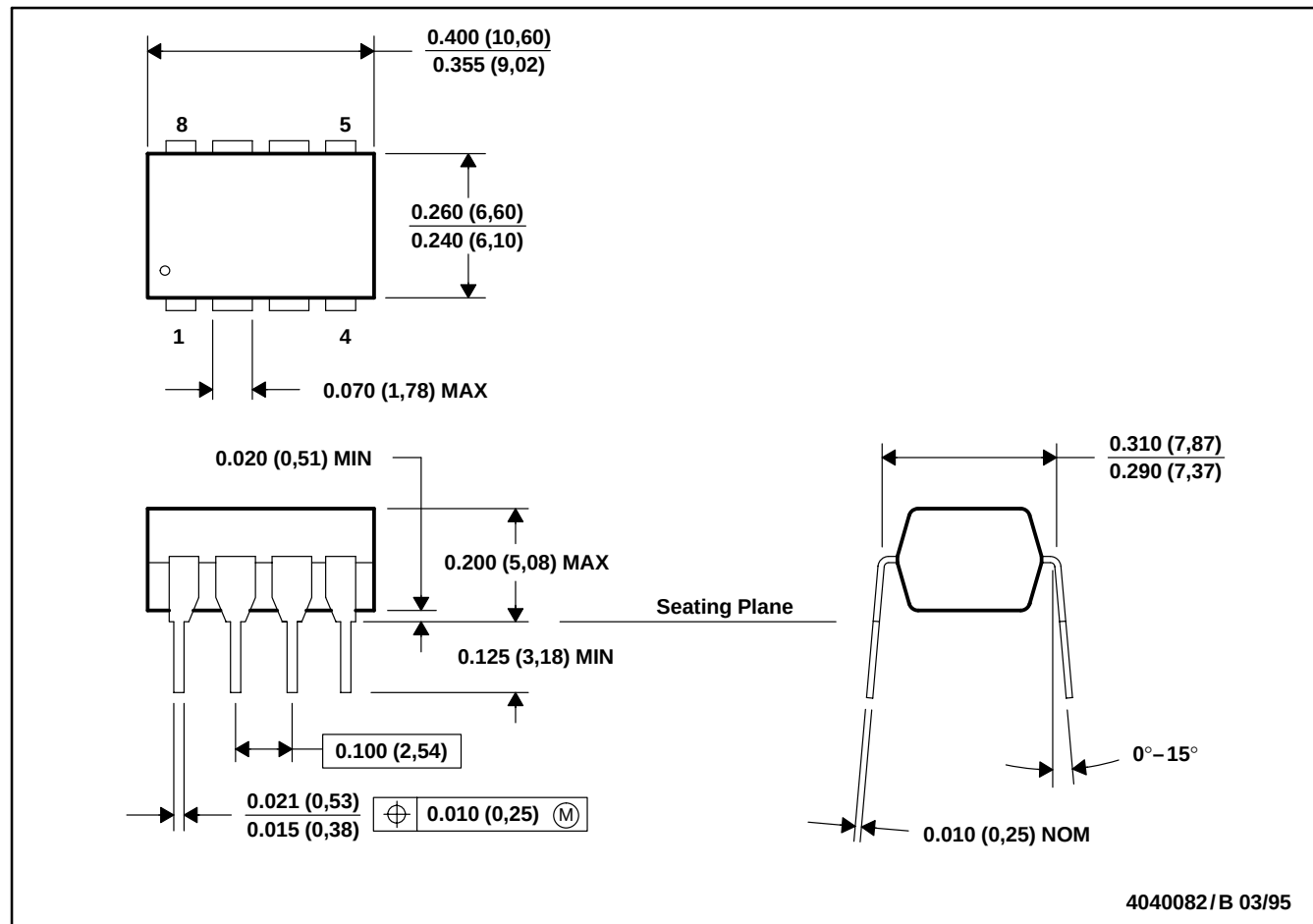
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MECHANICAL DATA

P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE PACKAGE



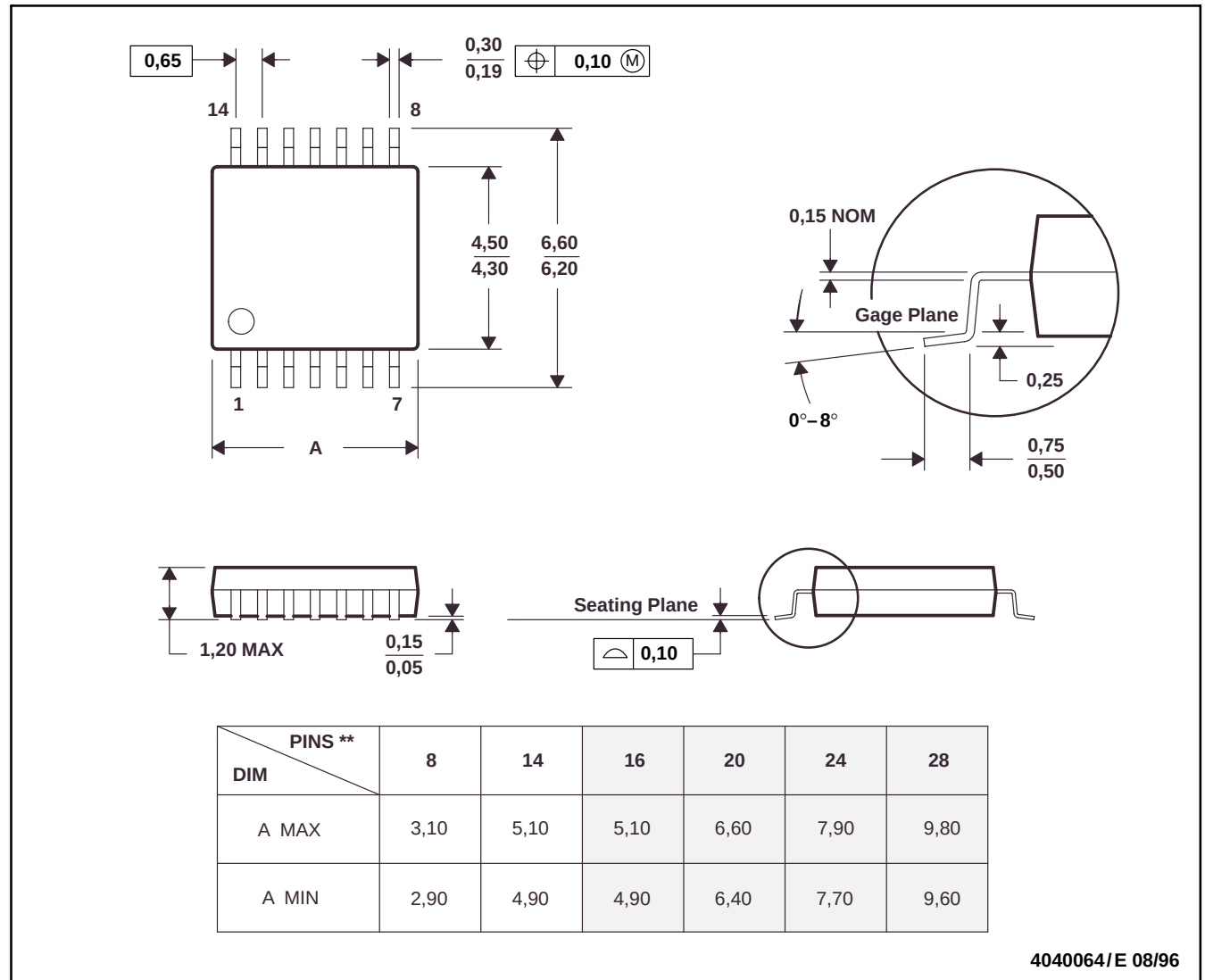
- NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Falls within JEDEC MS-001

MECHANICAL DATA

PW (R-PDSO-G)**

PLASTIC SMALL-OUTLINE PACKAGE

14 PIN SHOWN



- NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 D. Falls within JEDEC MO-153

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