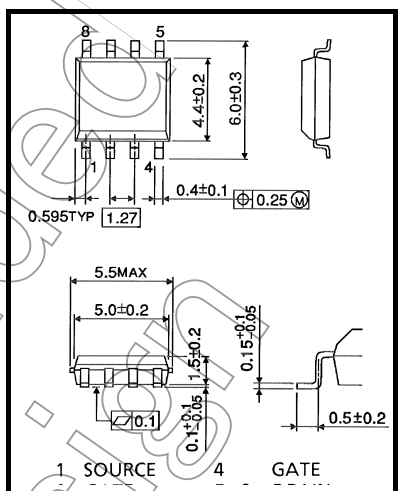
Q1:TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE(U−MOSⅢ)
Q2:TOSHIBA INCLUDES SCHOTTKY BARRIER DIODE FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE(U−MOSⅢ)

TPC8A01

DC-DC CONVERTER
Notebook PC
Portable Machines and Tools

- Includes Schottky Barrier Diode Type. (Q2)
  Low Forward Voltage: V_{DSF}=0.6V(Max.)
- Small footprint due to small and thin package.
- High Speed Switching.(Q1)
- Small Gate Charge.(Q1): Qg＝17nC(Typ.)
- Low drain-source ON resistance(Q2) $R_{DS\ (ON)}$ = 13 mΩ (typ.)
- High forward transfer admittance(Q2): $|Y_{fs}|$ = 11 S (typ.)
- Low leakage current. (Q1): I_{DSS} = 10 μA(Max.) (V_{DS} = 30 V)
  (Q2): I_{DSS} = 100 μA(Max.) (V_{DS} = 30 V)
- Enhancement-mode
  : (Q1) V_{th} = 1.1~2.3 V (V_{DS} = 10 V, I_D = 1 mA)
  : (Q2) V_{th} = 1.1~2.3 V (V_{DS} = 10 V, I_D = 1 mA)

Unit: mm



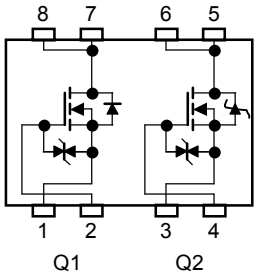
1 SOURCE	4 GATE
2 GATE	5, 6DRAIN/CATHOUDE
3 SURCE/ANODE	7, 8 DRAIN
JEDEC	—
JEITA	—
TOSHIBA	2-6J1E

Weight: 0.080 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating Q1	Rating Q2	Unit
Drain-source voltage			V_{DSS}	30	30	V
Drain-gate voltage (R_{GS} = 20 kΩ)			V_{DGR}	30	30	V
Gate-source voltage			V_{GSS}	±20	±20	V
Drain current	DC	(Note 1)	I_D	6	8.5	A
	Pulse	(Note 1)	I_{DP}	24	34	
Drain power dissipation (t = 10s) (Note 2a)	Single-device operation (Note 3a)		$P_{D(1)}$	1.5		W
	Single-device value at dual operation (Note 3b)		$P_{D(2)}$	1.1		
Drain power dissipation (t = 10s) (Note 2b)	Single-device operation (Note 3a)		$P_{D(1)}$	0.75		
	Single-device value at dual operation (Note 3b)		$P_{D(2)}$	0.45		
Single pulse avalanche energy			E_{AS}	46.8 (Note 4a)	93.9 (Note 4b)	mJ
Avalanche current			I_{AR}	6	8.5	A
Repetitive avalanche energy Single-device value at operation (Note 2a, 3b, 5)			E_{AR}	0.11		mJ
Channel temperature			T_{ch}	150		°C
Storage temperature range			T_{stg}	−55~150		°C

Circuit Configuration



(Includes Schottky Barrier Diode)

Note: (Note 1), (Note 2ab), (Note 3ab), (Note 4), (Note 5) Please see next page.

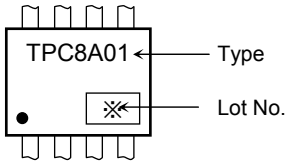
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic sensitive device. Please handle with caution.
Schottky barrier diodes are having large-reverse-current-leakage characteristic compare to the other rectifier products. This current leakage and not proper operating temprature or voltage may cause thermalrun. Please take forward and reverse loss into consideration when you design.

Thermal Characteristics

Characteristics		Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10s) (Note 2a)	Single-device operation (Note 3a)	$R_{th\ (ch\text{-}a)\ (1)}$	83.3	°C/W
	Single-device value at dual operation (Note 3b)	$R_{th\ (ch\text{-}a)\ (2)}$	114	
Thermal resistance, channel to ambient (t = 10s) (Note 2b)	Single-device operation (Note 2a)	$R_{th\ (ch\text{-}a)\ (1)}$	167	
	Single-device value at dual operation (Note 2b)	$R_{th\ (ch\text{-}a)\ (2)}$	278	

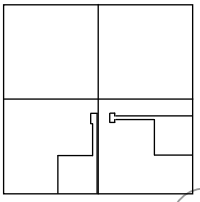
Marking



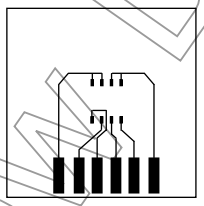
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:

a) Device mounted on a glass-epoxy board (a) b) Device mounted on a glass-epoxy board (b)



(a)



(b)

Note 3:

a) The power dissipation and thermal resistance values are shown for a single device (During single-device operation, power is only applied to one device.).

b) The power dissipation and thermal resistance values are shown for a single device (During dual operation, power is evenly applied to both devices.).

Note 4:

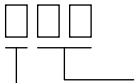
a) V_{DD} = 24 V, T_{ch} = 25°C (Initial), L = 1.0 mH, R_G = 25 Ω, I_{AR} = 6.0 A

b) V_{DD} = 24 V, T_{ch} = 25°C (Initial), L = 1.0 mH, R_G = 25 Ω, I_{AR} = 8.5 A

Note 5: Repetitive rating; pulse width limited by max channel temperature.

Note 6: • on lower left of the marking indicates Pin 1.

※ Weekly code: (Three digits)

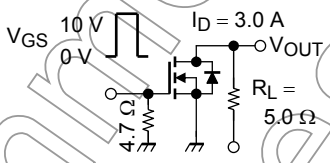


Week of manufacture
(01 for first week of year, continues up to 52 or 53)

Year of manufacture
(One low-order digits of calendar year)

Q1

Electrical Characteristics (Ta = 25°C)

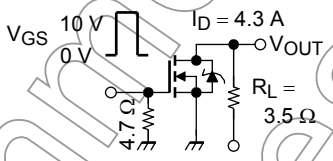
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	—	—	±10	μA
Drain cut-OFF current		I_{DSS}	V_{DS} = 30 V, V_{GS} = 0 V	—	—	−10	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	30	—	—	V
		$V_{(BR)\ DSX}$	I_D = 10 mA, V_{GS} = −20 V	15	—	—	
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	1.1	—	2.3	V
Drain-source ON resistance		$R_{DS\ (ON)}$	V_{GS} = 4.5 V, I_D = 3.0 A	—	23	30	mΩ
			V_{GS} = 10 V, I_D = 3.0 A	—	18	25	
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} =10 V, I_D = 3.0 A	4.5	9	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	940	—	pF
Reverse transfer capacitance		C_{rss}		—	130	—	
Output capacitance		C_{oss}		—	390	—	
Switching time	Rise time	t_r		—	17	—	ns
	Turn-ON time	t_{on}		—	25	—	
	Fall time	t_f		—	4	—	
	Turn-OFF time	t_{off}	Duty ≦ 1%, t_w = 10 μs	—	21	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≃ 24 V, V_{GS} = 10 V, I_D = 6.0 A	—	17	—	nC
			V_{DD} ≃ 24 V, V_{GS} = 5 V, I_D = 6.0 A	—	10	—	
Gate-source charge 1		$Q_{gs}1$	V_{DD} ≃ 24 V, V_{GS} = 10 V, I_D = 6.0 A	—	1.9	—	
Gate-drain (“miller”) charge		Q_{gd}		—	4.1	—	
Gateswitch charge		Q_{sw}		—	6	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	I_{DRP}	—	—	—	24	A
Forward voltage (diode)		V_{DSF}	I_{DR} = 6.0 A, V_{GS} = 0 V	—	—	−1.2	V

Q2 (Includes Schottky Barrier Diode)

Electrical Characteristics (Ta = 25°C)

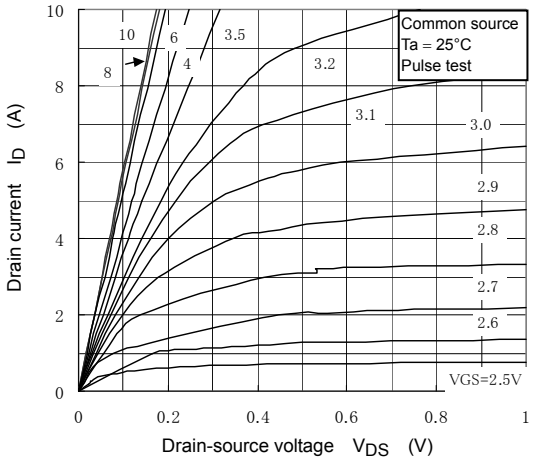
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	—	—	±10	μA
Drain cut-OFF current		I_{DSS}	V_{DS} = 30 V, V_{GS} = 0 V	—	—	10	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	30	—	—	V
		$V_{(BR)\ DSX}$	I_D = 10 mA, V_{GS} = −20 V	15	—	—	
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	1.1	—	2.3	V
Drain-source ON resistance		$R_{DS\ (ON)}$	V_{GS} = 4.5 V, I_D = 4.3 A	—	16	21	mΩ
			V_{GS} = 10 V, I_D = 4.3 A	—	13	18	
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 4.3 A	5.5	11	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	2295	—	pF
Reverse transfer capacitance		C_{rss}		—	360	—	
Output capacitance		C_{oss}		—	510	—	
Switching time	Rise time	t_r		—	8	—	ns
	Turn-ON time	t_{on}		—	17	—	
	Fall time	t_f		—	15	—	
	Turn-OFF time	t_{off}	Duty ≦ 1%, t_w = 10 μs	—	52	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≃ 24 V, V_{GS} = 10 V, I_D = 8.5 A	—	49	—	nC
			V_{DD} ≃ 24 V, V_{GS} = 5 V, I_D = 8.5 A	—	27	—	
Gate-source charge 1		$Q_{gs}1$	V_{DD} ≃ 24 V, V_{GS} = 10 V, I_D = 8.5 A	—	3.7	—	
Gate-drain (“miller”) charge		Q_{gd}		—	10.8	—	
Gateswitch charge		Q_{sw}		—	14.5	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

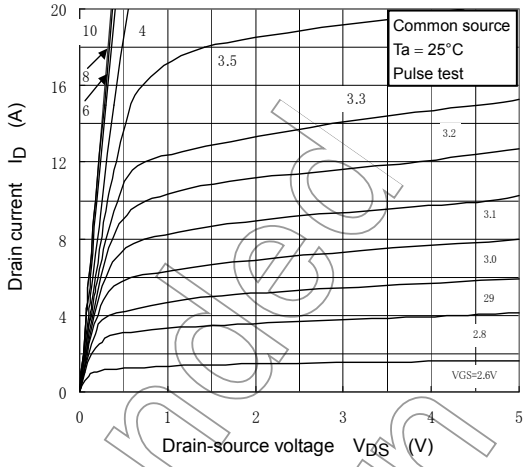
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	I_{DRP}	—	—	—	34	A
Forward voltage (diode)		V_{DSF}	I_{DR} = 1.0 A, V_{GS} = 0 V	—	−0.5	−0.6	V
			I_{DR} = 8.5 A, V_{GS} = 0 V	—	—	−1.2	

Q1

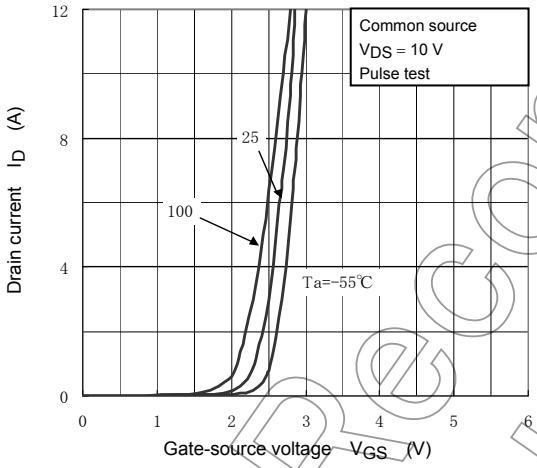
$I_D - V_{DS}$



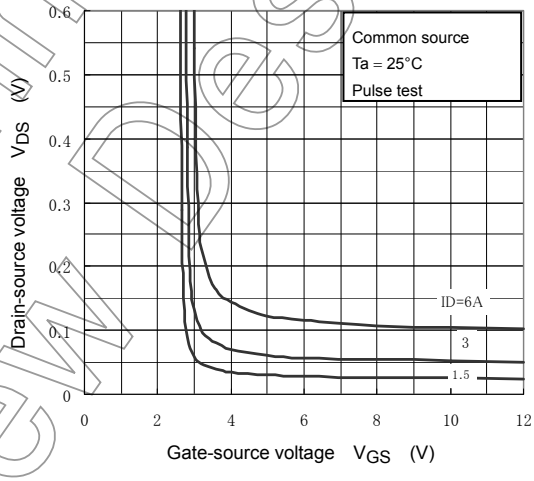
$I_D - V_{DS}$



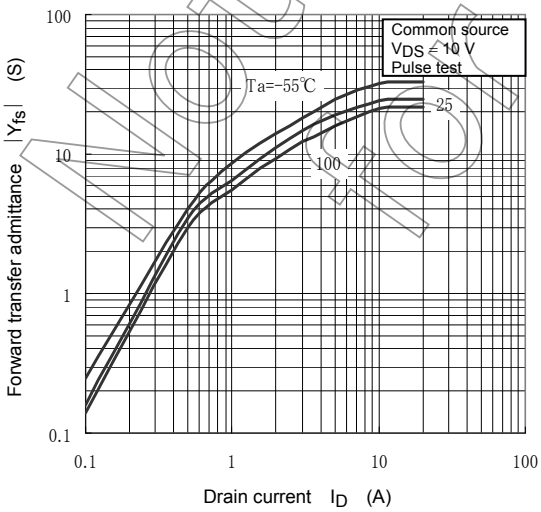
$I_D - V_{GS}$



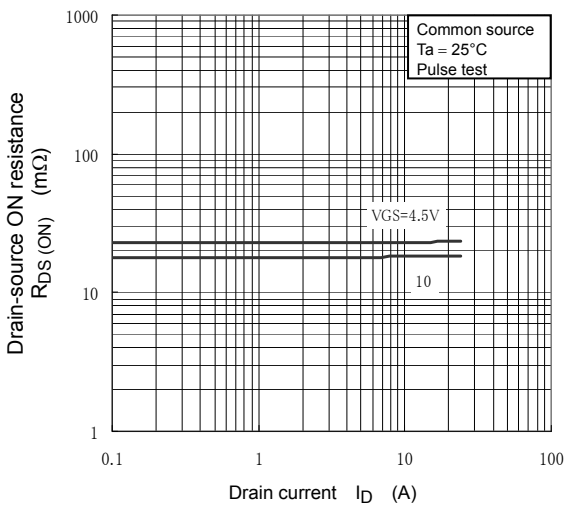
$V_{DS} - V_{GS}$



$|Y_{fs}| - I_D$

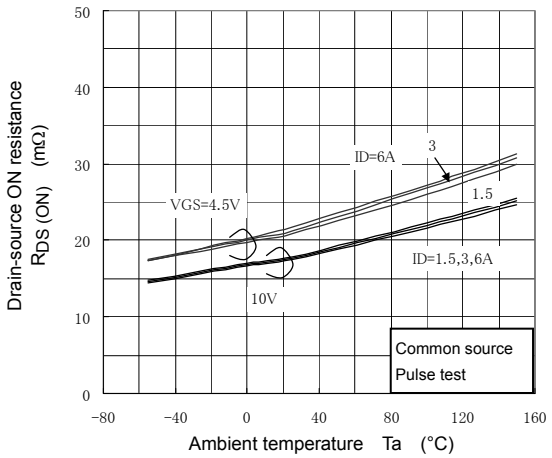


$R_{DS\ (ON)} - I_D$

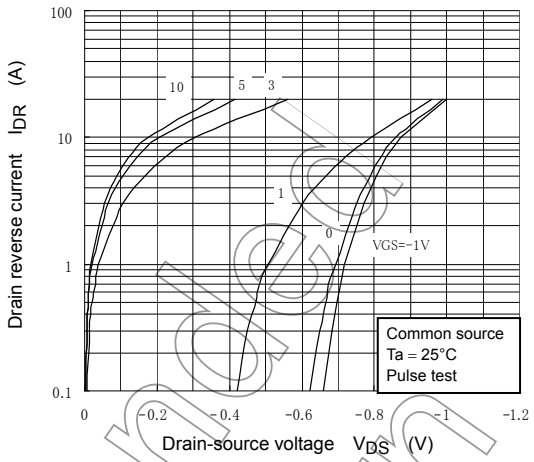


Q1

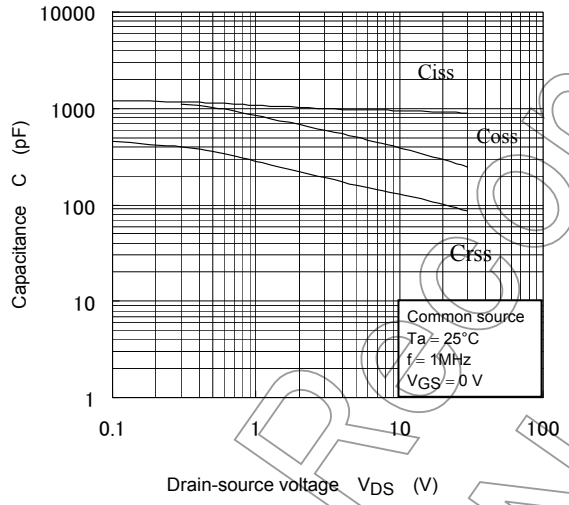
$R_{DS\ (ON)}$ – Ta



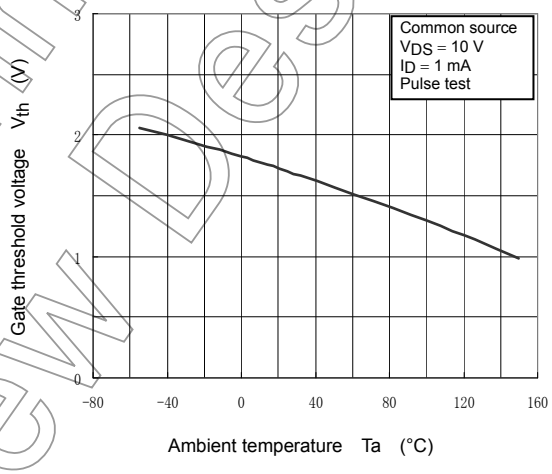
I_{DR} – V_{DS}



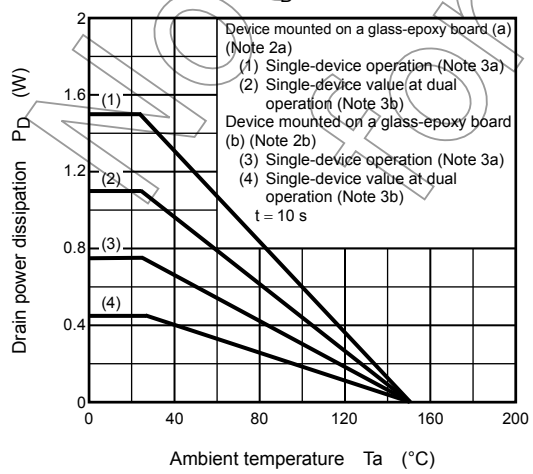
Capacitance – V_{DS}



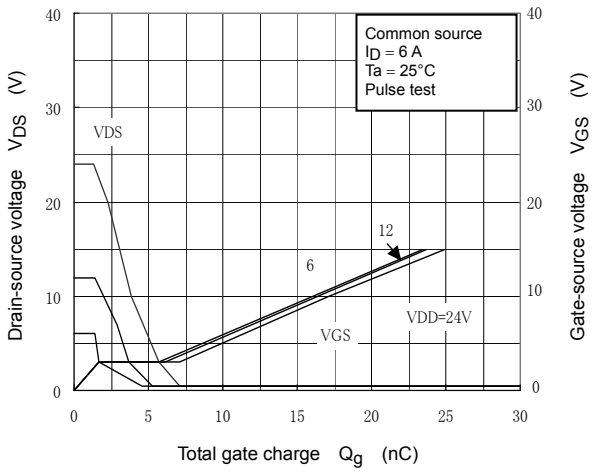
V_{th} – Ta



P_D – Ta

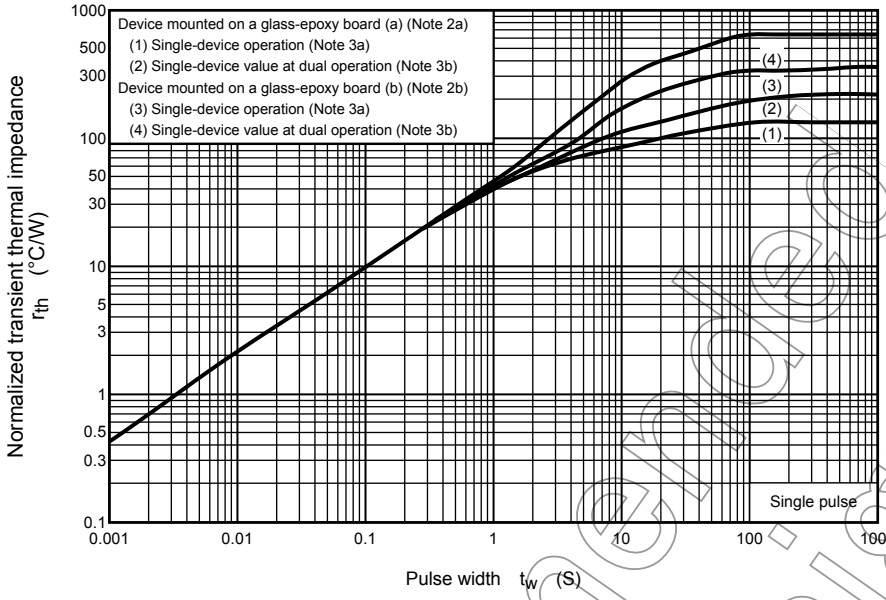


Dynamic input/output characteristics

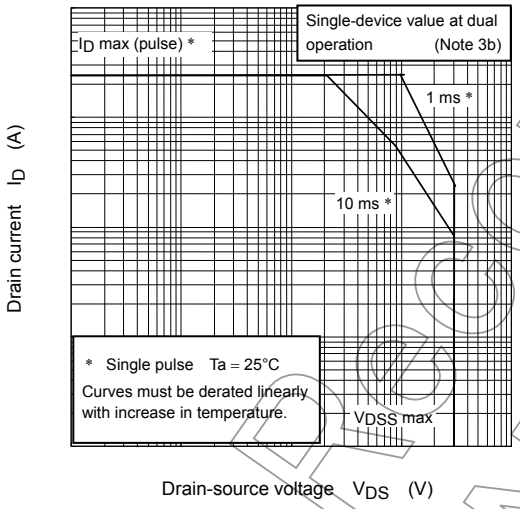


Q1

$r_{th} - t_w$

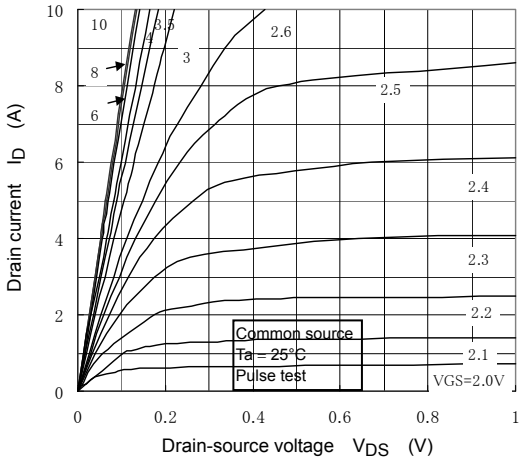


Safe operating area

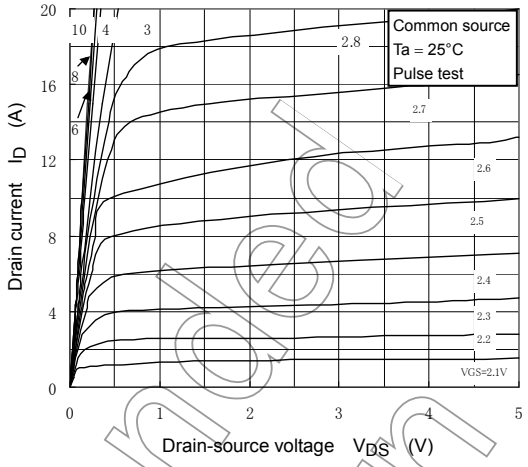


Q2(Includes Schottky Barrier Diode)

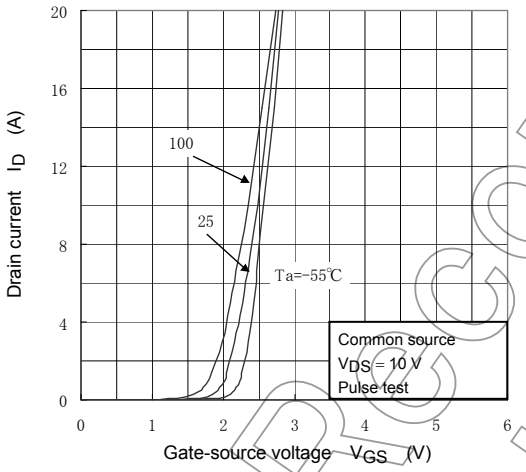
$I_D - V_{DS}$



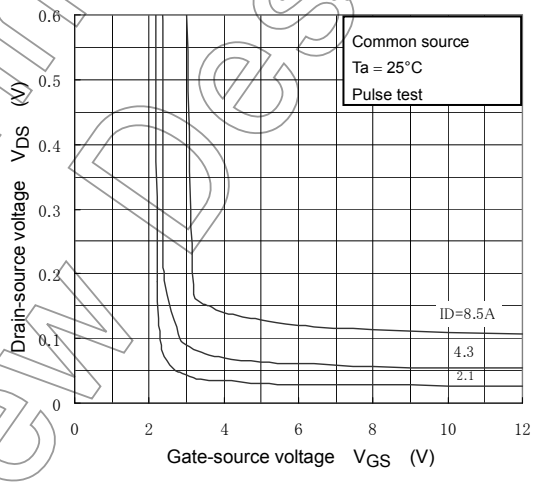
$I_D - V_{DS}$



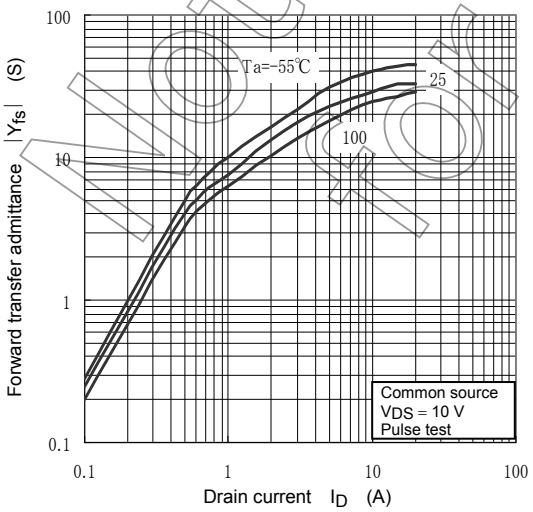
$I_D - V_{GS}$



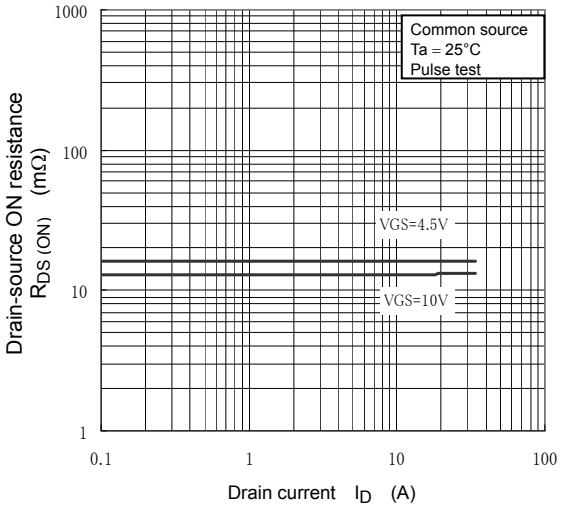
$V_{DS} - V_{GS}$



$|Y_{fs}| - I_D$

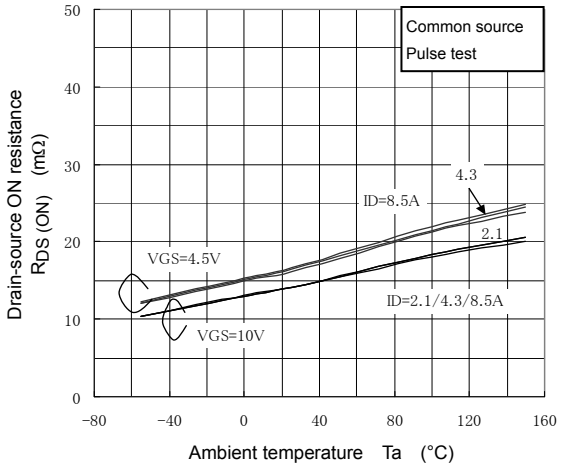


$R_{DS\ (ON)} - I_D$

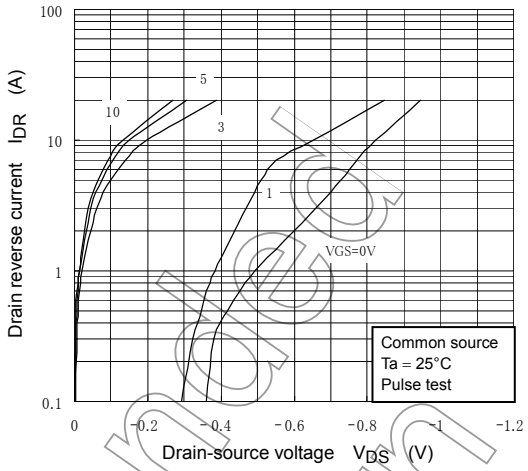


Q2(Includes Schottky Barrier Diode)

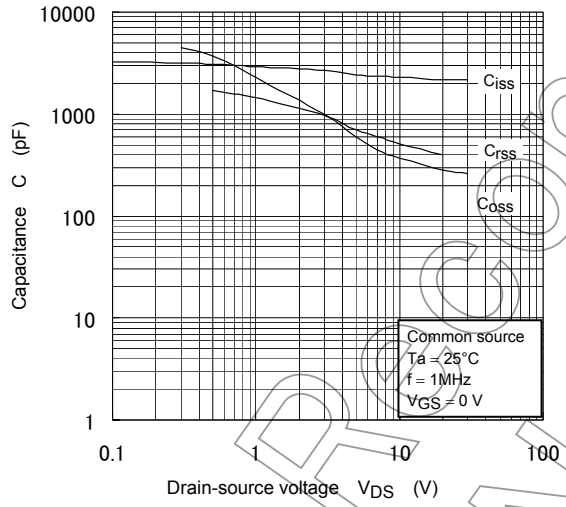
$R_{DS\ (ON)}$ – Ta



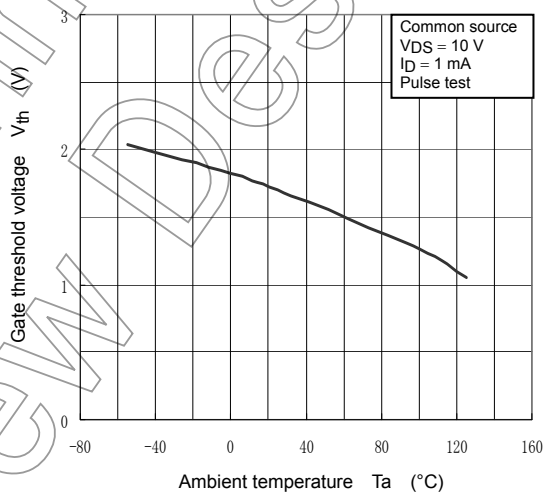
I_{DR} – V_{DS}



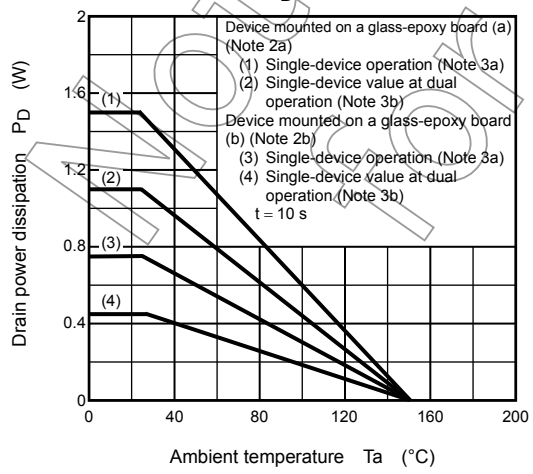
Capacitance – V_{DS}



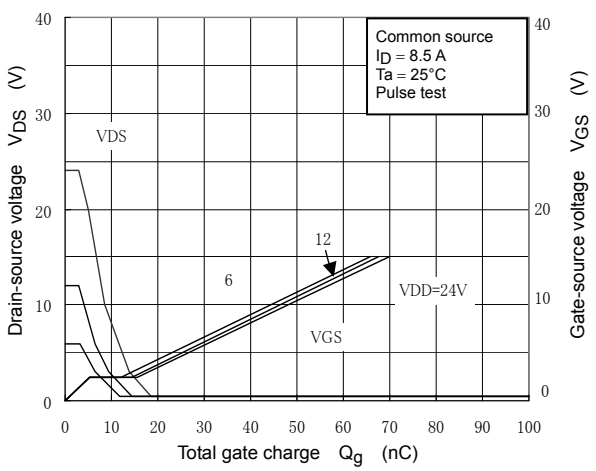
V_{th} – Ta



P_D – Ta

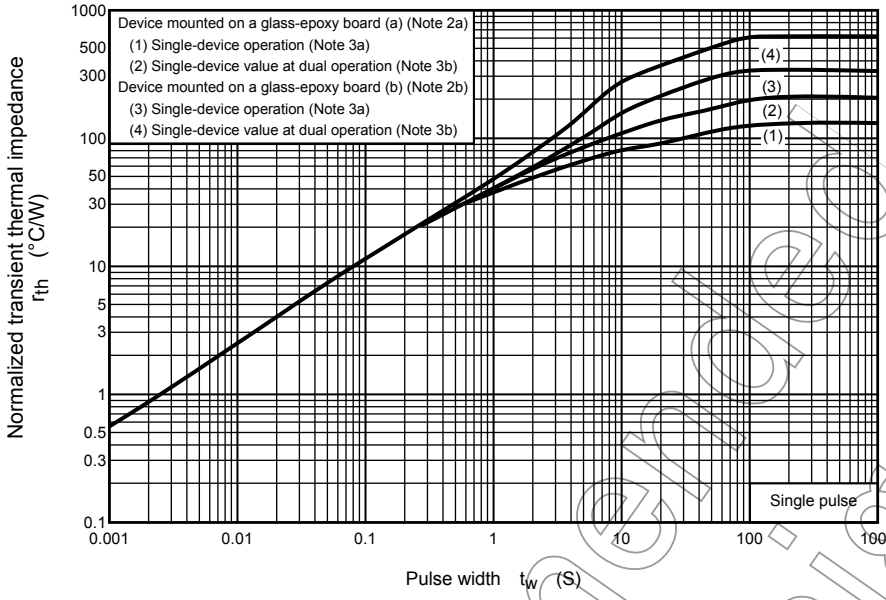


Dynamic input/output characteristics

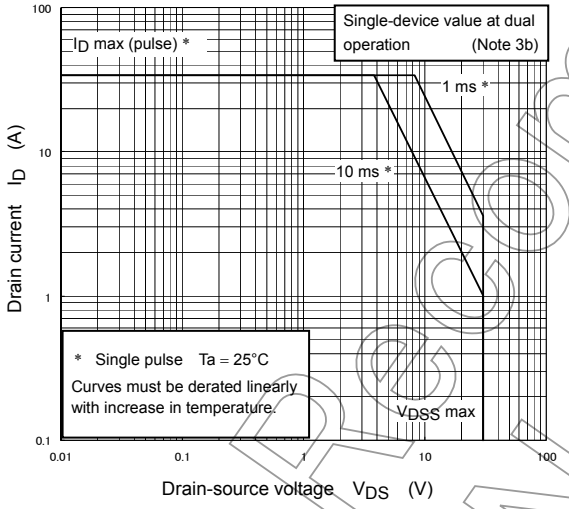


Q2(Includes Schottky Barrier Diode)

$r_{th} - t_w$

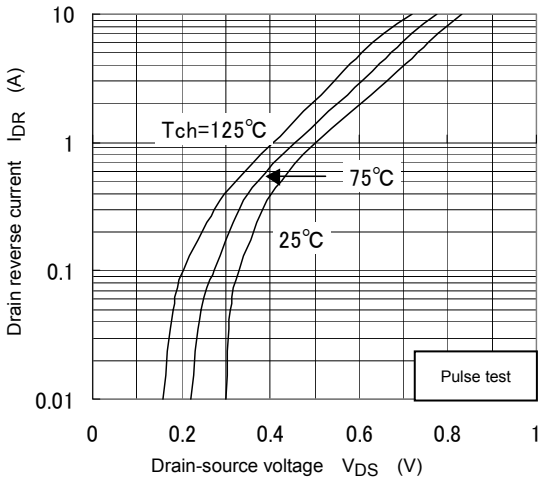


Safe operating area

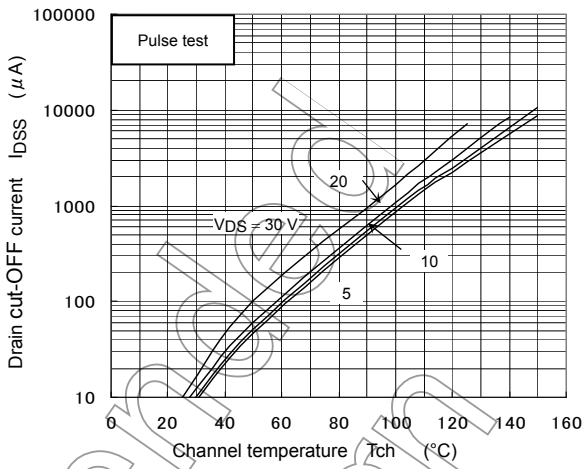


Q2 (V_{GS}=0V)

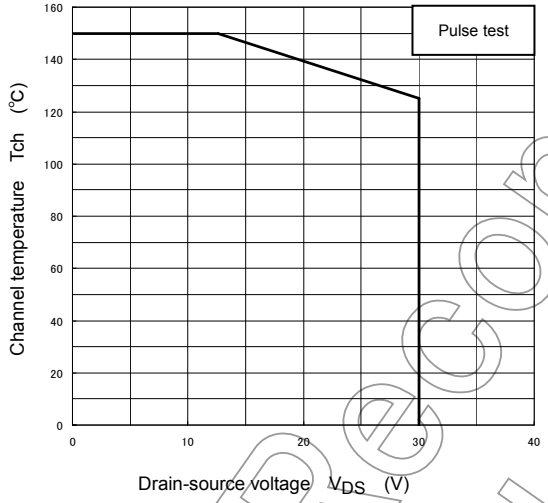
I_{DR} – V_{DSF}



I_{DSS} – Tch (typ.)



Tch – V_{DS}



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