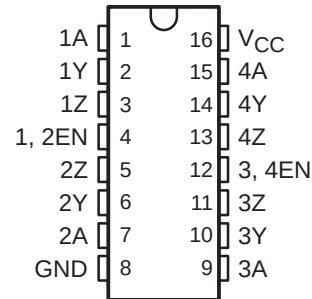


SN55ALS194, SN75ALS194 QUADRUPLE DIFFERENTIAL LINE DRIVERS

SLLS009D – OCTOBER 1985 – REVISED MAY 1995

- Meet or Exceed the Requirements of ANSI Standard EIA/TIA-422-B and ITU Recommendation V.11
- Designed to Operate Up to 20 Mbaud
- 3-State TTL-Compatible Outputs
- Single 5-V Supply Operation
- High Output Impedance in Power-Off Condition
- Two Pairs of Drivers, Independently Enabled
- Designed as Improved Replacements for the MC3487

SN55ALS194 . . . J OR W PACKAGE
SN75ALS194 . . . D OR N PACKAGE
(TOP VIEW)



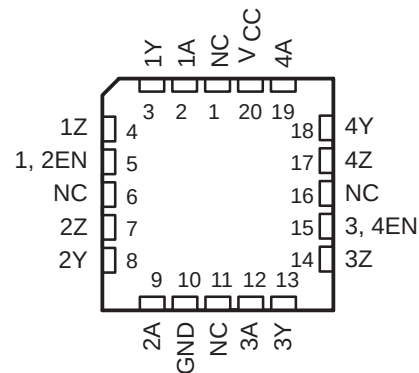
description

These four differential line drivers are designed for data transmission over twisted-pair or parallel-wire transmission lines. They meet the requirements of ANSI Standard EIA/TIA-422-B and ITU Recommendation V.11 and are compatible with 3-state TTL circuits. Advanced low-power Schottky technology provides high speed without the usual power penalty. Standby supply current is typically only 26 mA. Typical propagation delay time is less than 10 ns, and enable/disable times are typically less than 16 ns.

High-impedance inputs keep input currents low: less than 1 μ A for a high level and less than 100 μ A for a low level. The driver circuits can be enabled in pairs by separate active-high enable inputs. The SN55ALS194 and SN75ALS194 are capable of data rates in excess of 20 megabits per second and are designed to operate with the SN55ALS195 and SN75ALS195 quadruple line receivers.

The SN55ALS194 is characterized for operation over the full military temperature range of -55°C to 125°C . The SN75ALS194 is characterized for operation from 0°C to 70°C .

SN55ALS194 . . . FK PACKAGE
(TOP VIEW)



NC – No internal connection

FUNCTION TABLE
(each driver)

INPUTS A	OUTPUT EN	OUTPUTS	
		Y	Z
H	H	H	L
L	H	L	H
X	L	Z	Z

H = high level, L = low level, X = irrelevant,
Z = high impedance



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS
INSTRUMENTS**

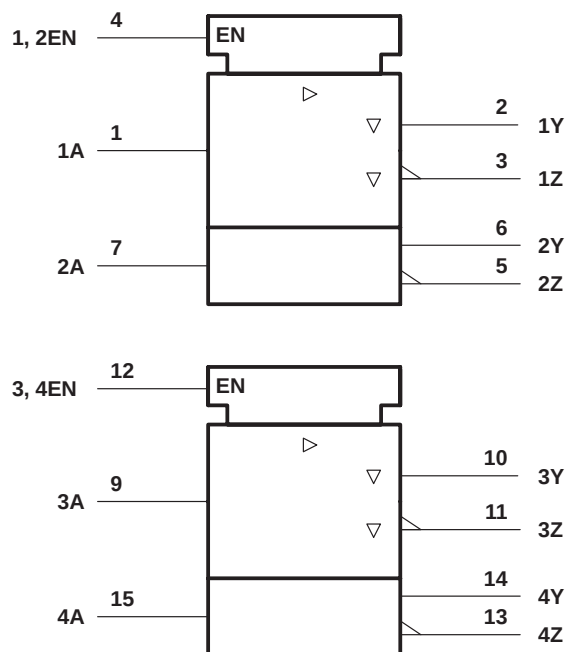
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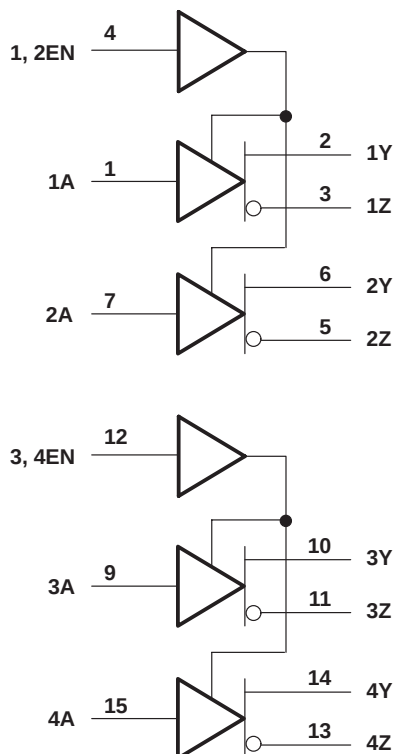
SN55ALS194, SN75ALS194 QUADRUPLE DIFFERENTIAL LINE DRIVERS

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logic symbol†



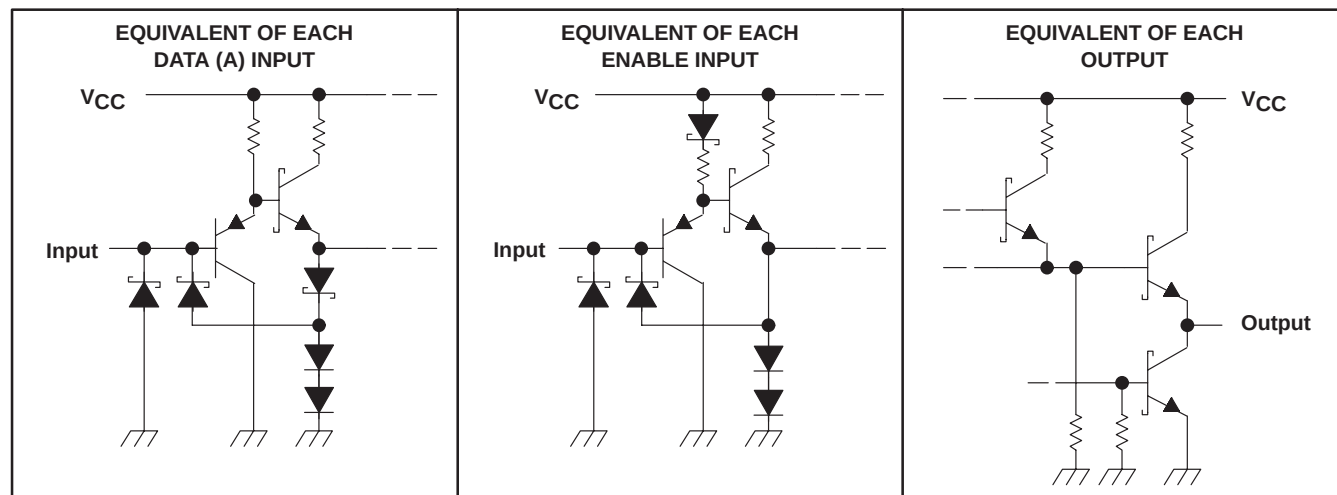
logic diagram (positive logic)



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the D, J, N, and W packages.

schematics of inputs and outputs



SN55ALS194, SN75ALS194 QUADRUPLE DIFFERENTIAL LINE DRIVERS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V_{CC} (see Note 1)	7 V
Input voltage, V_I	5.5 V
Output voltage, V_O	7 V
Continuous total dissipation	See Dissipation Rating Table
Operating free-air temperature range, T_A : SN55ALS194	– 55°C to 125°C
SN75ALS194	0°C to 70°C
Storage temperature range, T_{stg}	– 65°C to 150°C
Case temperature for 60 seconds, T_C : FK package	260°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D, N, or W package	260°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: J package	300°C

[†] Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: All voltage values are with respect to network ground terminal

DISSIPATION RATING TABLE

PACKAGE	$T_A \leq 25^\circ\text{C}$ POWER RATING	DERATING FACTOR ABOVE $T_A = 25^\circ\text{C}$	$T_A = 70^\circ\text{C}$ POWER RATING	$T_A = 125^\circ\text{C}$ POWER RATING
D	950 mW	7.6 mW/°C	608 mW	N/A
FK	1375 mW	11.0 mW/°C	880 mW	275 mW
J	1375 mW	11.0 mW/°C	880 mW	275 mW
N	1150 mW	9.2 mW/°C	736 mW	N/A
W	1000 mW	8.0 mW/°C	640 mW	200 mW

recommended operating conditions[‡]

		SN55ALS194			SN75ALS194			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX		
Supply voltage, V _{CC}		4.5	5	5.5	4.75	5	5.25	V	
High-level input voltage, V _{IH}	All inputs, T _A = 25°C	2			2			V	
	A inputs, T _A = Full range	2			2				
	EN inputs, T _A = Full range	2.1			2				
Low-level input voltage, V _{IL}		0.8			0.8			V	
High-level output current, I _{OH}		– 20			– 20			mA	
Low-level output current, I _{OL}	T _A = 25°C	48			48			mA	
	T _A = Full range	20			48				
Operating free-air temperature, T _A		– 55			0			70	°C

[‡] Full range is $T_A = -55^\circ\text{C}$ to 125°C for SN55ALS194 and $T_A = 0^\circ\text{C}$ to 70°C for SN75ALS194.

SN55ALS194, SN75ALS194

QUADRUPLE DIFFERENTIAL LINE DRIVERS

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electrical characteristics over recommended ranges of supply voltage and operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS [†]	MIN	TYP [‡]	MAX	UNIT
V _{IK} Input clamp voltage	V _{CC} = MIN, I _I = –18 mA			–1.5	V
V _{OH} High-level output voltage	V _{CC} = MIN, I _{OH} = –20 mA	2.4			V
		2.5			
V _{OL} Low-level output voltage	V _{CC} = MIN, I _{OL} = MAX			0.5	V
V _O Output voltage	I _O = 0	0		6	V
V _{OD1} Differential output voltage	I _O = 0	1.5		6	V
V _{OD2} Differential output voltage	R _L = 100 Ω, See Figure 1	1/2 V _{OD1} or 2 ^S			V
Δ V _{OD} Change in magnitude of differential output voltage [¶]				±0.4	V
V _{OC} Common-mode output voltage				±3	V
Δ V _{OC} Change in magnitude of common-mode output voltage [¶]				±0.4	V
I _O Output current with power off	V _{CC} = 0	V _O = 6 V		100	μA
		V _O = –0.25 V		–100	
I _{OZ} High-impedance-state output current	V _{CC} = MAX, Output enables at 0.8 V	V _O = 2.7 V		100	μA
		V _O = 0.5 V		–100	
I _I Input current at maximum input voltage	V _{CC} = MAX, V _I = 5.5 V			100	μA
I _{IH} High-level input current	V _{CC} = MAX, V _I = 2.7 V			50	μA
I _{IL} Low-level input current	V _{CC} = MAX, V _I = 0.5 V			–200	μA
I _{OS} Short-circuit output current [#]	V _{CC} = MAX, V _I = 2 V	–40		–140	mA
I _{CC} Supply current (all drivers)	V _{CC} = MAX, All outputs disabled		26	45	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at V_{CC} = 5 V, T_A = 25°C.

^S The minimum V_{OD2} with a 100-Ω load is either 1/2 V_{OD1} or 2 V, whichever is greater.

[¶] Δ|V_{OD}| and Δ|V_{OC}| are the changes in magnitude of V_{OD} and V_{OC}, respectively, that occur when the input is changed from a high level to a low level.

[#] Not more than one output should be shorted at a time, and duration of the short circuit should not exceed one second.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER	TEST CONDITIONS	SN55ALS194			SN75ALS194			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	
t _{PLH} Propagation delay time, low- to high-level output	C _L = 15 pF, See Figure 2		6	13		6	13	ns
t _{PHL} Propagation delay time, high- to low-level output			9	14		9	14	ns
Output-to-output skew			3.5	6		3.5	6	ns
t _{t(OD)} Differential output transition time	C _L = 15 pF, See Figure 3		8	14		8	14	ns
t _{PZH} Output enable time to high level	C _L = 15 pF, See Figure 4		9	12		9	12	ns
t _{PZL} Output enable time to low level			12	20		12	20	ns
t _{PHZ} Output disable time from high level			9	15		9	14	ns
t _{PLZ} Output disable time from low level			12	15		12	15	ns



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SYMBOL EQUIVALENTS

DATA SHEET PARAMETER	EIA/TIA-422-B
V_O	V_{Oa}, V_{Ob}
$ V_{OD1} $	V_O
$ V_{OD2} $	$V_t (R_L = 100 \Omega)$
$\Delta V_{OD} $	$ V_t - \bar{V}_t $
V_{OC}	$ V_{Os} $
$\Delta V_{OC} $	$ V_{Os} - \bar{V}_{Os} $
I_{OS}	$ I_{sa} , I_{sb} $
I_O	$ I_{xa} , I_{xb} $

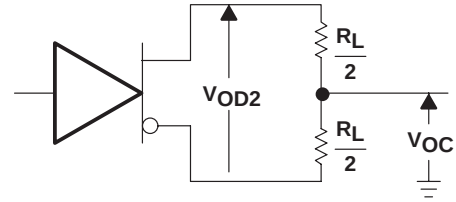
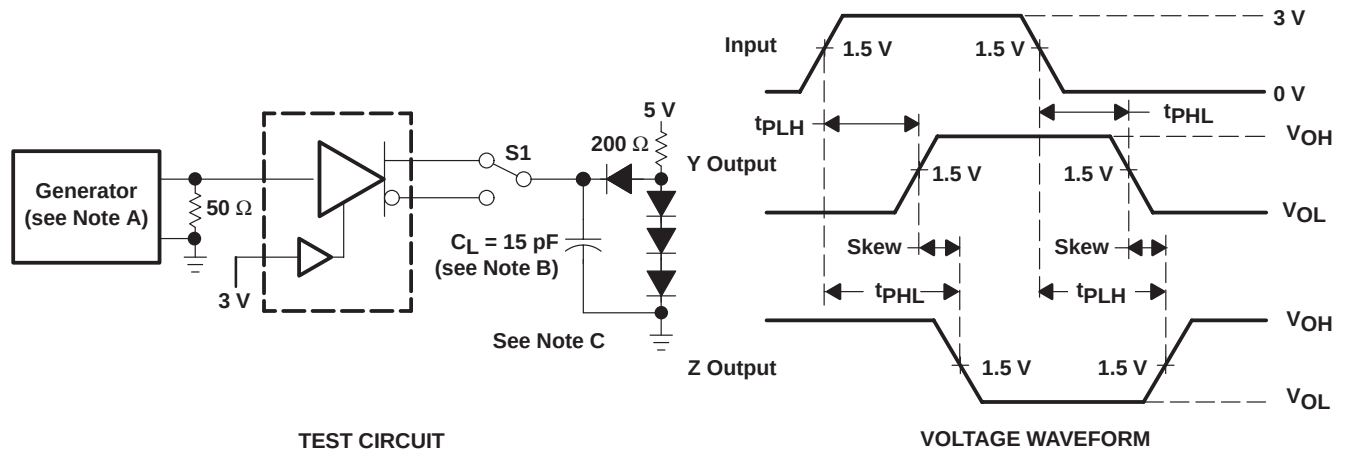


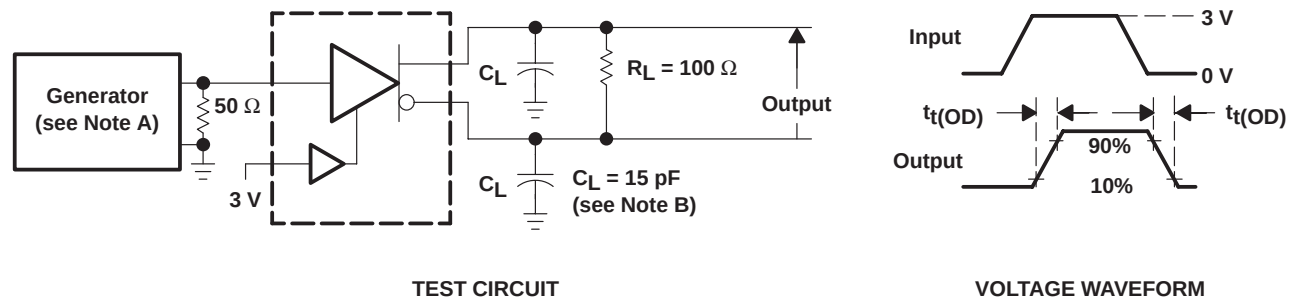
Figure 1. Driver V_{OD} and V_{OC}

PARAMETER MEASUREMENT INFORMATION



- NOTES: A. The input pulse is supplied by a generator having the following characteristics: $t_r \leq 5$ ns, $t_f \leq 5$ ns, $PRR \leq 1$ MHz, duty cycle $\leq 50\%$, $Z_O \approx 50 \Omega$.
B. C_L includes probe and stray capacitance.
C. All diodes are 1N916 or 1N3064.

Figure 2. Test Circuit and Voltage Waveform



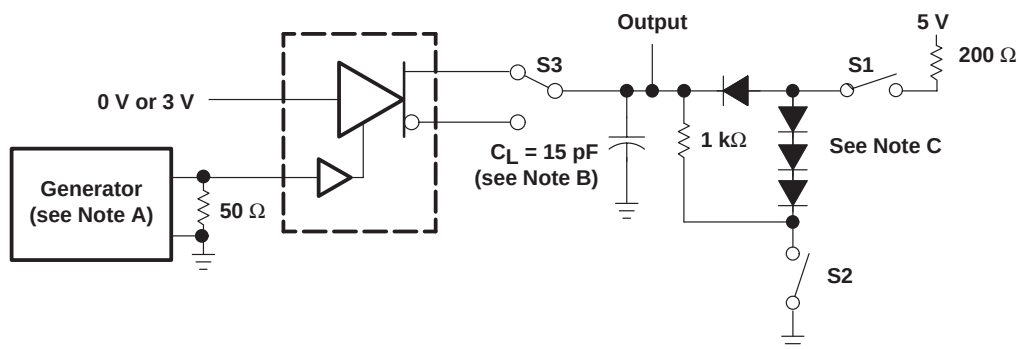
- NOTES: A. The input pulse is supplied by a generator having the following characteristics: $t_r \leq 5$ ns, $t_f \leq 5$ ns, $PRR \leq 1$ MHz, duty cycle $\leq 50\%$, $Z_O \approx 50 \Omega$.
B. C_L includes probe and stray capacitance.

Figure 3. Differential-Output Test Circuit and Voltage Waveform

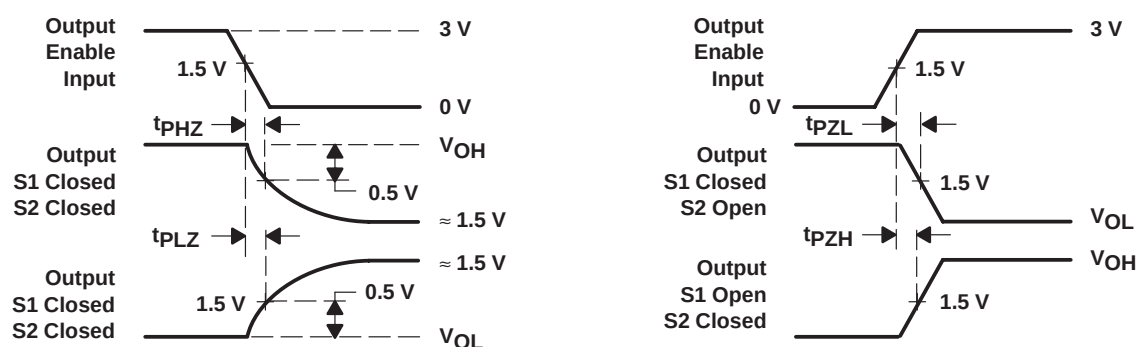
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PARAMETER MEASUREMENT INFORMATION



TEST CIRCUIT



VOLTAGE WAVEFORMS

- NOTES: A. The input pulse is supplied by a generator having the following characteristics: $t_r \leq 5$ ns, $t_f \leq 5$ ns, PRR ≤ 1 MHz, duty cycle $\leq 50\%$, $Z_O \approx 50 \Omega$.
B. C_L includes probe and stray capacitance.
C. All diodes are 1N916 or 1N3064.

Figure 4. Driver Test Circuit and Voltage Waveforms

TYPICAL CHARACTERISTICS†

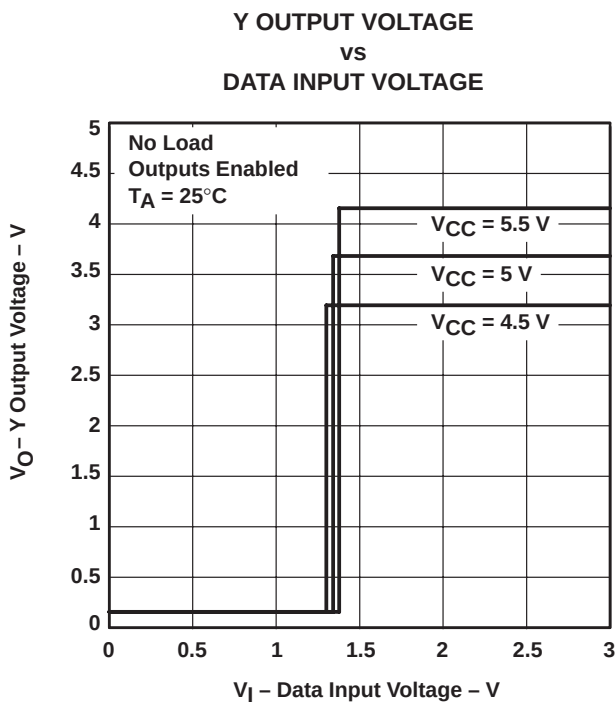


Figure 5

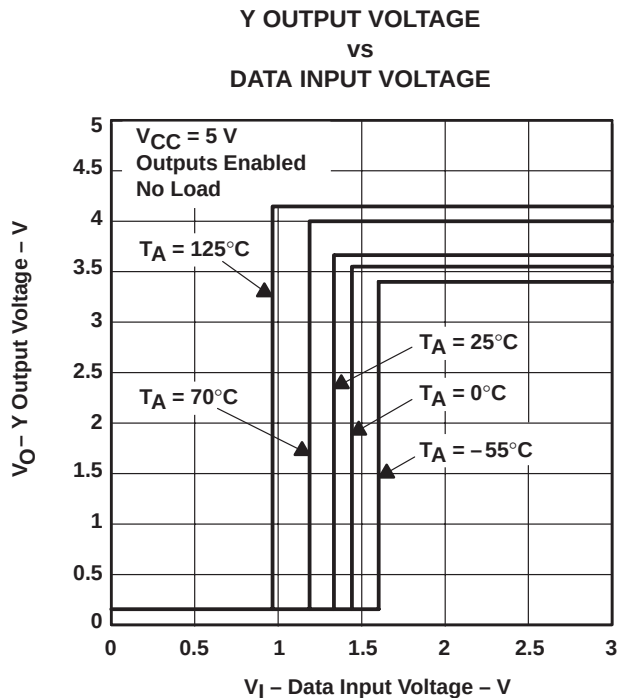


Figure 6

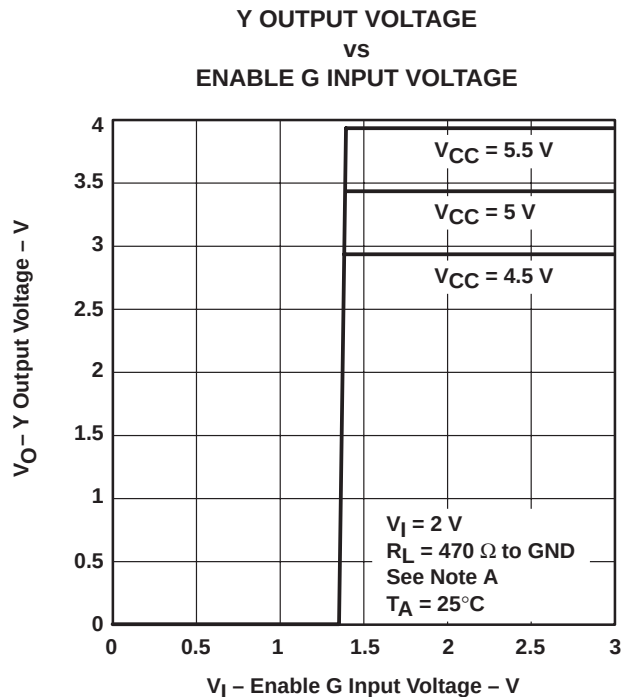


Figure 7

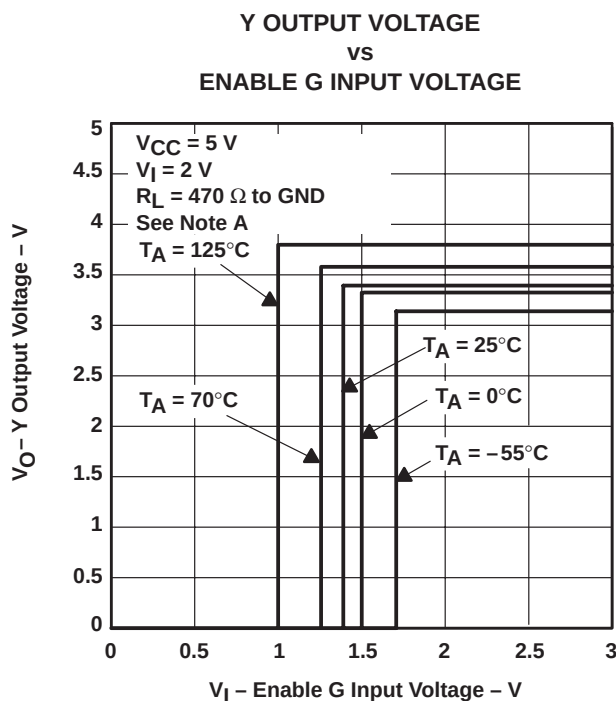


Figure 8

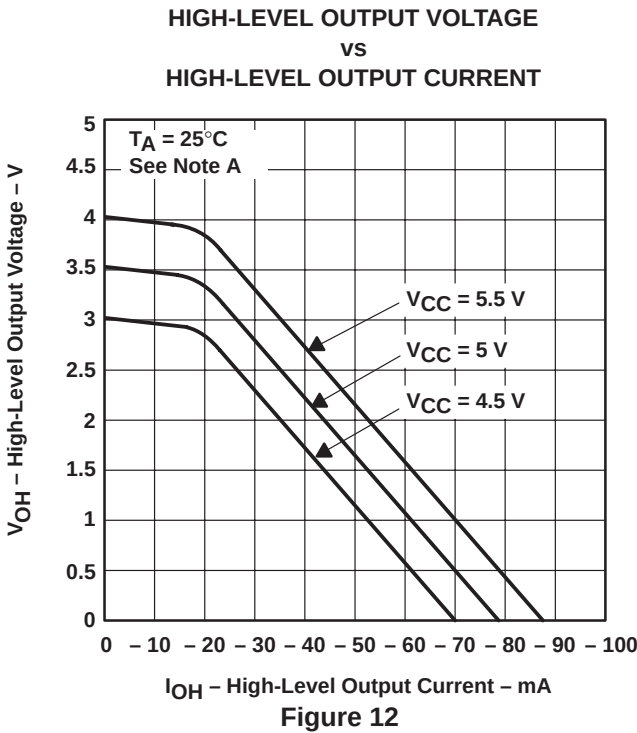
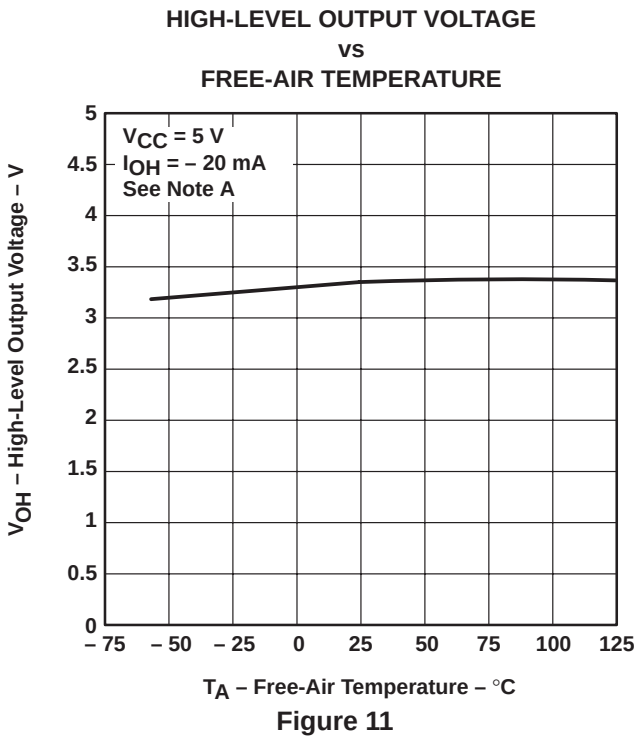
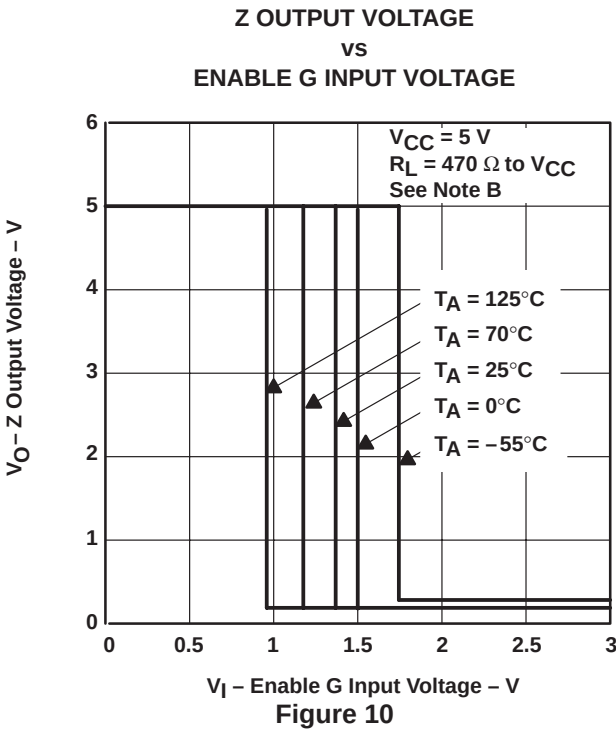
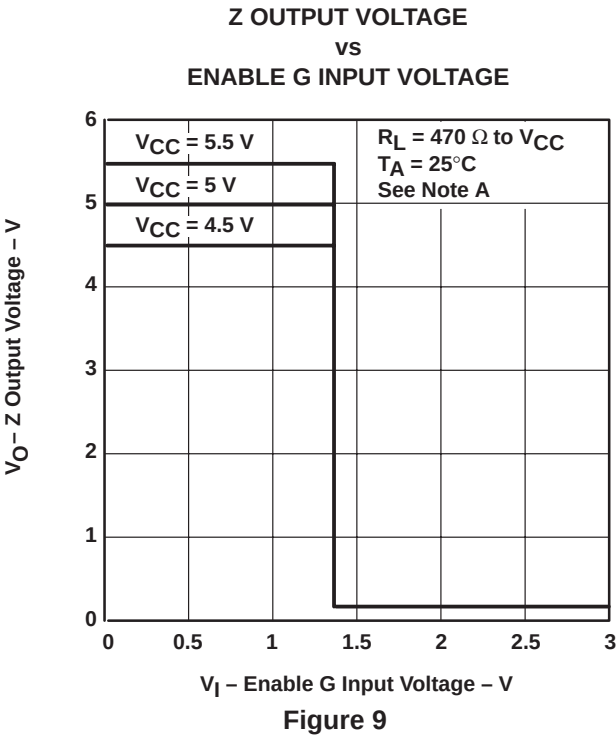
† Data for temperatures below 0°C and above 70°C are applicable to the SN55ALS194 circuits only.

NOTE A: The A input is connected to V_{CC} during the testing of the Y outputs and to GND during the testing of the Z outputs.

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TYPICAL CHARACTERISTICS†



† Data for temperatures below 0°C and above 70°C are applicable to the SN55ALS194 circuits only.

NOTES: A. The A input is connected to V_{CC} during the testing of the Y outputs and to GND during the testing of the Z outputs.

B. The A input is connected to ground during the testing of the Y outputs and to V_{CC} during the testing of the Z outputs.

TYPICAL CHARACTERISTICS†

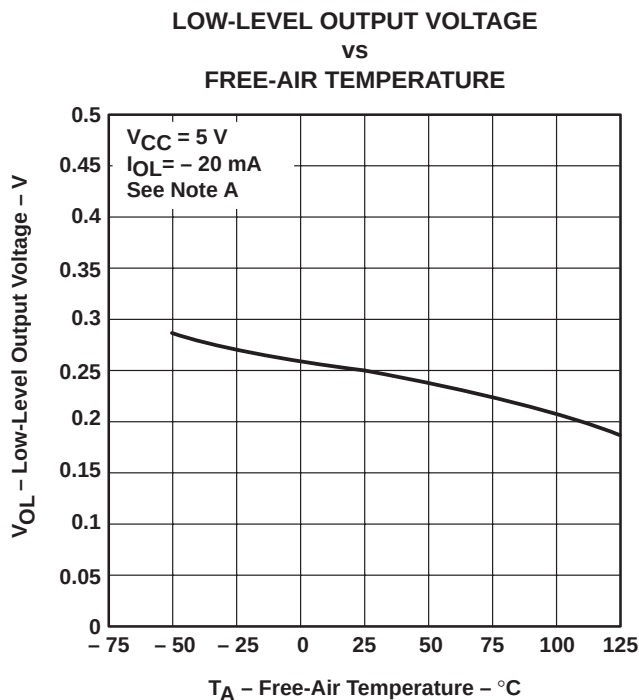


Figure 13

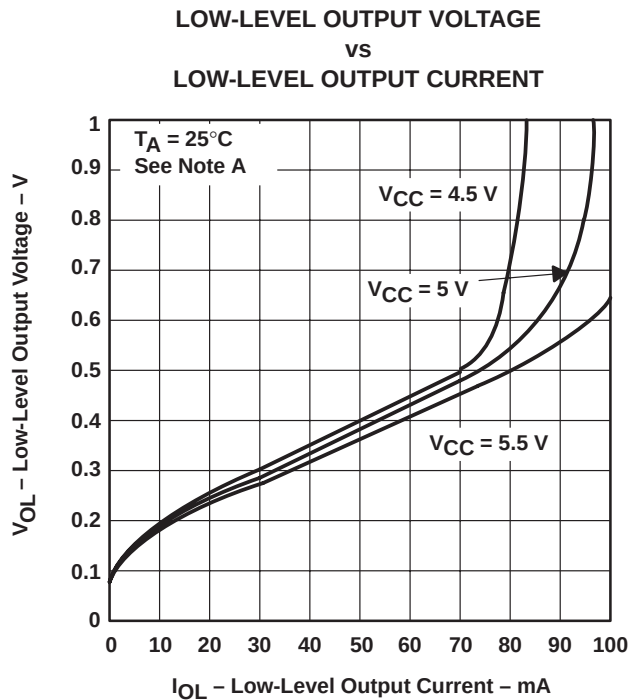


Figure 14

NOTE A: The A input is connected to GND during the testing of the Y outputs and to V_{CC} during the testing of the Z outputs.

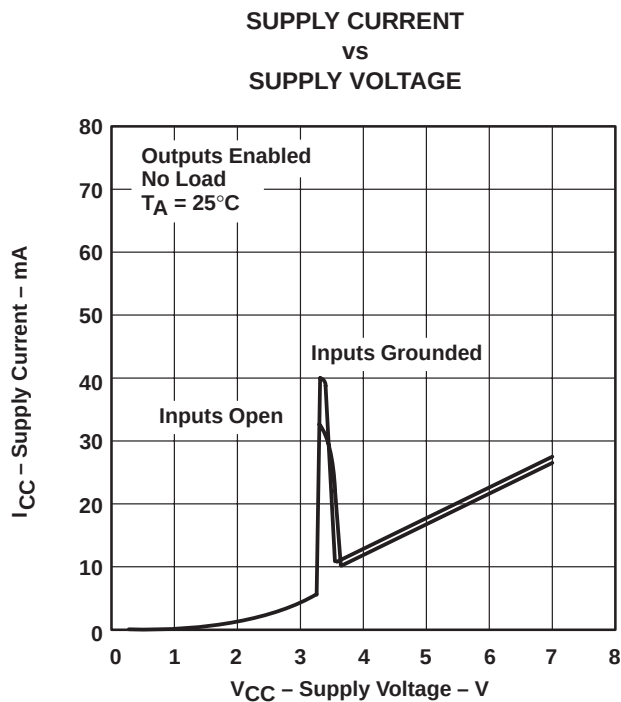


Figure 15

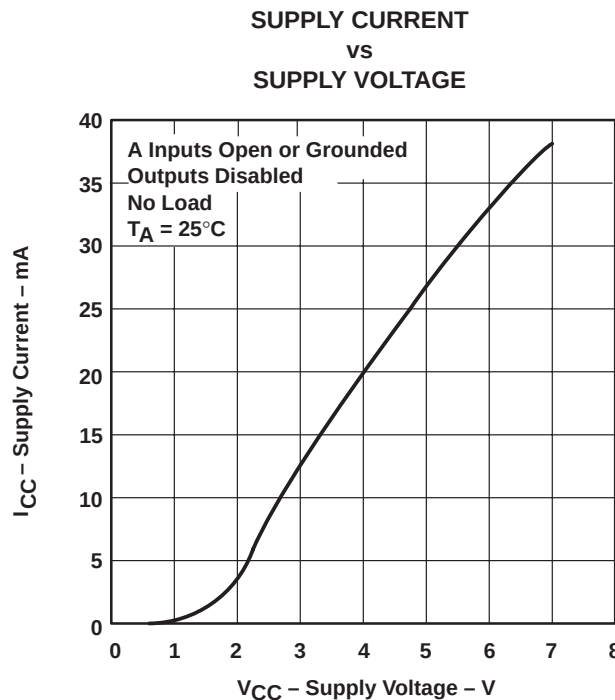


Figure 16

† Data for temperatures below 0°C and above 70°C are applicable to the SN55ALS194 circuits only.

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TYPICAL CHARACTERISTICS

SUPPLY CURRENT vs FREQUENCY

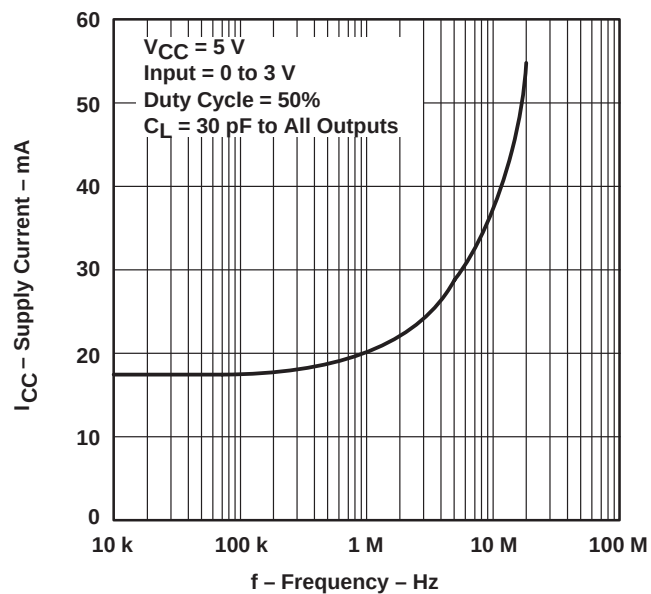


Figure 17

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