

# MPS4126

## Amplifier Transistor

### PNP Silicon

#### Features

- Pb-Free Packages are Available\*

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	$V_{CE}$	–25	Vdc
Collector–Base Voltage	$V_{CB}$	–25	Vdc
Emitter–Base Voltage	$V_{EB}$	–4.0	Vdc
Collector Current – Continuous	$I_C$	–200	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	625 5.0	W mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.5 12	W mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	–55 to +150	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

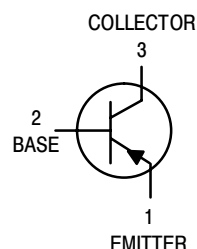
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	200	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction–to–Case	$R_{\theta JC}$	83.3	$^\circ\text{C}/\text{W}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

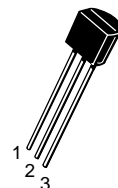


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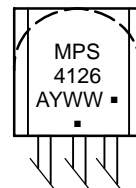
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#### MARKING DIAGRAM



TO-92  
CASE 29-11  
STYLE 1



MPS4126 = Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping†
MPS4126RLRA	TO-92	2,000/Tape & Reel
MPS4126RLRAG	TO-92 (Pb-Free)	2,000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MPS4126

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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### OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage (I <sub>C</sub> = –1.0 mA, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	–25	–	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = –10 µA, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	–25	–	Vdc
Emitter–Base Breakdown Voltage (I <sub>C</sub> = 0, I <sub>E</sub> = –10 µA)	V <sub>(BR)EBO</sub>	–4.0	–	Vdc
Collector Cutoff Current (V <sub>CB</sub> = –20 V, I <sub>E</sub> = 0)	I <sub>CBO</sub>	–	–50	nAdc
Emitter Cutoff Current (V <sub>EB</sub> = –3.0 V, I <sub>C</sub> = 0)	I <sub>EBO</sub>	–	–50	nAdc

### ON CHARACTERISTICS

DC Current Gain (I <sub>C</sub> = –2.0 mA, V <sub>CE</sub> = –1.0 V) (I <sub>C</sub> = –50 mA, V <sub>CE</sub> = –1.0 V)	h <sub>FE</sub>	120 60	360 –	–
Collector–Emitter Saturation Voltage (I <sub>C</sub> = –50 mA, I <sub>B</sub> = –5.0 mA)	V <sub>CE(sat)</sub>	–	–0.4	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = –50 mA, I <sub>B</sub> = –5.0 mA)	V <sub>BE(sat)</sub>	–	–0.95	Vdc

### SMALL–SIGNAL CHARACTERISTICS

Current–Gain — Bandwidth Product (I <sub>C</sub> = –10 mA, V <sub>CE</sub> = –20 V, f = 100 MHz)	f <sub>T</sub>	170	–	MHz
Output Capacitance (V <sub>CB</sub> = –5.0 V, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	–	4.5	pF
Input Capacitance (V <sub>EB</sub> = –0.5 V, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ib</sub>	–	11.5	pF
Small–Signal Current Gain (I <sub>C</sub> = –2.0 mA, V <sub>CE</sub> = 1.0 V, f = 1.0 kHz)	h <sub>fe</sub>	120	480	–
Noise Figure (I <sub>C</sub> = –100 µA, V <sub>CE</sub> = –5.0 V, R <sub>S</sub> = 1.0 kΩ, f = 1.0 kHz)	NF	–	4.0	dB

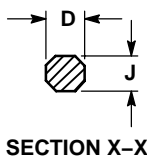
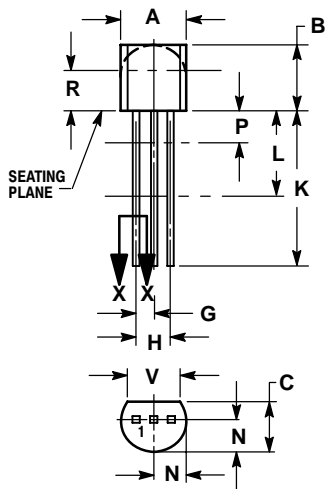
# MPS4126

## PACKAGE DIMENSIONS

### TO-92 (TO-226)

CASE 29-11

ISSUE AL




#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	---	12.70	---
L	0.250	---	6.35	---
N	0.080	0.105	2.04	2.66
P	---	0.100	---	2.54
R	0.115	---	2.93	---
V	0.135	---	3.43	---

#### STYLE 1:

1. PIN 1. EMITTER
2. BASE
3. COLLECTOR

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