

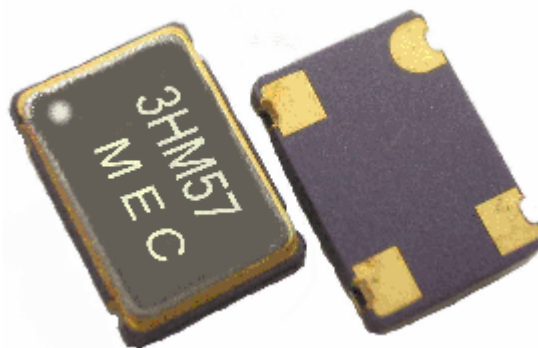
Low EMI Spread Spectrum Clock Oscillators

3HM57 Group "R"

**A DROP-IN REPLACEMENT SOLUTION FOR YOUR
EMI / EMC COMPLIANCE PROBLEM**

The principle sources of the EMI problem come from the system clocks. Therefore, rather than patch the problem with ferrite beads, EMI filters, ground plane and metal shielding, the most efficient and economic way to reduce the peak radiation energy is to use the low EMI clock oscillator.

Compared with the conventional clock oscillators, Mercury HM57 series spread spectrum (dithered) clock oscillators can reduce EMI as much as 12 dB. The beauty is it is a drop-in replacement for your existing 5x7 clock oscillator. No need to re-spin the board.



**3HM57 reduces your EMI
and shorten your time to market.**

Applications

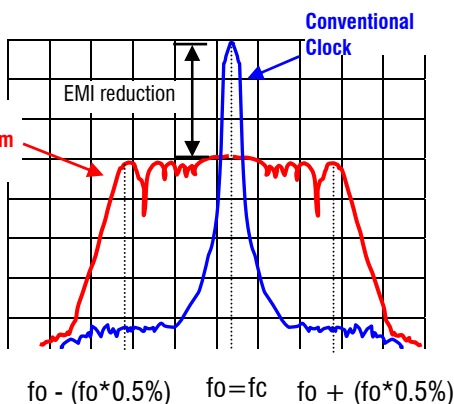
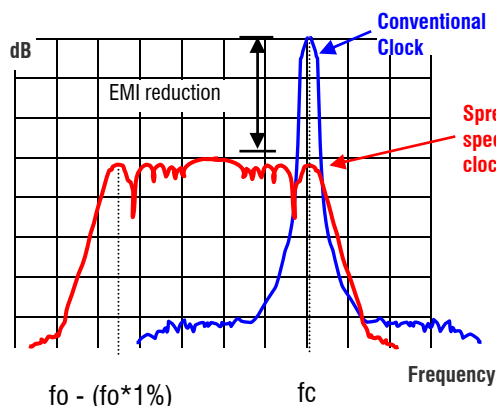
- Printers; Multiple function printers (MPCs)
- Digital copiers; PDAs
- Networking; LAN / WAN; Routers
- Storage systems (CD-ROM, VCD, DVD and HDD)
- Scanner; Modems; projectors
- Hand-held ID readers
- Embedded systems; Electrical musical instrument
- Automotive; GPS car navigation systems
- LCD PC monitors / LCD TVs
- ADSL; PCMCIA
- Still Digital cameras (SDCs)

Modulation Types

Output amplitude (dB) vs frequency span (MHz)

Down spread "D". "D1" as an example

Center spread "C". "C0.5" as an example



Spread Spectrum Clock (SSC):

Unlike the conventional clock, the mode energy of a spread spectrum clock is spread over a wider bandwidth, resulting from the **frequency modulation** technique. The modulation carrier frequency is in the KHz range which makes the modulation process transparent to the oscillator frequency. The controlled modulation process can be on all of one side of the nominal frequency (**down spread**) or 50% up and 50% down (**center spread**). The down spread is preferred if **over-clocking** is a problem to the system.

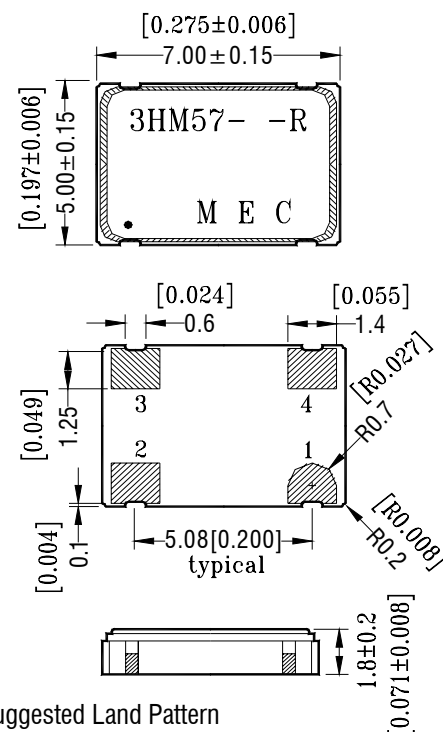
MERCURY www.mercury-crystal.com



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General Specifications: at $T_a = +25^\circ\text{C}$, $C_L = 15\text{ pF}$

Mercury Model	3HM57 Group "R"		
Frequency Range	5.0 ~ 160.0 MHz		
Spread Type	Total %	Down Spread (D)	Center Spread (C)
Spread Percentage (Part number suffix) (tolerance : $\pm 2\%$ of the total %)	1%	-1% (D1)	± 0.5 (C0.5)
	3%	-3% (D3)	± 1.5 (C1.5)
EMI Reduction (Reduction is applied to the entire spectrum)	-9 dBc min. 100 MHz at C0.5 -15 dBc min. 100 MHz at C1.5 With respect to the dB level when no modulation.		
Modulation Carrier Frequency (Dither rate)	6.9 KHz min.; 55.5 KHz max.; Frequency dependent. Call for details.		
Output Logic	CMOS Square Wave		
Input Voltage (V_{DD})	$V_{DD} = +3.3\text{ V D.C.} \pm 5\%$		
Frequency Stability (exclude modulation)	Commercial (0°C to $+70^\circ\text{C}$): "A": $\pm 25\text{ ppm}$; "B": $\pm 50\text{ ppm}$; "C": $\pm 100\text{ ppm}$		
	Industrial (-40°C to $+85^\circ\text{C}$): "D": $\pm 25\text{ ppm}$; "E": $\pm 50\text{ ppm}$; "F": $\pm 100\text{ ppm}$		
Output Voltage "High"; "1"	2.0 V min.; 3.2 V typical (at 90% V_{DD})		
Output Voltage "Low"; "0"	0.8 V max. ; 0.2 V typical (at 10% V_{DD})		
Rise Time / Fall Time	4 n sec. max. (10% $V_{DD} \leftrightarrow 90\% V_{DD}$)		
Load	15 pF		
Start-up Time	2 ms typical; 5 ms max.		
Stabilization Time	2 ms max.		
Current Consumption	10.0 MHz: 7 mA; 32.768 MHz: 8 mA		
	75.0 MHz: 17 mA; 125 MHz: 18 mA		
Duty Cycle	$50\% \pm 5\%$. ($C_L = 15\text{ pF}$; at 50% V_{DD})		
Cycle-to-cycle Jitter	$\pm 250\text{ ps}$ typical; $\pm 300\text{ ps}$ max.		
Output Impedance	40 ohms typical		
Static Discharge Voltage	$> 2000\text{ V}$ (per MIL-STD-883, method 3015)		
Storage Temperature	-65°C to $+150^\circ\text{C}$		
Aging	$\pm 5\text{ ppm}$ per year max.; $T_a = +25^\circ\text{C}$		
Packaging	16 mm tape and reel. 1000 pcs per reel		
Pin 1 Function	Output is high impedance when taken low. Output enable /disable time: 100 ns max.		



Pad Connections			
1	Tri-State	3	SSC Output
2	Ground	4	+3.3 V D.C.

Environmental Performance Specifications

Green Requirement	RoHS Compliant and Pb (lead) free
Storage temp. range	-55 to $+125^\circ\text{C}$
Humidity	85% RH, 85°C , 48 hours
Hermetic seal	Leak rate $2 \times 10^{-8}\text{ ATM-cm}^3/\text{sec}$ max.
Solderability	MIL-STD-202F method 208E
Reflow	260°C for 10 sec.
Vibration	MIL-STD-202F method 204, 35G, 50 to 2000 Hz
Shock	MIL-STD-202F method 213B, test condition. E, 1000GG $\frac{1}{2}$ sine wave

How to Order: Part number Example

“T”:Tri-State

Group

3HM57-BT-32.768R-C0.5

Frequency Stability Code

“A” ~ “F”. See table above

Frequency (MHz)

Spread Types and %	
Down Spread	
D1	-1%
D3	-3%
Center Spread	
C0.5	±0.5%
C1.5	±1.5%

Other Packages Available:

HM53: 5x3.2x1.2 mm 4 pad ceramic SMDs

HM14: Full size 4 pin DIPs

HM8: Half size 4 pin DIPs

HM42: 9.6x11.4x2.5 mm FR4 base leadless SMDs

HM44: 9.6x11.4x4.7 mm FR4 base leadless SMDs

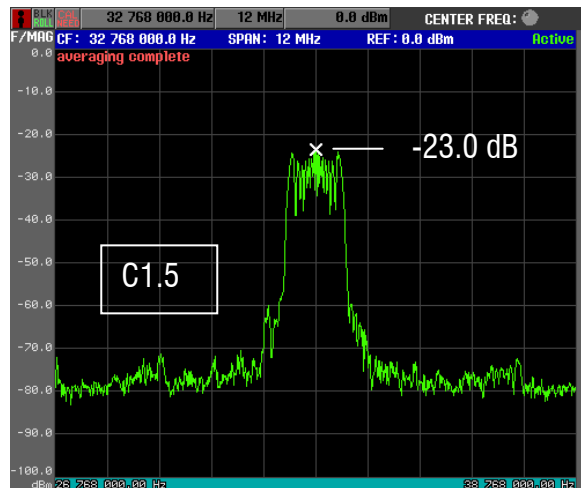
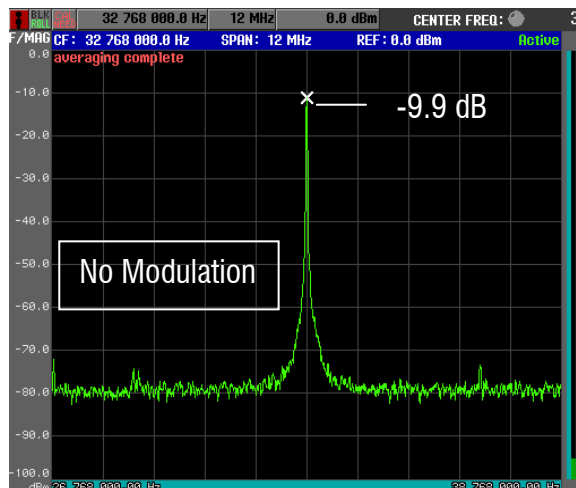
Please visit www.mercury-crystal.com

Instantaneous Frequencies (Example of 100 MHz)

If over-clocking is a problem to your system please choose down spread

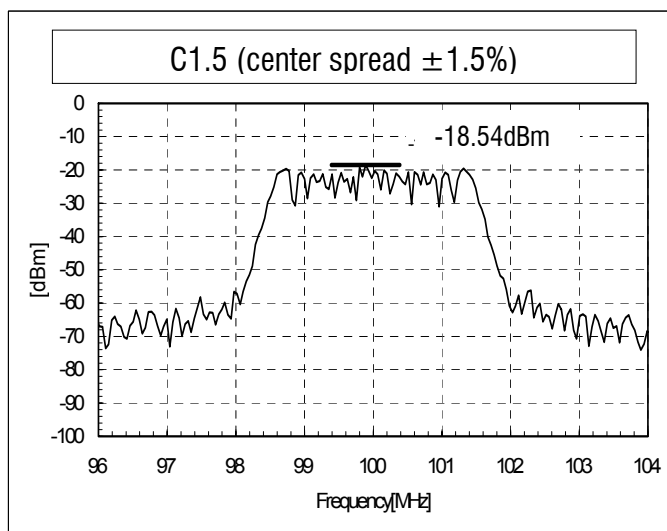
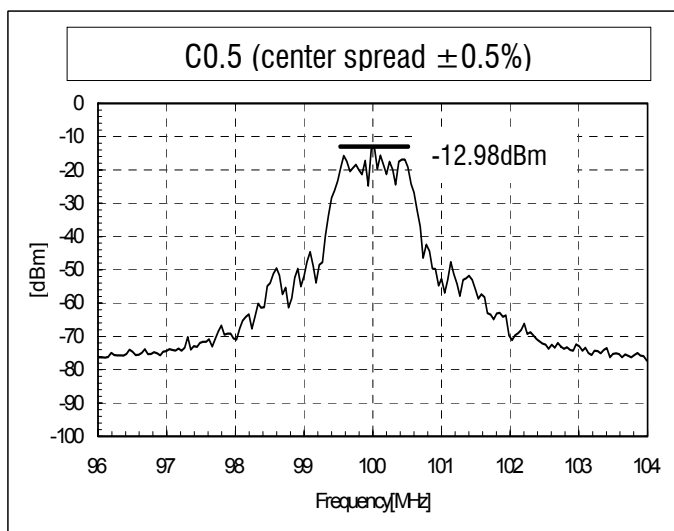
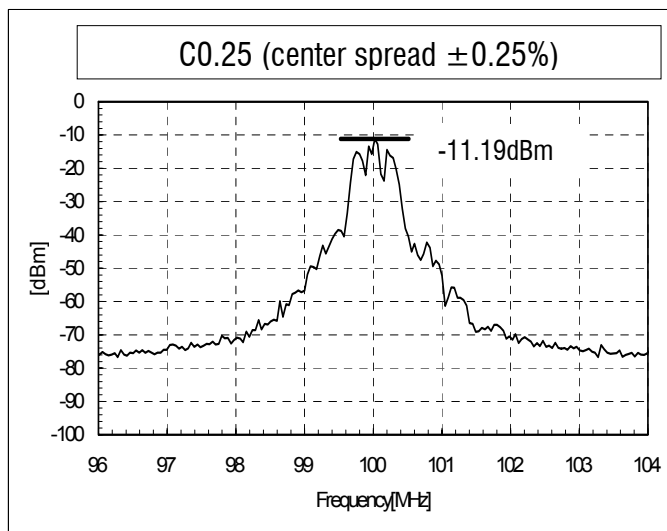
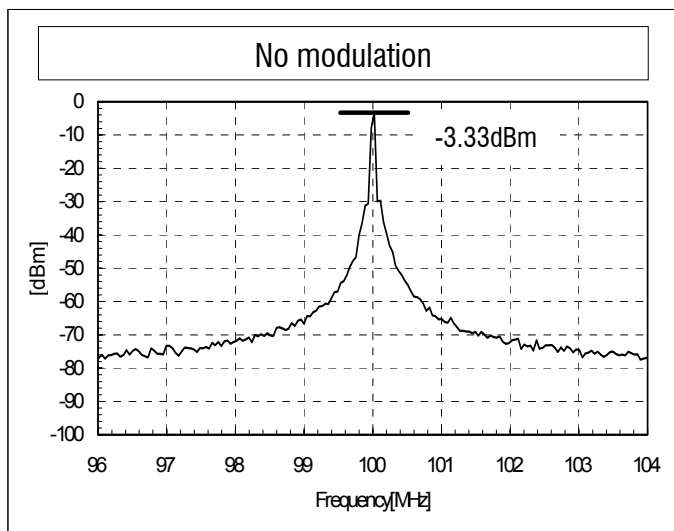
Total Spread %	Down Spread Instantaneous Frequency		Center Spread Instantaneous Frequency	
	min.	max.	min.	max.
	Down Range	Up Range	Down Range	Up Range
1 %	- 1%	0%	-0.5 %	+0.5%
	-10,000 ppm	0 ppm	-5000 ppm	+5000 ppm
	99.000000	100.000000	99.500000	100.500000
3 %	- 3.0%	0%	-1.5 %	+1.5%
	-30,000 ppm	0 ppm	-15,000 ppm	+15,000 ppm
	97.000000	100.000000	98.500000	101.500000

3HM57-32.768 at No Modulation and at Center Spread 1.5%: 13.1 dBc EMI reduction

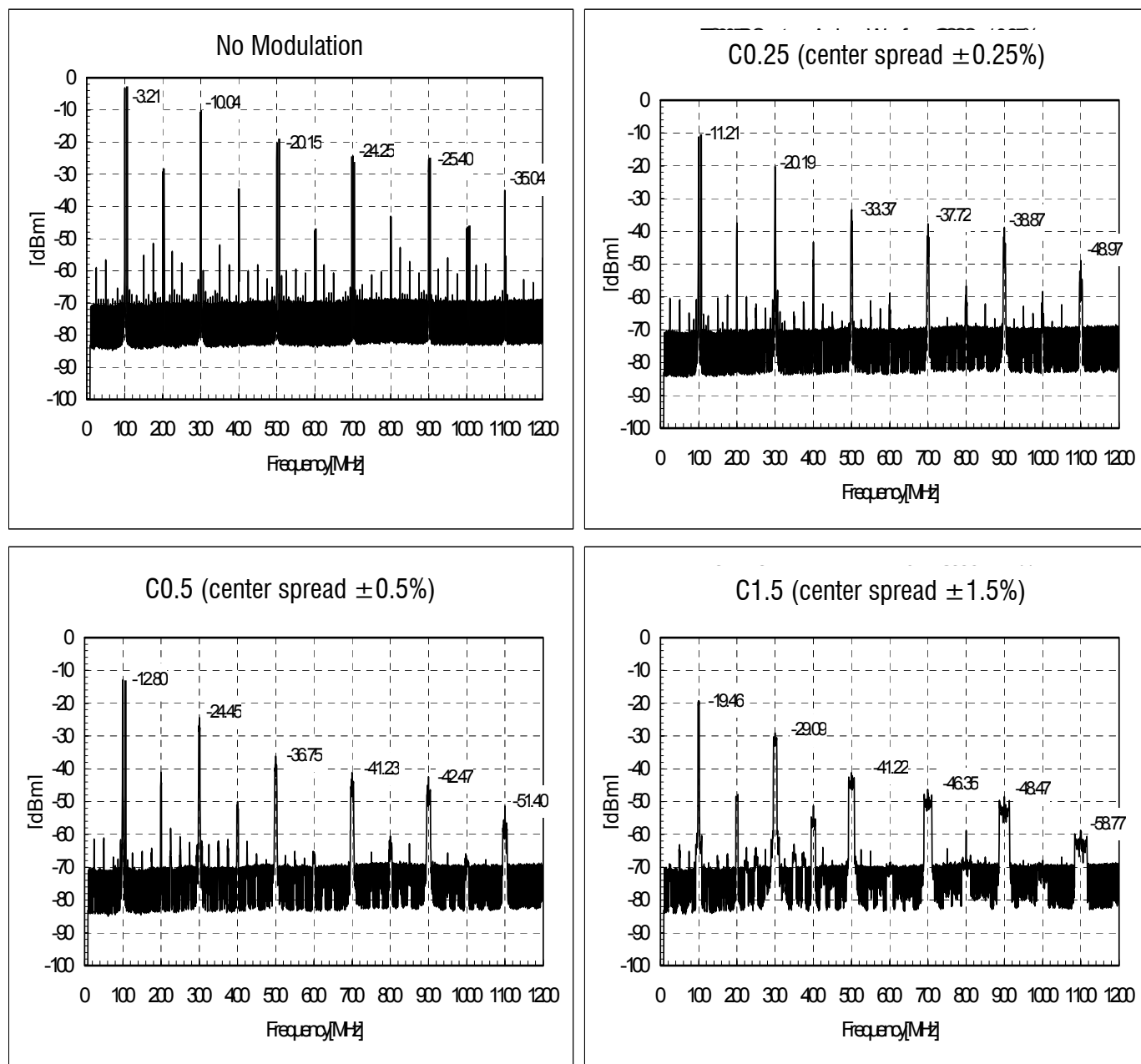


EMI Reduction of 3HM57 100 MHz at C0.5 and C1.5 (main mode only)

Modulation Carrier Frequency = 34.687 KHz



EMI Reduction of 3HM57 100 MHz at C0.5 and C1.5 (the whole spectrum)



For more technical information please visit www.mercury-crystal.com and download our technical note TN-020 (Title: "Low EMI Spread Spectrum Clock Oscillators").