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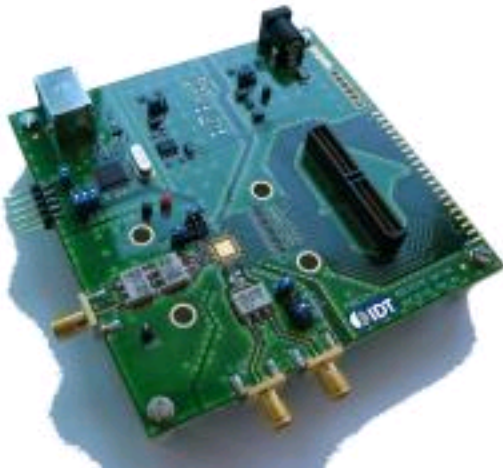Technical Support
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ADC1410S080F2
ADC1410S080F2 demoboard

IDTs ADC1410S080F2 demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVDS DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

Features

- SMA connector for clock and analog input signals
- LVDS/DDR outputs
- Single power supply (on-board regulators)

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
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🔒	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

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PRODUCT ID	TITLE
ADC1410S080HN	Single 14-bit ADC 80 Msps CMOS or LVDS DDR digital outputs

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