

# DATA SHEET

**LM111/211/311/311B**

Voltage comparator

Product data  
Supersedes data of 1994 Aug 31  
File under Integrated Circuits, IC11 Handbook

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# Voltage comparator

# LM111/211/311/311B

## DESCRIPTION

The LM111 series are voltage comparators that have input currents approximately a hundred times lower than devices like the  $\mu$ A710. They are designed to operate over a wider range of supply voltages; from standard  $\pm 15$  V op amp supplies down to a single 3 V supply. Their output is compatible with RTL, DTL, and TTL as well as MOS circuits. Further, they can drive lamps or relays, switching voltages up to 50 V at currents as high as 50mA.

Both the inputs and the outputs of the LM111 series can be isolated from system ground, and the output can drive loads referred to ground, the positive supply, or the negative supply. Offset balancing and strobe capability are provided and outputs can be wire-ORed.

Although slower than the  $\mu$ A710 (200 ns response time versus 40 ns), the devices are also much less prone to spurious oscillations. The LM111 series has the same pin configuration as the  $\mu$ A710 series.

## FEATURES

- Operates from single 3 V supply (LM311B)
- Maximum input bias current: 150 nA (LM311: 250 nA)
- Maximum offset current: 20 nA (LM311: 50 nA)
- Differential input voltage range:  $\pm 30$  V
- Power consumption: 135 mW at  $\pm 15$  V
- High sensitivity: 200 V/mV
- Zero crossing detector

## ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
8-Pin Plastic Small Outline Package (SO)	$-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	LM111D	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	$-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	LM111N	SOT97-1
8-Pin Plastic Small Outline Package (SO)	$-25^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	LM211D	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	$-25^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	LM211N	SOT97-1
8-Pin Plastic Small Outline Package (SO)	$0^{\circ}\text{C}$ to $+70^{\circ}\text{C}$	LM311D	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	$0^{\circ}\text{C}$ to $+70^{\circ}\text{C}$	LM311N	SOT97-1
8-Pin Plastic Small Outline Package (SO)	$0^{\circ}\text{C}$ to $+70^{\circ}\text{C}$	LM311BD	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	$0^{\circ}\text{C}$ to $+70^{\circ}\text{C}$	LM311BN	SOT97-1

## PIN CONFIGURATION

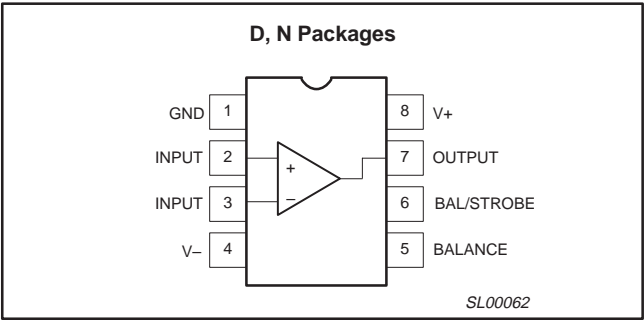


Figure 1. Pin Configuration

## APPLICATIONS

- Precision squarer
- Positive/negative peak detector
- Low voltage adjustable reference supply
- Switching power amplifier

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LM111/211/311/311B

## EQUIVALENT SCHEMATIC

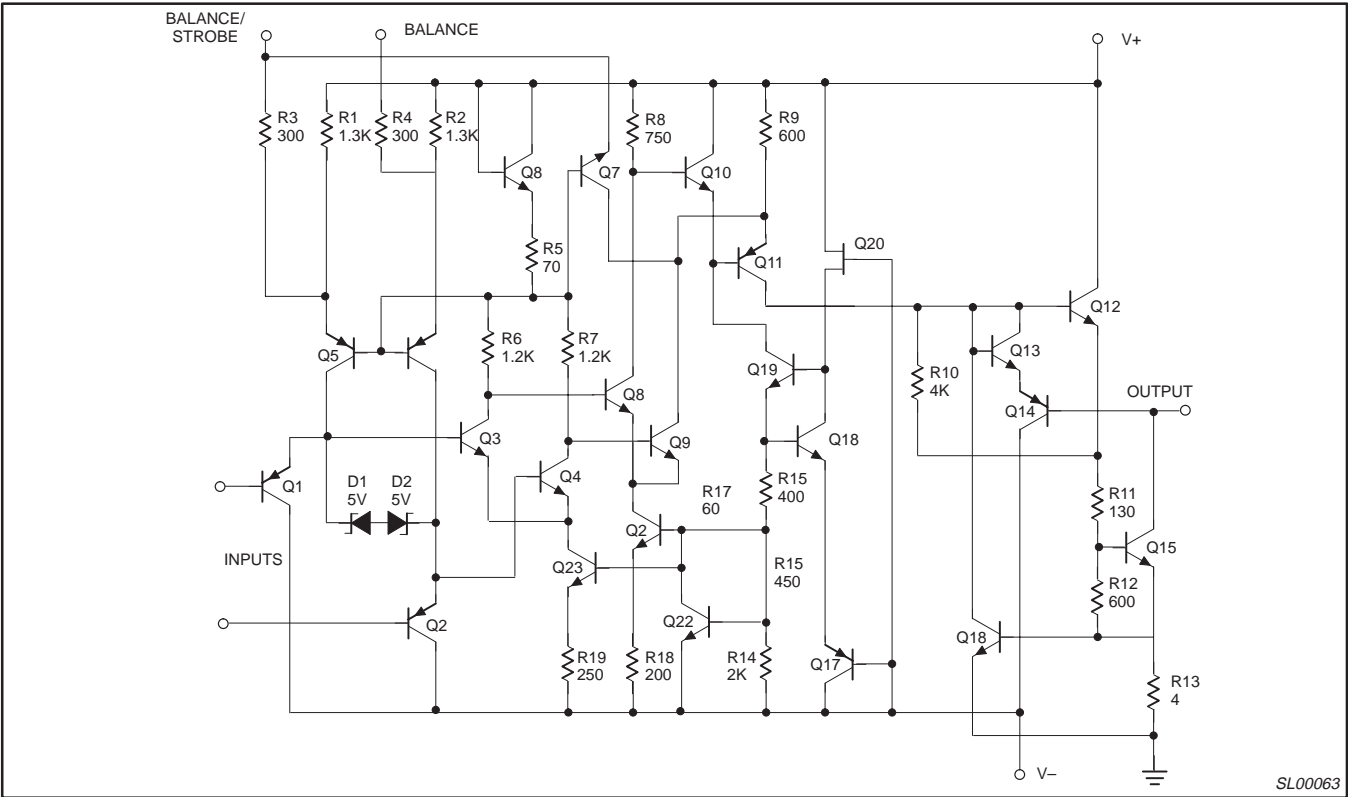


Figure 2. Equivalent Schematic

## ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
$V_S$	Total supply voltage	36	V
	Output to negative supply voltage: LM111/LM211	50	V
	LM311/LM311B	40	V
	Ground to negative supply voltage	30	V
	Differential input voltage	$\pm 30$	V
$V_{IN}$	Input voltage <sup>1</sup>	$\pm 15$	V
$P_{D\ MAX}$	Maximum power dissipation, $T_{amb} = 25\ ^\circ\text{C}$ (still-air) <sup>2</sup>		
	N package	1190	mW
	D package	780	mW
$I$	Output short-circuit duration	10	sec
$T_{amb}$	Operating ambient temperature range		
	LM111	-55 to +125	$^\circ\text{C}$
	LM211	-25 to +85	$^\circ\text{C}$
	LM311/LM311B	0 to +70	$^\circ\text{C}$
$T_{stg}$	Storage temperature range	-65 to +150	$^\circ\text{C}$
$T_{sld}$	Lead soldering temperature (10 sec max)	230	$^\circ\text{C}$

### NOTES:

- This rating applies for  $\pm 15\text{ V}$  supplies. The positive input voltage limit is 30 V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30 V below the positive supply, whichever is less.
- Derate above 25  $^\circ\text{C}$ , at the following rates:  
N package at 9.5 mW/ $^\circ\text{C}$   
D package at 6.2 mW/ $^\circ\text{C}$

## Voltage comparator

## LM111/211/311/311B

**DC ELECTRICAL CHARACTERISTICS<sup>1, 2, 3, 6</sup>**

Over temperature range unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	LM111/LM211			LM311			LM311B			UNIT
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V <sub>OS</sub>	Input offset voltage <sup>3</sup>	T <sub>amb</sub> = 25 °C; R <sub>S</sub> ≤ 50 kΩ		0.7	3.0		2.0	7.5		2.0	7.5	mV
I <sub>OS</sub>	Input offset current <sup>3</sup>	T <sub>amb</sub> = 25 °C		4.0	10		6.0	50		6	25	nA
I <sub>BIAS</sub>	Input bias current	T <sub>amb</sub> = 25 °C		60	100		100	250		100	200	nA
A <sub>V</sub>	Voltage gain	T <sub>amb</sub> = 25 °C		200			200			200		V/mV
	Response time <sup>4</sup>	T <sub>amb</sub> = 25 °C		200			200			500		ns
V <sub>SAT</sub>	Saturation voltage	LM111/211 V <sub>IN</sub> ≤ -5 mV; I <sub>OUT</sub> = 50 mA LM311/B V <sub>IN</sub> ≤ -10 mV; I <sub>OUT</sub> = 50 mA T <sub>amb</sub> = 25 °C		0.75	1.5		0.75	1.5		0.75	1.5	V
I <sub>BAL/STR</sub>	Strobe on current	T <sub>amb</sub> = 25 °C		3.0			3.0			3.0		mA
I <sub>LEAKAGE</sub>	Output leakage current <sup>6</sup>	LM111/211 V <sub>IN</sub> ≥ 5 mV; V <sub>OUT</sub> = 35 V LM311/B V <sub>IN</sub> ≥ 10 mV; V <sub>OUT</sub> = 35 V T <sub>amb</sub> = 25 °C, I <sub>STROBE</sub> = 3 mA (V <sub>-</sub> = V <sub>GND</sub> = -5 V)		0.2	10		0.2	50		0.2	50	nA
V <sub>OS</sub>	Input offset voltage <sup>3</sup>	R <sub>S</sub> ≤ 50 kΩ			4.0			10			10	mV
I <sub>OS</sub>	Input offset current <sup>3</sup>				20			70			50	nA
I <sub>BIAS</sub>	Input bias current				150			300			250	nA
V <sub>IN</sub>	Input voltage range	V = ±15 V (Pin 7 may go to 5 V)	-14.5	13.8 to -14.7	13.0	-14.5	13.8 to -14.7	13.0	V <sub>-</sub> +0.5		V <sub>+</sub> -1.5	V
V <sub>OL</sub>	Saturation voltage <sup>6</sup>	V <sub>+</sub> ≥ 4.5 V, V <sub>-</sub> = 0 V LM111/211 V <sub>IN</sub> ≤ -6 mV; I <sub>SINK</sub> ≤ 8 mA LM311/B V <sub>IN</sub> ≤ -10 mV; I <sub>SINK</sub> ≤ 8 mA		0.23	0.4		0.23	0.4		0.23	0.4	V
I <sub>OH</sub>	Output leakage current	V <sub>IN</sub> ≥ 5 mV; V <sub>OUT</sub> = 35 V		0.1	0.5							μA
I <sub>CC</sub>	Positive supply current	T <sub>amb</sub> = 25 °C		5.1	6.0		5.1	7.5		1.6	3.5	mA
I <sub>EE</sub>	Negative supply voltage	T <sub>amb</sub> = 25 °C		4.1	5.0		4.1	5.0				mA

**NOTES:**

1. This rating applies for ±15 V supplies. The positive input voltage limit is 30 V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30 V below the positive supply, whichever is less.
2. These specifications apply for V<sub>S</sub> = ±15 V and 0 °C < T<sub>amb</sub> < 70 °C unless otherwise specified. With the LM211, however, all temperature specifications are limited to -25 °C ≤ T<sub>amb</sub> ≤ +85 °C, and for the LM111 is limited to -55 °C < T<sub>amb</sub> < +125 °C. The offset voltage, offset current, and bias current specifications apply for any supply voltage from a single 5 V supply up to ±15 V supplies.
3. The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with 1 mA load. Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.
4. The response time specified is for a 100 mV input step with 5 mV over-drive.
5. Do not short the strobe pin to ground; it should be current driven at 3 mA to 5 mA.
6. LM311B, all parameters are at V<sub>+</sub> = 3 V ±10%; V<sub>-</sub> = GND = 0 V.

## Voltage comparator

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## TYPICAL APPLICATIONS

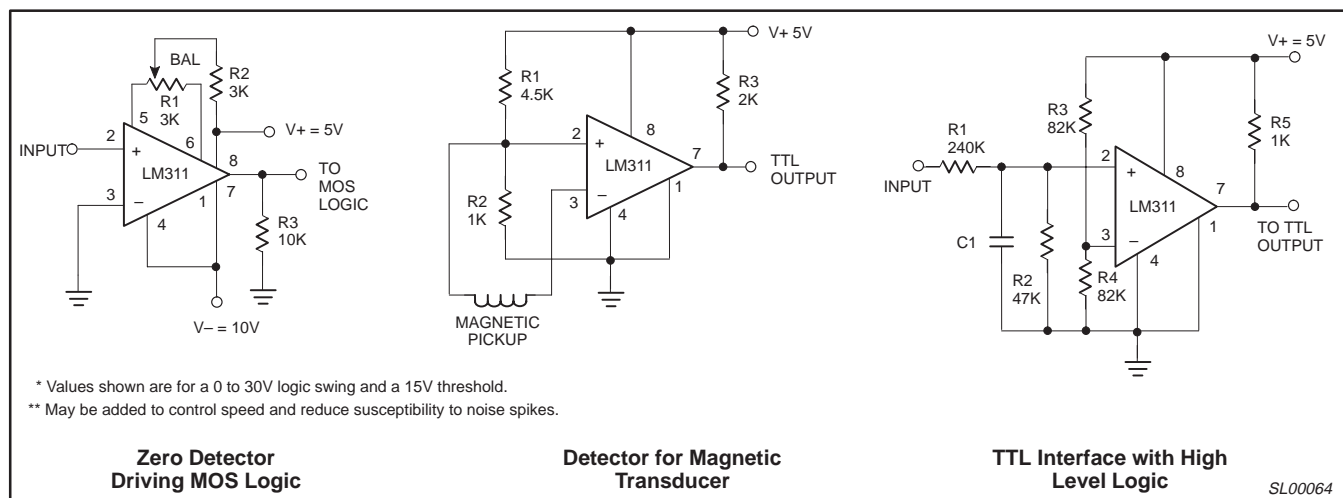


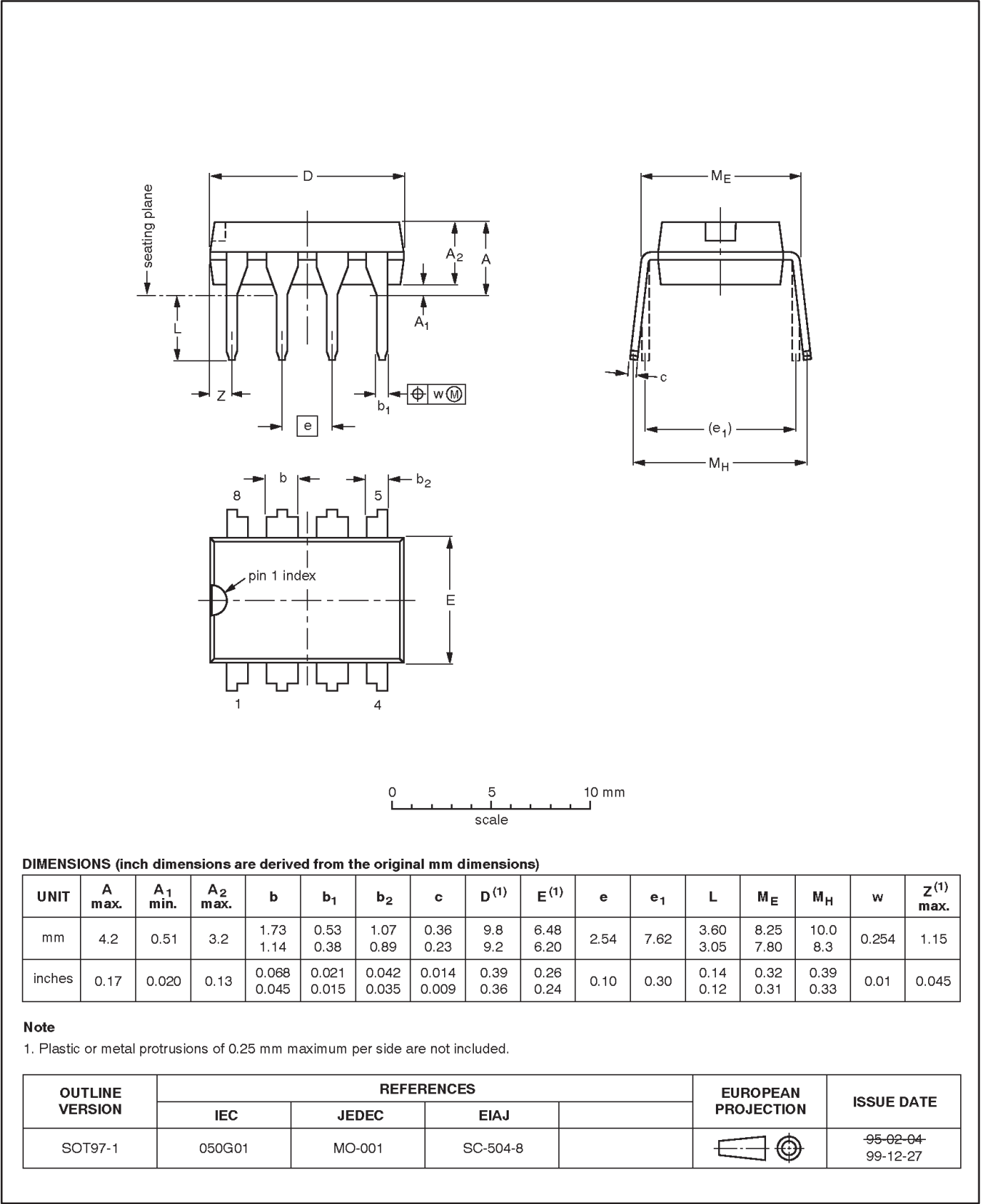
Figure 3. Typical Applications

Voltage comparator

LM111/211/311/311B

DIP8: plastic dual in-line package; 8 leads (300 mil)

SOT97-1

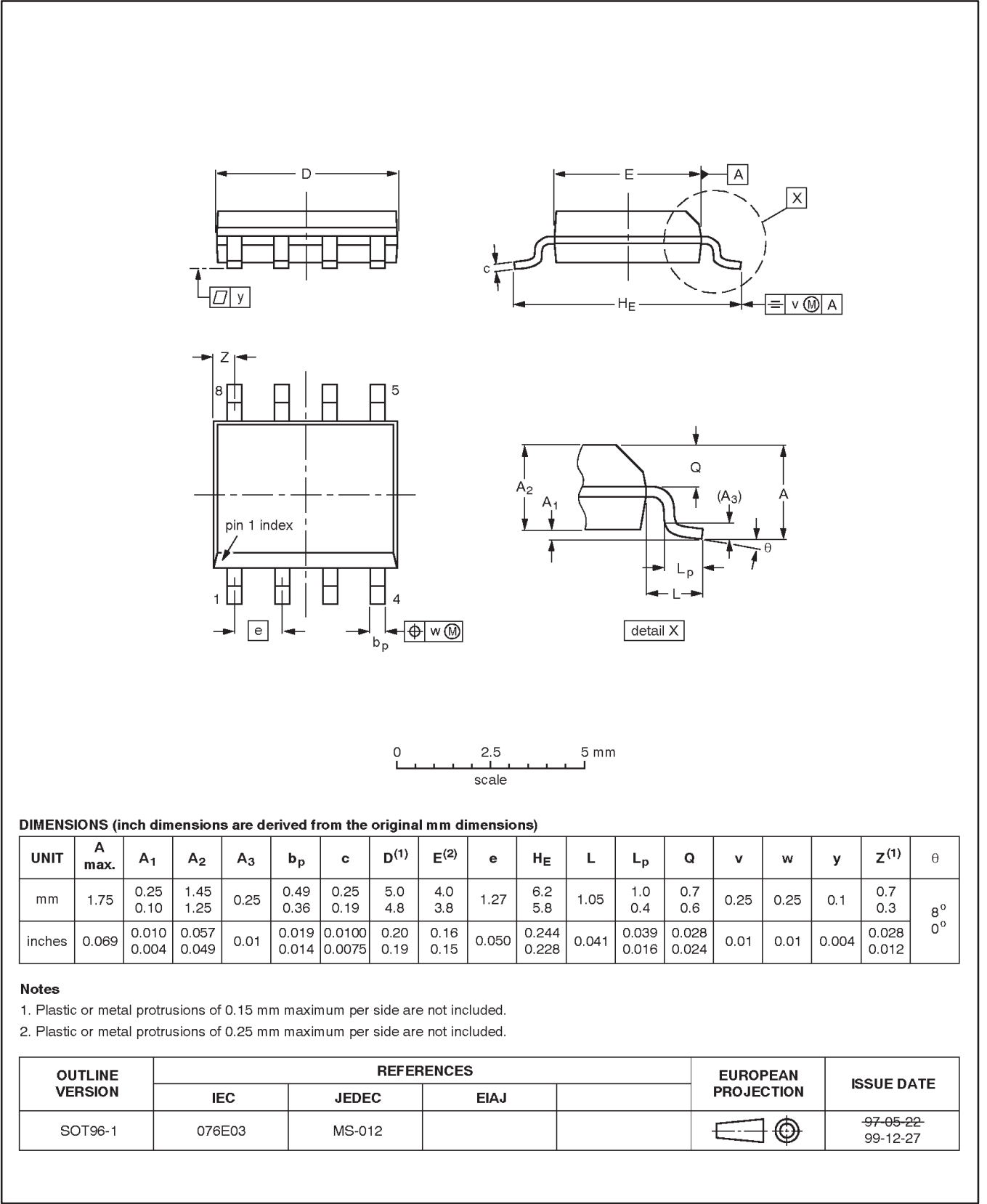


Voltage comparator

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SO8: plastic small outline package; 8 leads; body width 3.9 mm

SOT96-1



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## Data sheet status

Data sheet status <sup>[1]</sup>	Product status <sup>[2]</sup>	Definitions
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