

To : _____

Specification of FDTC TFT-LCD module

FLC48SXC8V-05

Approval
Date : By :

This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. Fujitsu shall not be liable against the Customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission.

Specification No. : Tech Bes LCD-00161

Issue Date : September 13, 2002

Issued by :

F. Yamada
Director
Products Engineering Dept. LCD product Div.

FUJITSU DISPLAY TECHNOLOGIES CORPORATION

REVISION HISTORY

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TENTATIVE**1. APPLICATION**

This specification is applied to the 19-inch SXGA supported TFT-LCD module.

2. PRODUCT NAME AND MODEL NUMBER

2-1 Product Name : **LCD Module**

2-2 Model Name : FLC48SXC8V-05

2-3 Drawing Number : NA19020-C971

3. OVERVIEW

This LCD module has a TFT active matrix type liquid crystal panel 1280x1024 pixels, and diagonal size of 48cm(19-inch). This LCD has a LVDS dual interface and can display 16,777,216 colors.

The power supply of this LCD module is +5V DC voltage.

This module has the characteristics for applying TCO'99.

4. CONFIGURATION

This LCD module consists of a color TFT-LCD panel that is mounted with TFT driver ICs and a cold-cathode fluorescent tube back-light.

The inverter for the back-light is not included.

Figure 4-1 shows a block diagram of this LCD module.

DOCUMENT CONTROL SECTION

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A	Input signals (LVDS Dual) RXO0+ RXE0+ RXO0- RXE0- RXO1+ RXE1+ RXO1- RXE1- RXO2+ RXE2+ RXO2- RXE2- RXO3+ RXE3+ RXO3- RXE3- RXOC+ RXEC+ RXOC- RXEC- PD Vcc (+5V) LVDS Receiver Internal Circuit DC/DC converter Bias voltage Supply circuit Gate Driver Data driver TFT color LCD panel 1280x3(RGB)x1024 Back-light (6 CCFL)					A																													
B						B																													
C	Power supply (Separate) Inverter circuit					C																													
Figure 4-1 Block Diagram																																			
5. MECHANICAL SPECIFICATIONS																																			
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A 8. ELECTRICAL SPECIFICATIONS Table 8-1 shows the electrical specifications of this LCD module. Figure 8-1 shows the measurement circuit. Figure 8-2(A) shows the equivalent circuit of the logic signal input area. Figure 8-2(B) shows the equivalent circuit of the supply voltage Input area. A																																																																																																									
B Table 8-1 Electrical Specifications <table border="1" style="width:100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 25%;">Item</th> <th style="width: 10%;">Symbol</th> <th style="width: 15%;">Condition</th> <th style="width: 10%;">MIN.</th> <th style="width: 10%;">TYP.</th> <th style="width: 10%;">MAX.</th> <th style="width: 10%;">Unit</th> <th style="width: 10%;">Remark</th> </tr> </thead> <tbody> <tr> <td>Differential-input Voltage (Hign)</td> <td>V_{IH}</td> <td rowspan="2">V_{CM}=+1.2V</td> <td>—</td> <td>—</td> <td>100</td> <td>mV</td> <td></td> </tr> <tr> <td>Differential-input Voltage (Low)</td> <td>V_{IL}</td> <td>-100</td> <td>—</td> <td>—</td> <td>mV</td> <td></td> </tr> <tr> <td>Input PD Voltage (High)</td> <td>V_{IHPD}</td> <td rowspan="4">V_{CC}=+5.0±0.25V V_{SS}=0V DCLK=54MHz Ta=25° C</td> <td>2.0</td> <td>—</td> <td>3.3</td> <td>V</td> <td></td> </tr> <tr> <td>Input PD Voltage (Low)</td> <td>V_{ILPD}</td> <td>0</td> <td>—</td> <td>0.8</td> <td>V</td> <td></td> </tr> <tr> <td>Supply Current</td> <td>I_{CC}</td> <td>—</td> <td>800</td> <td>1,500</td> <td>mA</td> <td>*1</td> </tr> <tr> <td>Supply Rush Current</td> <td>I_{SCC}</td> <td>—</td> <td>—</td> <td>3.5</td> <td>A</td> <td rowspan="2">*2</td> </tr> <tr> <td>Supply Rush Current Duration(1.5A excess)</td> <td>T_{SCC}</td> <td>—</td> <td>—</td> <td>1</td> <td>ms</td> </tr> <tr> <td rowspan="4" style="writing-mode: vertical-rl; transform: rotate(180deg);">BACK LIGHT (*3)</td> <td rowspan="2">CCFL Turn on Voltage</td> <td>V_S</td> <td>f_L=50kHz,Ta=25°C</td> <td>—</td> <td>1,400</td> <td>1,600</td> <td rowspan="2">V_{rms}</td> <td></td> </tr> <tr> <td></td> <td>f_L=50kHz,Ta=0°C</td> <td>—</td> <td>—</td> <td>1,600</td> <td></td> </tr> <tr> <td>Lighting Voltage</td> <td>V_L</td> <td>f_L=50kHz I_L=7mA</td> <td>—</td> <td>750</td> <td>—</td> <td>V_{rms}</td> <td></td> </tr> <tr> <td>Lighting Frequency</td> <td>f_L</td> <td>V_L=750V_{rms}</td> <td>40</td> <td>50</td> <td>60</td> <td>kHz</td> <td></td> </tr> <tr> <td></td> <td>Tube Current</td> <td>I_L</td> <td>f_L=50kHz V_L=750V_{rms}</td> <td>4</td> <td>—</td> <td>7</td> <td>mArms</td> <td>*4</td> </tr> </tbody> </table> B								Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remark	Differential-input Voltage (Hign)	V _{IH}	V _{CM} =+1.2V	—	—	100	mV		Differential-input Voltage (Low)	V _{IL}	-100	—	—	mV		Input PD Voltage (High)	V _{IHPD}	V _{CC} =+5.0±0.25V V _{SS} =0V DCLK=54MHz Ta=25° C	2.0	—	3.3	V		Input PD Voltage (Low)	V _{ILPD}	0	—	0.8	V		Supply Current	I _{CC}	—	800	1,500	mA	*1	Supply Rush Current	I _{SCC}	—	—	3.5	A	*2	Supply Rush Current Duration(1.5A excess)	T _{SCC}	—	—	1	ms	BACK LIGHT (*3)	CCFL Turn on Voltage	V _S	f _L =50kHz,Ta=25°C	—	1,400	1,600	V _{rms}			f _L =50kHz,Ta=0°C	—	—	1,600		Lighting Voltage	V _L	f _L =50kHz I _L =7mA	—	750	—	V _{rms}		Lighting Frequency	f _L	V _L =750V _{rms}	40	50	60	kHz			Tube Current	I _L	f _L =50kHz V _L =750V _{rms}	4	—	7	mArms	*4
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C (*1) Typical current situation : Color bar pattern. V_{CC}=5.0V Maximum current situation : White pattern. V_{CC}=4.75V Without rush current. (*2) These items prescribe the rush current for starting internal DC/DC. Charging current to capacitors of V_{CC} is not prescribed. (*3) Back-light specifications are valid when using a suitable inverter such as the T.B.D. (*4) Tube current (I_L) shows the value of the current that is consumed at one lamp. This LCD module has 6 lamps. Each 3 lamps are placed at upper side and lower side of the display. 3 lamps is connected independently. Each low voltage terminals are connected with separate cable to Back-light connector. C																																																																																																									
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Measurement circuit is based on Figure 8-1.

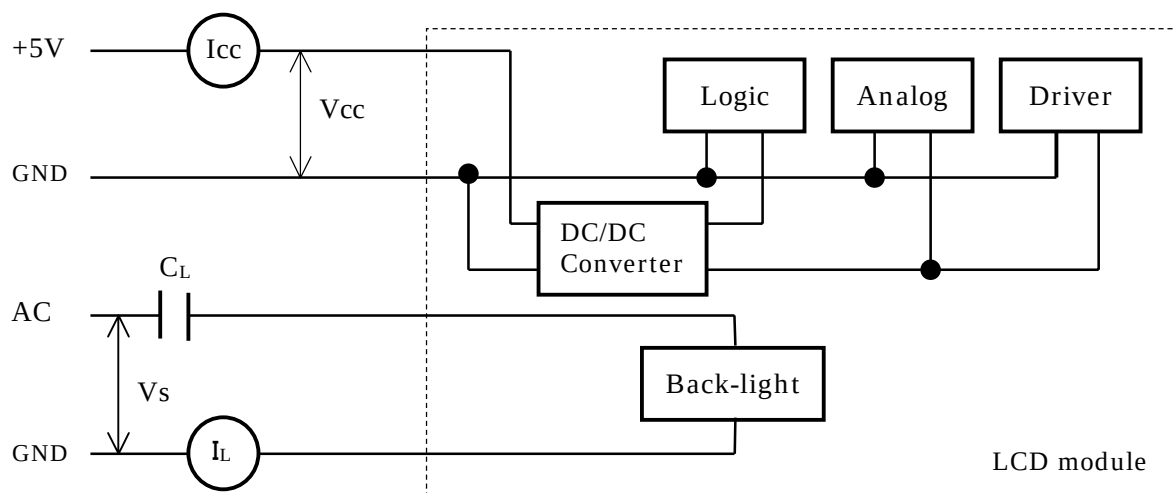
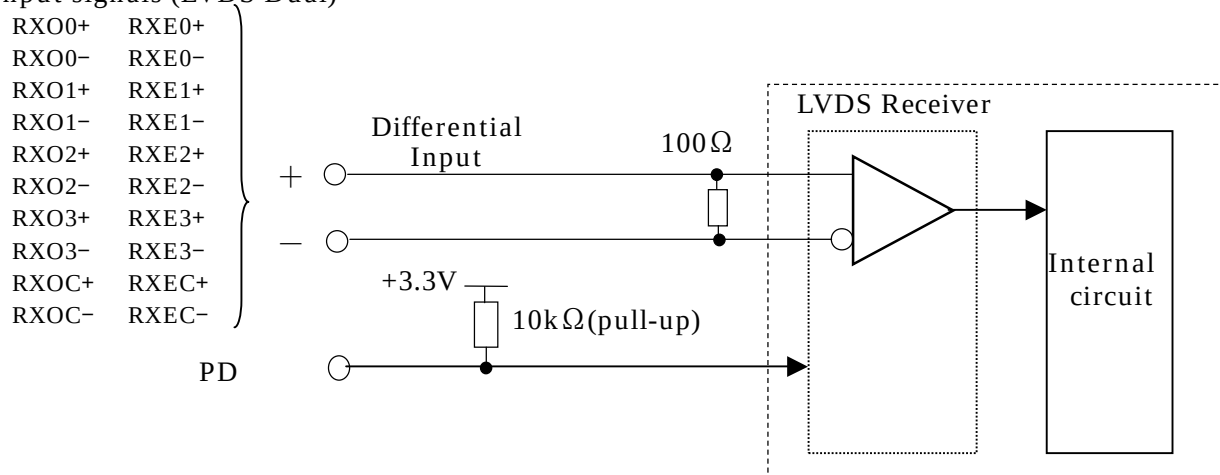


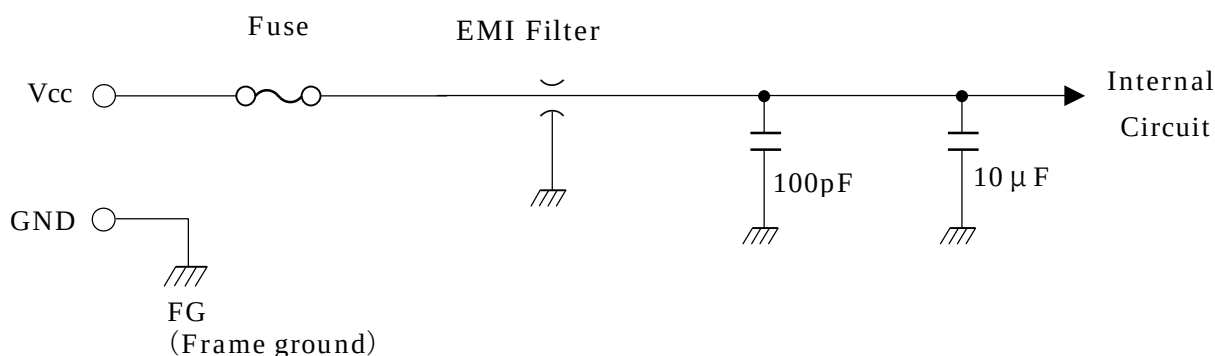
Figure 8-1 Measurement circuit

Input signals (LVDS Dual)



LVDS Receiver : DS90CF386 (National Semiconductor Corp. or equivalent)

Figure 8-2(A) Equivalent circuit of logic signal Input



Fuse : F0603C3R00FWTRM 3.0A (Kyocera Corp. or equivalent)

EMI Filter : SGM20C1E332 (Sumitomo Metal Inc. or equivalent)

Figure 8-2(B) Equivalent circuit of power supply

Fuse : F0603C3R00FWTRM 3.0A (Kyocera Corp. or equivalent)
 EMI Filter : SGM20C1E332 (Sumitomo Metal Inc. or equivalent)

Figure 8-2(B) Equivalent circuit of power supply

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9. OPTICAL SPECIFICATIONS Table 9-1 shows the optical specifications of this LCD module.															
Table 9-1 Optical Specifications Ta=25°C, Signal Timing=Typ.															
Item		Symbol	Condition		Specifications			Unit	Remark						
					MIN.	TYP.	MAX.								
Visual Angle	Horizontal	$\theta_{L,R}$	CR□10	$\theta_{U,D}=0^{\circ}$	85	—	—	deg		(1)(2) (3)(5) (6)					
	Vertical	$\theta_{U,D}$		$\theta_{L,R}=0^{\circ}$	85	—	—	deg							
	All Direction	θ			—	80	—	deg							
Contrast Ratio		CR	$\theta_{L,R,U,D}=0^{\circ}$		400	600	—	—	White/Black	(1)(2) (3)(5)					
Response Time(ON) (B→W)		t_{on}	$\theta_{L,R,U,D}=0^{\circ}$	Ta=25°C	—	15	30	ms		(1) (4) (5)					
				Ta=0°C	—	50	100	ms							
Response Time(OFF) (W→B)		t_{off}	$\theta_{L,R,U,D}=0^{\circ}$	Ta=25°C	—	10	25	ms							
				Ta=0°C	—	50	100	ms							
Brightness		I	$\theta_{L,R,U,D}=0^{\circ}$ $V_{CC}=5V$ $I_L=7mA$		(360)	(450)		cd/m ²	White*1	(1)(5)					
Brightness Uniformity		□I			70	—	—	%		(1)(5) (7)					
Chromaticity	W	x			(0.293)	(0.323)	(0.353)	—			(1) (5)				
		y			(0.307)	(0.337)	(0.367)	—							
	R G B	(x, y)	Red	(0.648 , 0.346) Typ.											
			Green	(0.292 , 0.602) Typ.											
Blue			(0.150 , 0.130) Typ.												
LCD Panel Type					TFT Color										
Display Mode					Normaly Black										
Wide Viewing Angle Technology					MVA										
Optimum Viewing Angle					— (symmentry)					(6)					
Display Color					16,777,216 (8-bit color)										
Color of non-display area					Black										
Surface Treatment					Anti-glare (Haze value: (25%), 2H)										
(*1) Value at 15~20 minutes after lighting on. (Note) •CS-1000 (MINOLTA Co., Ltd.) , BM-5A(Topcon) and the like should be used as a luminance colorimeter. Field=1°, L=500mm • Back-light current = 7mA, Dark room condition(1 lux or less)															
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Note 4) Definition of Response Time

Based on Figure 9-4.

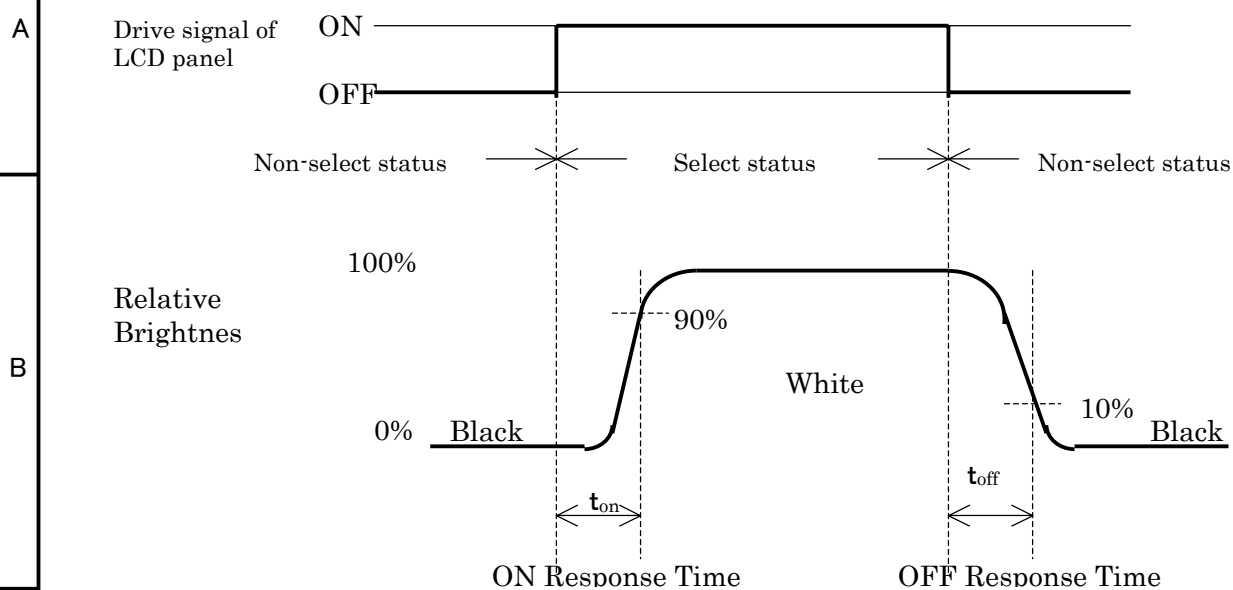


Figure 9-4 Definition of Response Time

Note 5) Contrast Ratio and Response Measurement System

Based on Figure 9-5.

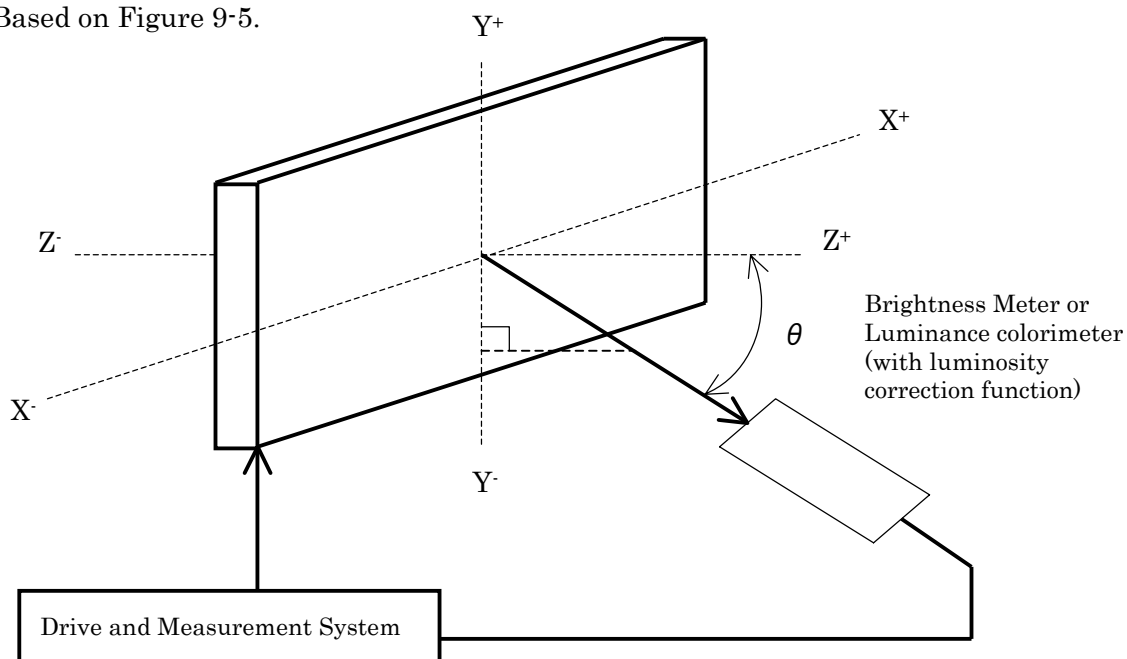


Figure 9-5 Contrast Ratio and Response Time Measurement System

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Note 6) Definition of Optimum Viewing Angle

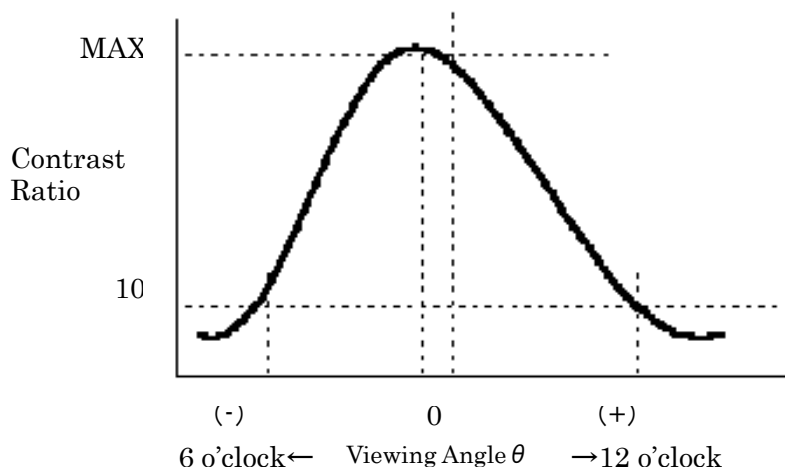


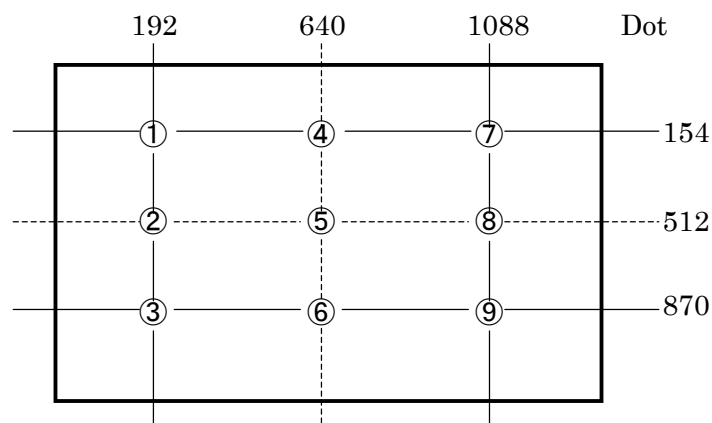
Figure 9-6 Definition of Viewing Angle

Note 7) Definition of Brightness Uniformity

Brightness uniformity is defined by the following formula.

Brightness (I1~I9) art measured at the following 9 points (□~□) on the display area that is shown in Figure 9-7.

$$\text{Brightness Uniformity } (\square L) = \frac{|\text{Min. In }|}{|\text{Max. In }|} \times 100 (\%) , n = 1 \text{ to } 9$$



Note) Each measurement point (□~□) defines the center spot of view of Brightness Meter.
The tolerance of measurement position is ± 3 mm.

Figure 9-7 Measurement Points

						TITLE		FLC48SXC8V-05	
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10. INTERFACE SPECIFICATIONS							
10-1 Signal descriptions							
Table 10-1 shows the description and configuration of interface signals (CN1).							
Table 10-1 Interface signals (CN1)							
Pin No.		Symbol		I/O		Function	
1		RxO0-		I		Negative differential input	
2		RxO0+		I		Positive differential input	
3		RxO1-		I		Negative differential input	
4		RxO1+		I		Positive differential input	
5		RxO2-		I		Negative differential input	
6		RxO2+		I		Positive differential input	
7		GND		—		Ground	
8		RxOC-		I		Negative differential input	
9		RxOC+		I		Positive differential input	
10		RxO3-		I		Negative differential input	
11		RxO3+		I		Positive differential input	
12		RxEO-		I		Negative differential input	
13		RxEO+		I		Positive differential input	
14		GND		—		Ground	
15		RxE1-		I		Negative differential input	
16		RxE1+		I		Positive differential input	
17		GND		—		Ground	
18		RxE2-		I		Negative differential input	
19		RxE2+		I		Positive differential input	
20		RxEC-		I		Negative differential input	
21		RxEC+		I		Positive differential input	
22		RxE3-		I		Negative differential input	
23		RxE3+		I		Positive differential input	
24		GND		—		Ground	
25		TST		—		Test pin *1	
26		PD		I		LVDS Core Power Down	
27		TST		—		Test pin *1	
28		Vcc		—		+5V power supply	
29		Vcc		—		+5V power supply	
30		Vcc		—		+5V power supply	
Connector : FI-X30S-HF (Japan Aviation Electronics)							
User's connector : FI-X30M (FPC type) (Japan Aviation Electronics)							
FI-X30H (Wire type)							
FI-X30C (Coaxial cable type)							
*1: Keep open. (Internal test use only.)							
DATE		DESIGN		CHECK		APPROVE	
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DESIGN		CHECK		APPROVE		DESCRIPTION	
FUJITSU DISPLAY TECHNOLOGIES CORPORATION		12/		CUST.		TITLE	
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10-2 LVDS Data Assignment

Table 10-2 shows the LVDS Data Assignment.

Table 10-2 LVDS Data Assignment

Input signal *1		Transmitter DS90CF383,C385		Interface connector			Receiver DS90CF386		LCD Control input	
		pin	INPUT	System side	LCD module		pin	OUTPUT		
					pin					
LVDS Odd	RO2	51	TxIN0	Tx OUT0+	2	RxO0+	27	RxOUT0	RO2	
	RO3	52	TxIN1				29	RxOUT1	RO3	
	RO4	54	TxIN2				30	RxOUT2	RO4	
	RO5	55	TxIN3				32	RxOUT3	RO5	
	RO6	56	TxIN4	Tx OUT0-	1	RxO0-	33	RxOUT4	RO6	
	RO7	3	TxIN6				35	RxOUT6	RO7	
	GO2	4	TxIN7				37	RxOUT7	GO2	
	GO3	6	TxIN8				38	RxOUT8	GO3	
	GO4	7	TxIN9	Tx OUT1+	4	RxO1+	39	RxOUT9	GO4	
	GO5	11	TxIN12				43	RxOUT12	GO5	
	GO6	12	TxIN13				45	RxOUT13	GO6	
	GO7	14	TxIN14				46	RxOUT14	GO7	
	BO2	15	TxIN15	Tx OUT1-	3	RxO1-	47	RxOUT15	BO2	
	BO3	19	TxIN18				51	RxOUT18	BO3	
	BO4	20	TxIN19				53	RxOUT19	BO4	
	BO5	22	TxIN20				54	RxOUT20	BO5	
	BO6	23	TxIN21	Tx OUT2+	6	RxO2+	55	RxOUT21	BO6	
	BO7	24	TxIN22				1	RxOUT22	BO7	
	RSVD	27	TxIN24				3	RxOUT24	Not use	
	RSVD	28	TxIN25				5	RxOUT25	Not use	
	ENAB	30	TxIN26	Tx OUT2-	5	RxO2-	6	RxOUT26	ENAB	
	RO0	50	TxIN27				7	RxOUT27	RO0	
	RO1	2	TxIN5				34	RxOUT5	RO1	
	GO0	8	TxIN10	Tx OUT3+	11	RxO3+	41	RxOUT10	GO0	
	GO1	10	TxIN11				42	RxOUT11	GO1	
	BO0	16	TxIN16				49	RxOUT16	BO0	
	BO1	18	TxIN17				50	RxOUT17	BO1	
	RSVD	25	TxIN23				2	RxOUT23	Not use	
		DCLK	31	TxCLK IN	TxCLK OUT+ TxCLK OUT-	9 8	RxCLK IN+ RxCLK IN-	26	RxCLK OUT	DCLK
	LVDS Even	RE2	51	TxIN0	Tx OUT0+	13	RxEO+	27	RxOUT0	RE2
RE3		52	TxIN1	29				RxOUT1	RE3	
RE4		54	TxIN2	30				RxOUT2	RE4	
RE5		55	TxIN3	32				RxOUT3	RE5	
RE6		56	TxIN4	Tx OUT0-	12	RxEO-	33	RxOUT4	RE6	
RE7		3	TxIN6				35	RxOUT6	RE7	
GE2		4	TxIN7				37	RxOUT7	GE2	
GE3		6	TxIN8				38	RxOUT8	GE3	
GE4		7	TxIN9	Tx OUT1+	16	RxE1+	39	RxOUT9	GE4	
GE5		11	TxIN12				43	RxOUT12	GE5	
GE6		12	TxIN13				45	RxOUT13	GE6	
GE7		14	TxIN14				46	RxOUT14	GE7	
BE2		15	TxIN15	Tx OUT1-	15	RxE1-	47	RxOUT15	BE2	
BE3		19	TxIN18				51	RxOUT18	BE3	
BE4		20	TxIN19				53	RxOUT19	BE4	
BE5		22	TxIN20				54	RxOUT20	BE5	
BE6		23	TxIN21	Tx OUT2+	19	RxE2+	55	RxOUT21	BE6	
BE7		24	TxIN22				1	RxOUT22	BE7	
RSVD		27	TxIN24				3	RxOUT24	Not use	
RSVD		28	TxIN25				5	RxOUT25	Not use	
RSVD		30	TxIN26	Tx OUT2-	18	RxE2-	6	RxOUT26	Not use	
RE0		50	TxIN27				7	RxOUT27	RE0	
RE1		2	TxIN5				34	RxOUT5	RE1	
GE0		8	TxIN10	Tx OUT3+	23	RxE3+	41	RxOUT10	GE0	
GE1		10	TxIN11				42	RxOUT11	GE1	
BE0		16	TxIN16				49	RxOUT16	BE0	
BE1		18	TxIN17				50	RxOUT17	BE1	
RSVD		25	TxIN23				2	RxOUT23	Not use	
		DCLK	31	TxCLK IN	TxCLK OUT+ TxCLK OUT-	21 20	RxCLK IN+ RxCLK IN-	26	RxCLK OUT	Not use

*1 ·RSVD (reserved) pin on a transmitter should be connected with Ground.

·Input odd or even data depending on the display position of the LCD module.

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TENTATIVE10-3 Color Data Assignment

Table 10-3 shows the Color Data Assignment.

Table 10-3 Color Data Assignment

Color			R Input data								G Input data								B Input data							
	Odd		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
	Even		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red		1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta		1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
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		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter	253	1	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
↓	254	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Red	255	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
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	Brighter	253	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0
↓	254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Green	255	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
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	Brighter	253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0
↓	254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	
Blue	255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

Note.1) Definition of gray scale:Color (n)...”n” indicates gray scale level.

Larger number means brighter level.

Note.2) Data; 1:High, 0:Low

Note 3) Color data consist of 8 bit red, green and blue data of odd and even number pixel data.

Total data number is 48 signals. This module is able to display 16,777,216 colors because each red, green and blue data is controlled independently.

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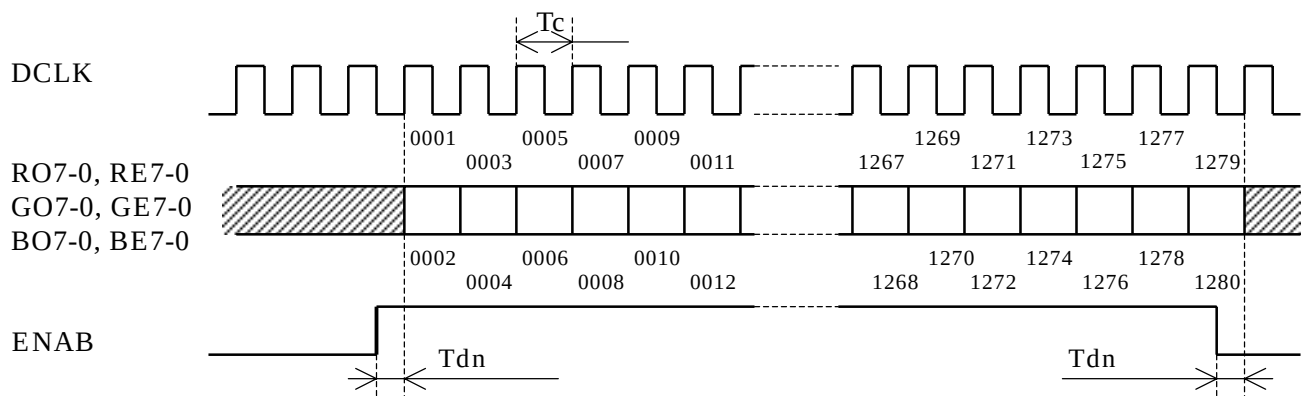
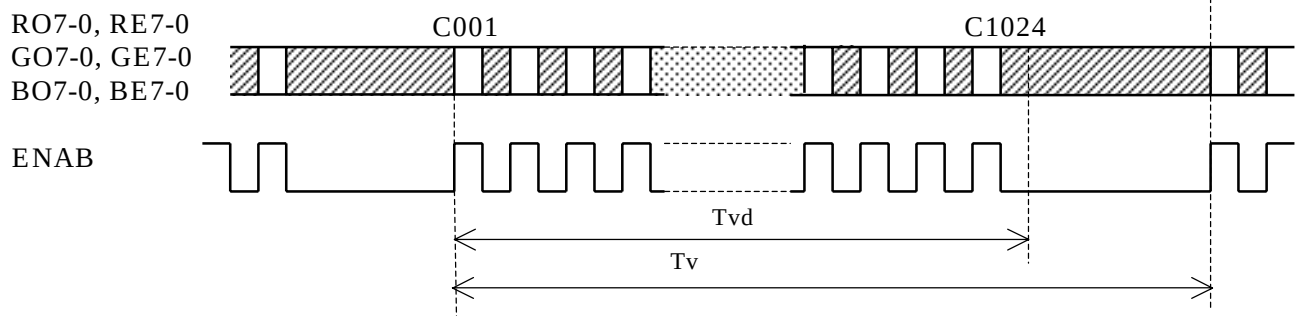
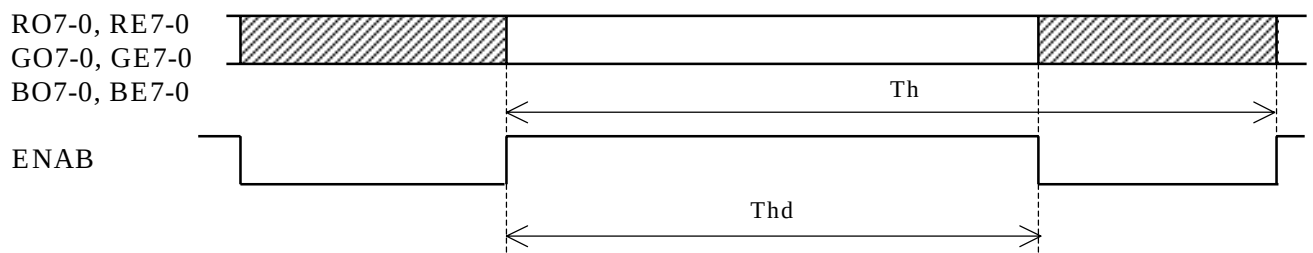
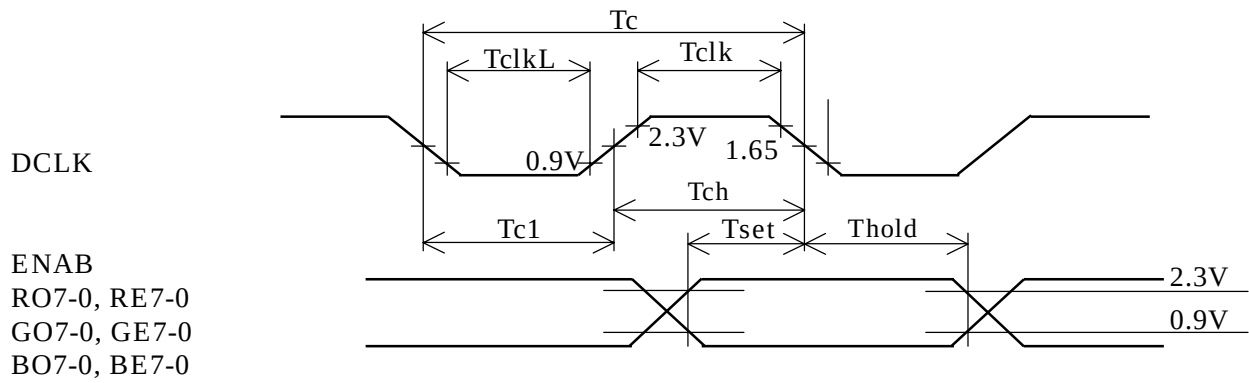


Figure 10-1 Input Signal Timing Chart

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10-4 Correspondence between Data and Display Position

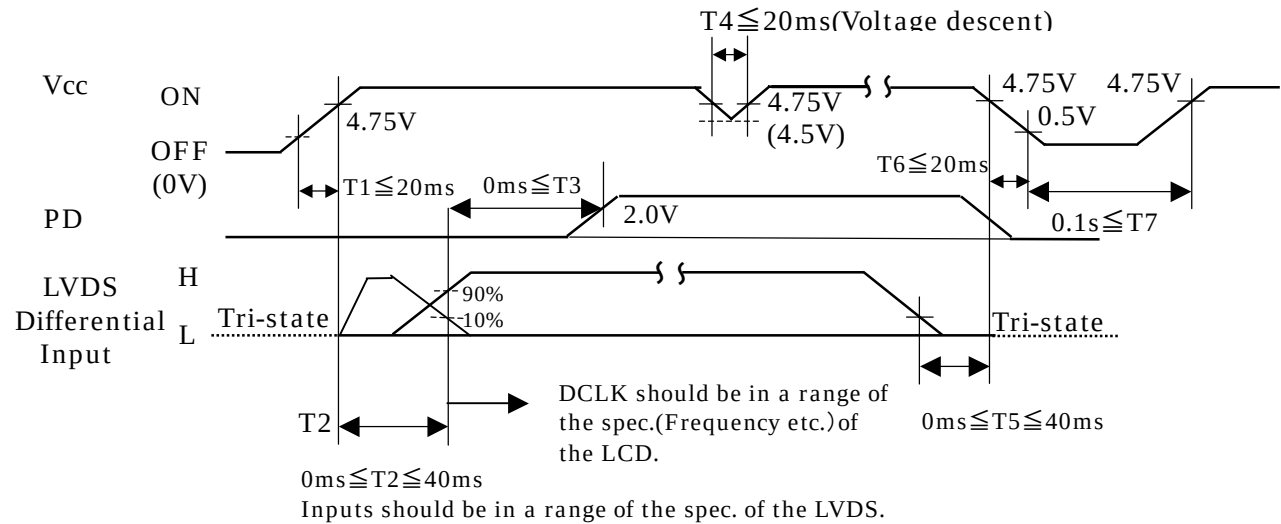
Figure 10-2 shows the Correspondence between Data and Display Position.

	S0001	S0002	S0003	S0004	S0005	S0006	S0007	-----	S3839	S3840	
C001	RO 0001	GO 0001	BO 0001	RE 0002	GE 0002	BE 0002	RO 0003	GO 0003		GE 1280	BE 1280
C1024	RO 0001	GO 0001	BO 0001	RE 0002	GE 0002	BE 0002	RO 0003	GO 0003		GE 1280	BE 1280

Figure 10-2 Correspondence Data and Display Position

10-5 Power Supply Sequence

The sequence of input signals and On/Off of the power supply of this LCD module should be in the specification shown in Figure 10-3 to prevent latch-up of the driver ICs and DC driving of the LCD panel.



*Note : PD input can be set open, if it is not used.

Figure 10-3 Power Supply Sequence

DATE	DOCUMENT CONTROL SECTION					TITLE		FLC48SXC8V-05	
						DRAW. NO.		Tech Bes LCD-00161	
						CUST.			
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11. BACK-LIGHT SPECIFICATIONS

11-1 Pin configuration for Back-light

Table 11-1 shows the description and Pin assignment of the connectors (CN-A to D) for the Back-light of this LCD module.

Table 11-1 Pin Assignment of CN-A to CN-D

Pin No.	Signal						Function	Cable color
	CN-A	CN-B	CN-C	CN-D	CN-E	CN-F		
1	V _{L1}	V _{L2}	V _{L3}	V _{L4}	V _{L5}	V _{L6}	Power supply	T.B.D.
2	—	—	—	—	—	—	—	—
3	GND	GND	GND	GND	GND	GND	Ground	T.B.D.

Connector : Housing : BHR-03VS-1
 Contact : SBH-001T-P0.5
 User's Connector : Post with base: SM02(8.0)B-BHS-1-TB
 Supplier : Japan Solderless Terminal Trading Company LTD. (J.S.T.)

11-2 CCFL

Supplier: T.B.D. Part No. T.B.D.

11-3 Life

The life of the back-light is a minimum of 50,000 hours at the following conditions.

(1) Working conditions

- ①Ambient temperature: $25 \pm 5^{\circ}\text{C}$
 ②Tube current (I_L) : (7mA or less)

(2) Definition of life

- ①Brightness becomes 50% or less than the minimum brightness value shown in Table 9-1.
 ②The lamp cannot be lit by the minimum value of the breakdown voltage(1760Vrms) shown in Table 8-1.
 ③Flashing.

11-4 Lamp assembly set (for replacement)

Lamp assembly set(with charge)is prepared for replacing old lamp to new one. This set consists of a upper lamp assembly and a lower lamp assembly.

Type number : T.B.D.
 Drawing No. : T.B.D.
 Minimum order qty. unit : T.B.D.

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12. APPEARANCE SPECIFICATIONS							
12-1 Appearance							
No.		Item		Judgment method and standard			
1		Bright spot (high and Low)		≤4 dots (Note 1)			
2		Bright spot connection (high and low)		≤2 pair (Note 1) (2 dot connection in horizontal only)			
3		Total of bright spot		≤4 dots			
4		Dark spot		≤8 dots (Note 2)			
5		Dark spot connection		≤3 pairs (Note 2)			
6		Total of dark spot		≤8 dots (Note 2)			
7		Total of dot defect (bright and dark)		≤ 8 dots			
8		Distance of bright spot		high-hgh ≥15mm others ≥ 5mm			
9		Distance of dark spot		≥ 5mm			
10		Scratch on polarizer, line shape		W≤0.03 ——— Ignore 0.03<W≤0.05 L≤6 Ignore 6<L≤12 ≤5 12<L 0 0.05<W≤0.10 L≤0.6 Ignore 0.6<L 0 0.10<W ——— 0			
11		Dent on polarizer, dot shape		D≤0.3 Ignore 0.3<D≤0.4 ≤5 0.4<D 0			
12		Bubble in polarizer		D≤0.3 Ignore 0.3<D≤0.5 ≤5 0.5<D 0			
13		Black white spot (Foreign circular matter)		D≤0.5 ≤5 0.5<D 0			
14		Light leakage by foreign articles		D≤0.3 Ignore 0.3<D≤0.6 ≤4 0.6<D 0			
15		Lints, black/white line		W≤0.03 ——— Ignore 0.03<W≤0.05 L≤6 Ignore 6<L≤12 ≤4 12<L 0 0.05<W≤0.10 L≤0.6 Ignore 0.6<L≤5 ≤2 5<L 0 0.10<W (W+L)/2=D Conform to No.13			
D:Average diameter [mm], W:Width [mm], L:Length [mm], S=(bright spot size)/(dot size)							
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		1		2		3		4							
TENTATIVE															
A A <u>12-2 Dot defects (Bright spots, Dark spots)</u> <u>12-2-1 Zone</u> · Inside display dot area (376.32×301.056mm) · Display dot area means active area. · One pixel consists of 3 dots (red, green and blue). · Foreign particle and scratch unharful to display image, such as the foreign particle under polarizer film but outside of the display area and scratch on metal bezel, backlight module or polarizer film out of the display area, etc., are not counted. B B <u>12-2-2 Bright spots</u> (1) Bright spots by the defect of TFT. · Visible under bias of 2% ND filter High bright spot R•G · Visible under 5% but invisible under 2% ND filter..... Low bright spot R•G•B · Invisible under bias of 5% ND filter..... Not counted (2) Bright spots by the light passing through tears, breaks, etc in color filter. · Exceed size of a half dot High bright spot · A half dot or less Not counted (3) Bright spots by the light passing through tears, breaks, etc in chromium mask. · Exceed 50μm High bright spot · 50μm or less Not counted C C <u>12-2-3 Test condition</u> · Inspector must observe the LCD screen from the normal direction under the illumination by a single 20W fluorescent lamp. The distance between the LCD screen and the inspector should be a height of 50cm above the worktable. The vertical illuminance is 300 to 600lux (reference value). · Bright spot should be counted under entire black screen. · Dark spot should be counted under entire white screen. · Input signal timing should be typical value. D D (Note1) Please do not mistake a single bright spot for a bright spot connection due to Cs(supplemental capacitance) line at the center of each dot. (Note2) If a pixel is dark partially, it connects into the number of dark spots in accordance with following rule. (a) $A < 1/3$: Not count. Only one of 4 dark connection is allowed. (b) $1/3 \leq A < 2/3$: Considered as 0.5 dot. (c) $2/3 \leq A$: Considered as 1 dot. (A=Dark spot size/dot size) E E															
DOCUMENT CONTROL SECTION		DATE								TITLE		FLC48SXC8V-05			
										DRAW. NO.		Tech Bes LCD-00161		CUST.	
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										20 /					

1		2		3		4	
TENTATIVE							
13. ENVIRONMENTAL SPECIFICATIONS							
Table 13-1 show the environmental specifications.							
Table 13-1 Environmental specifications							
Item		Condition		Remark			
Temperature		Operation	0~50℃	Temperature on surface of LCD panel (display area.)			
		Storage	-20~60℃				
Humidity		Operation	20~85%RH	Maximum wet-bulb temperature should not exceed 29℃. No condensation.			
		Storage	5~85%RH				
Vibration		Non-operation	10~500Hz, 1octave/ 20minute, 19.6m/s ² (2G), 1.5mm max, 1hour each X, Yand Z directions.	For single module without package.			
Shock *1		Non-operation	294m/s ² (30G), 6ms, 1time each ±X, ±Y and ±Z directions.				
*1) When LCD module is mounted with side mount holes, the shock condition is 196m/s ² (20G).							
NOTE: Table 13-2 and Figure 13-1 show the shock resistance standard when module is packaged.							
Table 13-2 Shock resistance standard when module is packaged							
Dropping location		Dropping height		Count			
A~J		60cm		1 time			
Figure 13-1 Direction to apply shock to package							
DATE						DOCUMENT CONTROL SECTION	
EDIT						TITLE	
DATE						FLC48SXC8V-05	
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DESCRIPTION						FUJITSU DISPLAY TECHNOLOGIES CORPORATION	
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1							

14. INDICATIONS

This module has the following indications.

- (1) Product name : **LCD unit**
- (2) Model Number : **FLC48SXC8V-05**
- (3) Product Drawing Number : **NA19020-C971**
- (4) Manufacturing Number : **2Y00001**

Serial number
(To be reset every month on 1st.)

Manufacturing month
(Oct. = X, Nov. =Y, Dec. =Z)

Last digit of manufacturing year.

- (5) Version number : **01A** (Example)
-1st 2 digits "01" means operational version.
-3rd alphabet means functional version.
- (6) Manufacturer Country Name : **MADE IN JAPAN**
- (7) Company Name : **FUJITSU DISPLAY TECHNOLOGIES CORP.**
- (8) Disposal method of cold-cathode tubes. (See Figure 14-2)
- (9) Caution when changing cold-cathode tubes. (See Figure 14-3)

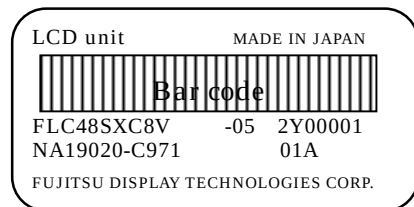


Figure 14-1 Product Label (Example)

• THIS TFT COLOR LCD CONTAINS COLD CATHODE FLUORESCENT LAMPS. PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR ITS DISPOSAL.
• 当該液晶ディスプレイユニットには蛍光管が組み込まれていますので、地方自治体の条例または規則に従って廃棄して下さい。

Figure 14-2

• WHEN CHANGING COLD CATHODE FLUORESCENT LAMPS, FOLLOW OPERATING SPECIFICATIONS. ESPECIALLY BE CAREFUL ABOUT THE LAMPS SIDE-EDGE.
• 蛍光管の交換は作業仕様書に従って行って下さい。特に蛍光管ホルダ側面のエッジに気をつけて下さい。

Figure 14-3

15. PACKAGING

15-1 Packing specifications

- (1) 5 LCD modules/1package.
- (2) Weight:approximately 16kg/1package.
- (3) Outline dimensions: 534mm (W)x329mm (D)x 480mm (H)

15-2 Packing method

Figure 15-2 show the packing method.

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	DESIG.			CHECK		APPR.			22 /

1

2

3

4

TENTATIVE

A

A

B

B

C

C

D

D

E

DOCUMENT CONTROL SECTION

DATE

Anti-Electric Bag

Hook the cable

Protective sheet

Front side

Rear side

Front side

Fig.15-2 (a) Packaging Method

F

						TITLE		FLC48SXC8V-05	
						DRAW. NO.		CUST.	
						Tech Bes LCD-00161			
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	FUJITSU DISPLAY TECHNOLOGIES CORPORATION		23 /	
DESIG.				CHECK					

1

1

2

3

4

TENTATIVE

A

A

Holder (bottom)

LCD unit

B

B

C

C

Holder (top, left)

Holder (top, right)

D

D

480

534

329

Label (example)

型 格 (TYPE)	FLC48SXC8V	-05 数 量 (QTY.)	5
図 番	NA19020-C971	版 数	01A
(DRWG. NO.)		(REV. NO.)	
Bar code	2Y00001	Bar code	
	2Y00002		
	2Y00003		
	2Y00004		
	2Y00005		
Bar code			
MADE IN JAPAN			

- Taping
Top : H or I method
Bottom : H method
- Top and bottom holders should be anti-electrostatic type.

Fig.15-2 (b) Packaging Method

DOCUMENT CONTROL SECTION

DATE

E

F

						TITLE		FLC48SXC8V-05	
						DRAW. NO.		CUST.	
						Tech Bes LCD-00161			
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DESIG.				CHECK		APPR.			

1

1

2

3

4

TENTATIVE

A

A

B

B

C

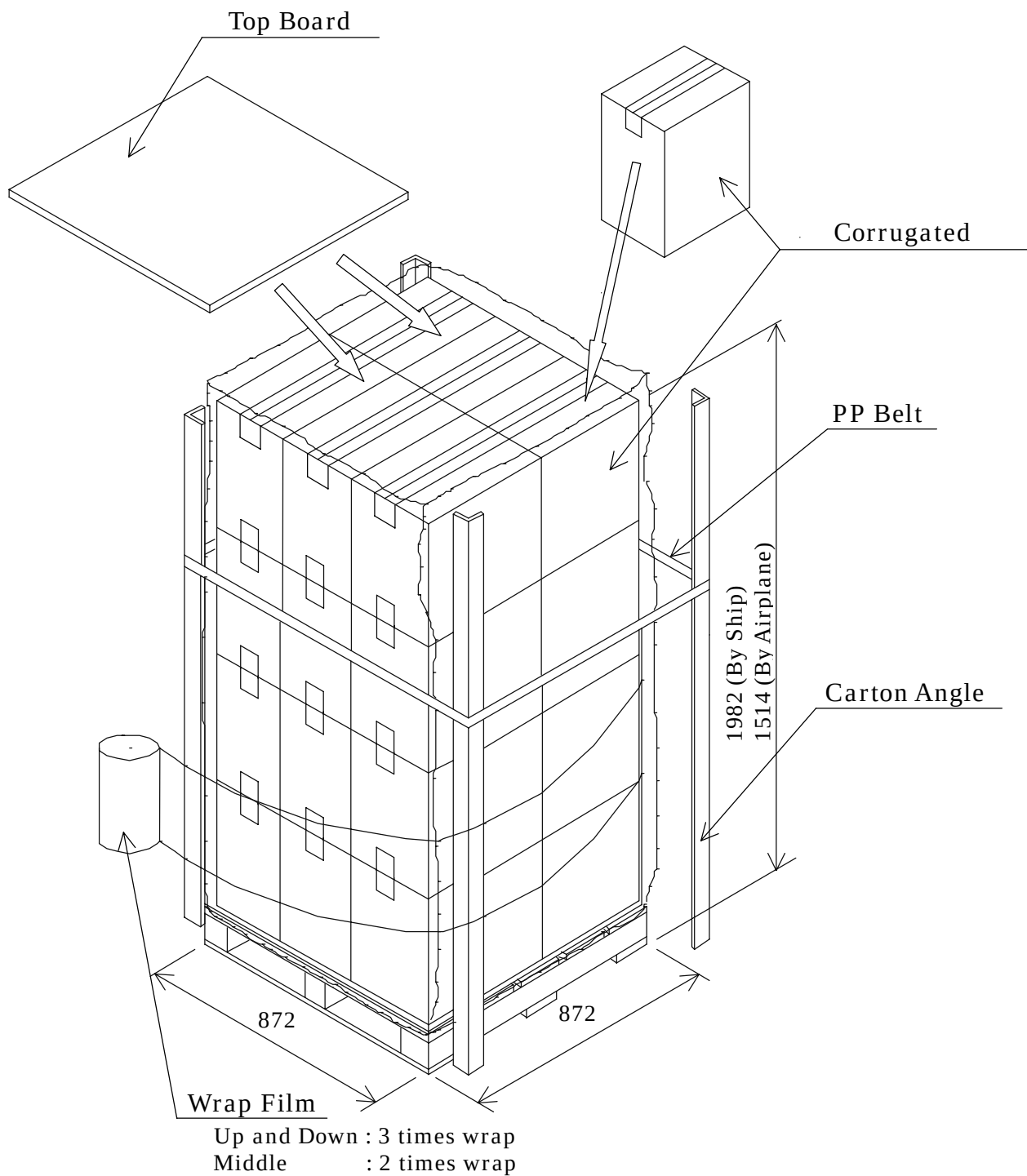
C

D

D

E

F



Note:1) 4 boxes × 4 layers (maximum 16 boxes) : by ship
4 boxes × 2 layers (maximum 8 boxes) : by airplane
Note:2) This drawing shows marine transportation specification.

Fig.15-2 (c) Packaging Method

DOCUMENT CONTROL SECTION

DATE

TITLE

FLC48SXC8V-05

DRAW. NO.

Tech Bes LCD-00161

CUST.

EDIT

DATE

DESIG.

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APPR.

DESCRIPTION

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CORPORATION

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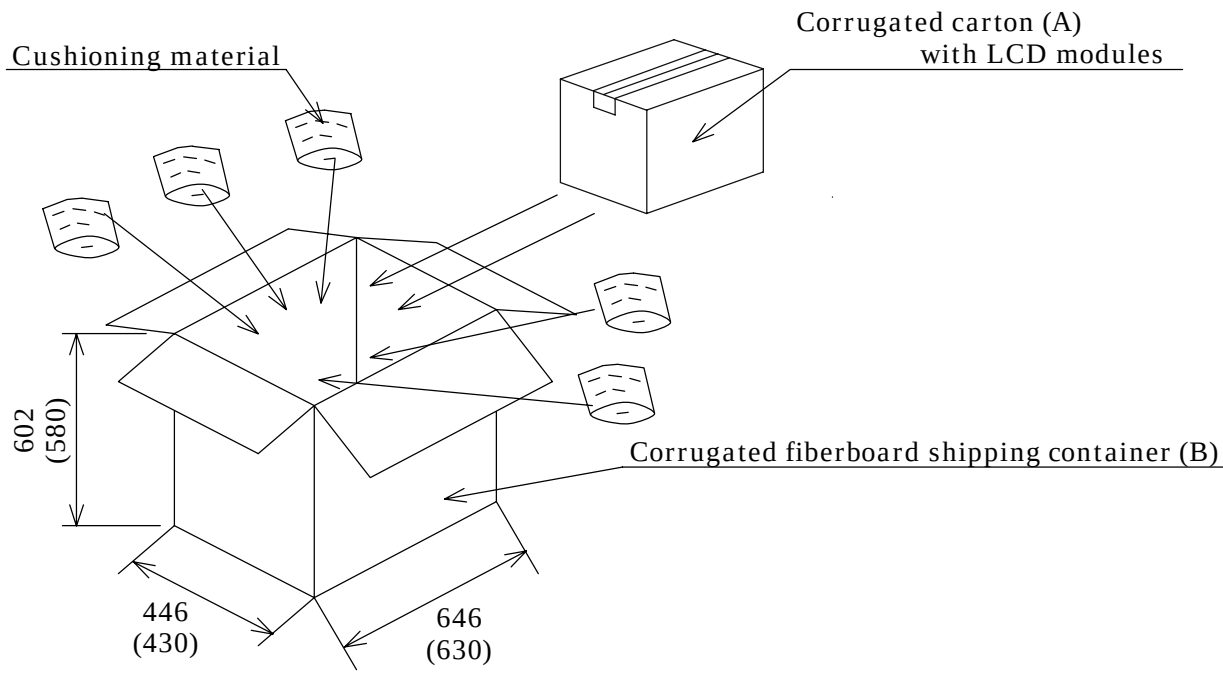
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TENTATIVE



Note 1) The carton (A) should be placed in the middle of the container (B) with enough cushioning materials.

Note2) The figures in () show inside measurements of the container (B).

Figure.15-2 (d) Packing method

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DESIG.				CHECK		APPR.			

16.WARRANTY

17. PRECAUTIONS

Adhere to the following precautions to use this LCD module properly.

(1) Fail safe design

LCD module has an inherent chance of failure. Customers must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions.

(2) Handling of LCD panel

① Do not apply any strong mechanical shock to the LCD panel.

Since the LCD panel is made of glass, excessive shock may damage the panel or cause a malfunction.

② Do not press hard on the LCD panel surface.

In the LCD panel, the gap between two glass plates is kept perfectly even to maintain display properties and reliability. The hard pressure on the LCD panel may cause the following problems.

- ① Ununiformity of color
- ② Disorder of orientation of liquid crystal

Problem ① returns to normal condition after a while. Problem ② returns to normal condition by turning the power off and turning on again.
However these operations should be avoided to insure reliability.

③ Do not scratch the polarizer film on the LCD panel surface.

- Do not press or rub the display surface with a hard tool, tweezers, etc.
- For handling, use cotton or conductive gloves so that the display surface is not soiled.
- If dust or dirt soils the display surface, clean it as follows with a soft cloth (deerskin, etc.)

[Dust] Wipe off with a soft cloth. (do not rub.)

[Dirt] Apply clear water to a soft cloth and squeeze hard out of water drops, then lightly wipe off the specified parts. Only if the dirt is hardly wiped off, use isopropyl alcohol or ethanol.

Be careful not to splash the water or the solvents on the edge of polarizer and in the LCD unit.

The polarizer possibly exfoliates due to the solvent and water penetrated between the polarizer and the LCD panel.

Do not use unspecified solvent such as ketone (acetone, etc.) and aromatics (xylene, toluene, etc.)

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TENTATIVE

(Caution) Be careful not to allow the water or solvent to enter the module.

• If saliva or water drops are left for a long period of time, the part may become deformed or discolored.

Wipe off immediately in the same way as for dirt.

• Do not allow oil to adhere to the module since excessive oil is hard to clean.

④ Do not place or contact objects on the display surface for a long period of time.

This may make some parts of the LCD module distorted and the quality of display may deteriorate.

(3) Handling of LCD module

① Do not pull the cold-cathode tube cable strongly.

If the cable is pulled with the strength of 2kg or more, the cable may be damaged or may lose reliability.

② Assemble the module into user's system in a dust free environment.

Conductive foreign matter adheres to the module may cause failures.

③ Take anti-electrostatic measures for assembling the module.

Since the LCD module contains CMOS-ICs, the following points should be observed.

• For assembling the module, operator should be grounded and wear cotton or conductive gloves.

• Floor of work area and work table to assemble the LCD module should be covered with electrostatic shielding in order to discharge static electricity via an earth wire.

• If necessary, ground operation tools (soldering iron, radio pliers, tweezers, etc.).

• Do not take the module out of the conductive bag until the module is assembled.

• Do not assemble the module under low humidity (50%RH or less).

④ Do not pull the connecting cable on the rear face of the LCD module strongly.

⑤ Do not disassemble or remodel the LCD module.

Disassembly or remodeling of the LCD module may result in malfunctions or deterioration of the display quality and reliability.

(4) Precautions in regards of operating the LCD module

① Adhere to the specified power supply sequence.

If not followed, the CMOS-IC may cause a latch-up, or DC voltage may be applied to the liquid crystal, which cause a failure or serious deterioration in display quality.

② Do not operate the LCD module when condensation occurs.

If the LCD module is operated when condensation is on the terminals of the LCD panel, the terminals cause electrochemical reaction, and may reach disconnection. Condensation easily occurs especially when the module is moved from cold environment to warm environment.

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③ The following troubles occur when the LCD module is not used under recommended temperature.

- Operation under high temperature(>50°C): Display colors shift to blue.
- Storage under high temperature(>60°C): The polarizer film deteriorates and contrast decreases.
- Operation under low temperature(< 0°C): The response speed decreases considerably.
- Storage under low temperature(<-20°C): The liquid crystal may solidify and become damaged.

④ Be sure to input the control signals at the correct timing.

If control signals (DCLK, ENAB) are not input, or if the timing is out of the specified timing, DC voltage may be applied to the liquid crystal and, as a result, cause image sticking or deterioration of contrast.

(5) Precautions in regards of designing module mounting

① Excessive force should not be applied to the screen or the rear side of the LCD module.

Excessive pressure on the screen caused by the installation of the LCD module may deteriorate display quality and reliability.

Brightness uniformity and the reliability of CCFL may decrease if the pressure is applied to the backlight module.

② Avoid twisting and bending the LCD module.

Excessive twist and bend may damage display quality and reliability.

③ Avoid extending the power cable between the LCD module and inverter.

This may cause the backlight to flicker or not to light.

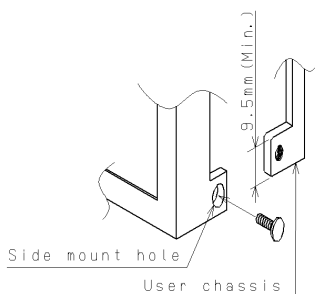
④ Keep the backlight cable apart from the metal enclosure of the LCD module.

When frequency current for backlight driving leak to the metal enclosure, the desired brightness may not be assured.

⑤ When mounting LCD module with M3 screws (x4), tighten the screws with torque below.

User hole : 50N(5kgf) , Side mount hole : 30N(3kgf)

⑥ When mounting LCD module with screws for side-mount, the width of the contacting metal should be 9.5mm or more.



TENTATIVE

(6) Storage method

① Do not store the LCD module in an atmosphere of organic solvent or corrosive gas.

In an organic solvent atmosphere, the polarizer film discolors and display quality deteriorates.

In a corrosive gas environment, various parts of the module may corrode or deteriorate.

② Store the LCD module in a Fujitsu package.

At storing, Fujitsu packages can be stacked up to 3 boxes.

The LCD module is in an anti-static bag. Keep the module in that status.

③ The LCD module is recommended to be stored in humidity controlled, cool and dark locations.

Recommended storage environment

- Place : Dark (avoid direct sunlight)
- Temperature : 10~35°C
- Humidity : 50~60%RH

Note) If the module is left in an environment of 60°C and above for a long period of time, optical characteristics may deteriorate.

(7) Disposal Method

① LCD module

The components of this LCD module can be grouped into metal, resin, glass and so on. As the backlight contains CCFL which includes mercury, it must be disposed according to the local ordinance or regulations.

② Package

All the packages are made of recyclable papers except the anti-ESD bag.

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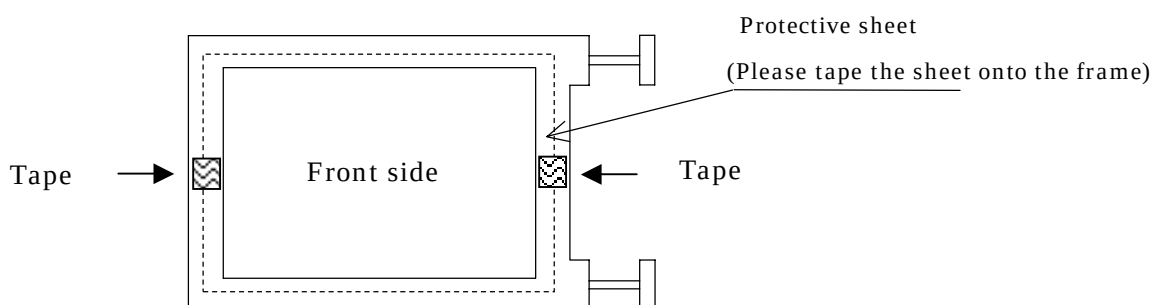
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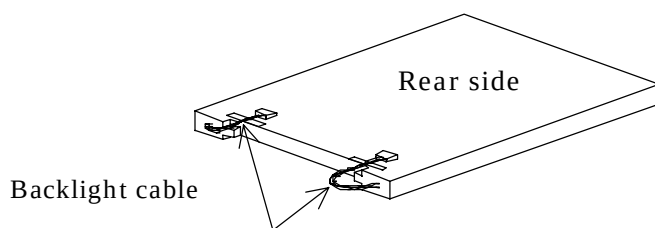
(8) Return method of the LCD module requested for repair or analysis of the problem

- When returning the LCD modules, adhere to the following procedures not to damage the LCD panel or the backlight cables. (Fig. 17-1(a)~(b))
- When the LCD module is returned without following the specified packaging procedures, FDTC will not take responsibility for the damages caused by the failure of the packaging method.

(1) Attach protective sheet.



(2) Hook the backlight cables.



* If the cables are not fixed, the connectors may scratch the LCD panel surface or the cables may be damaged.

(3) Put the LCD module into the anti-electrostatic bag
(Please do not use torn anti-electrostatic bags)

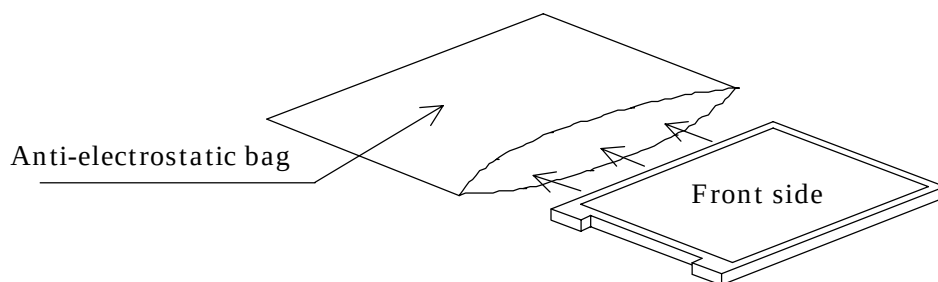


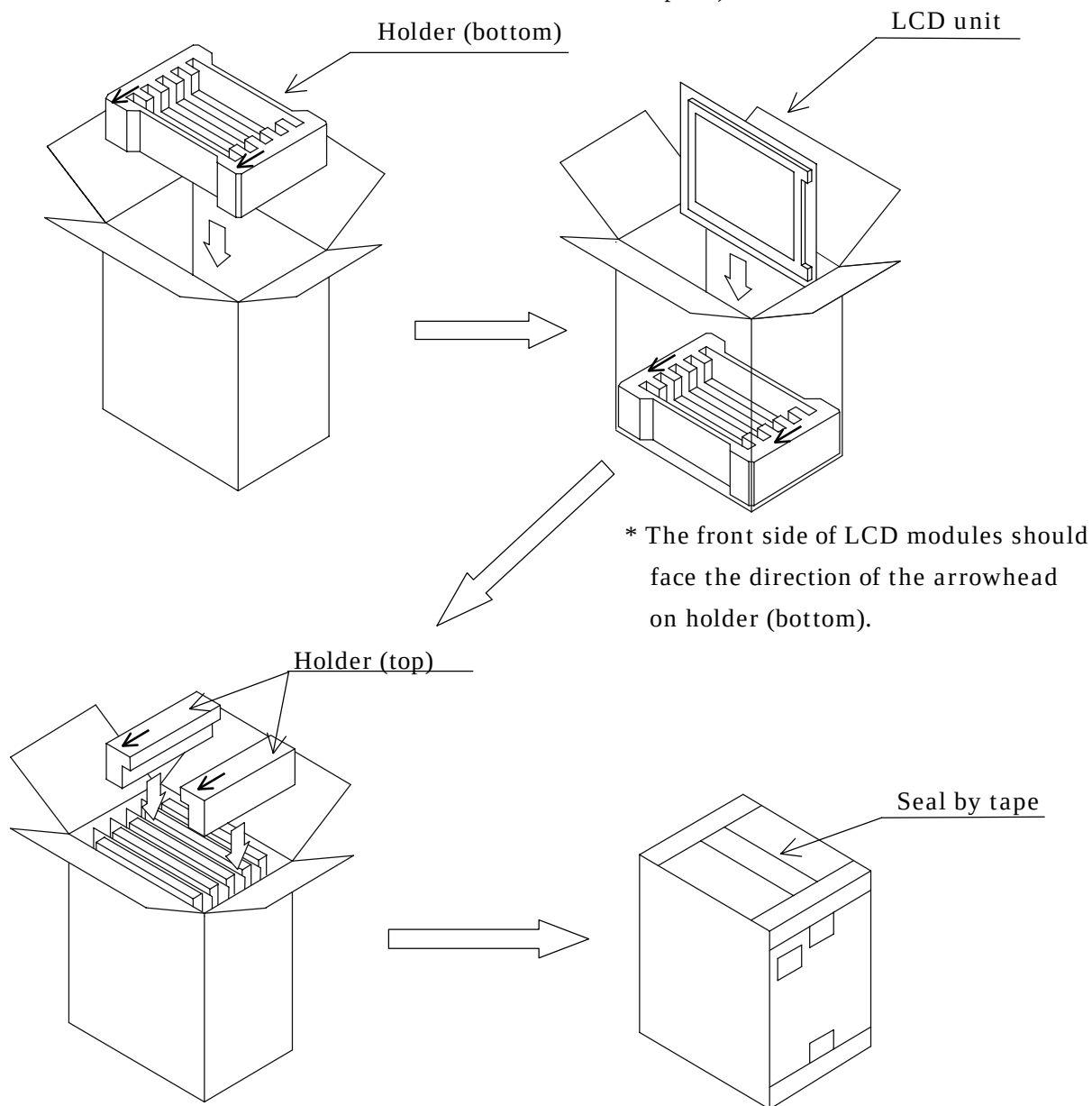
Fig. 17-1(a) Packaging method

						TITLE			FLC48SXC8V-05	
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TENTATIVE**(4) Storage into the carton box**

- When using the carton box manufactured by FDTC

(Please use carton boxes and arrowheads that are not collapsed)



- * The direction of the arrowhead on holder (top) should face the front side of the LCD modules.

****The arrowheads are shown on the holders.****

Fig. 17-1(b) Packaging method

- When not using the carton box manufactured by FDTC

Please pack the LCD modules one by one and make sure not to damage the LCD modules when transporting.

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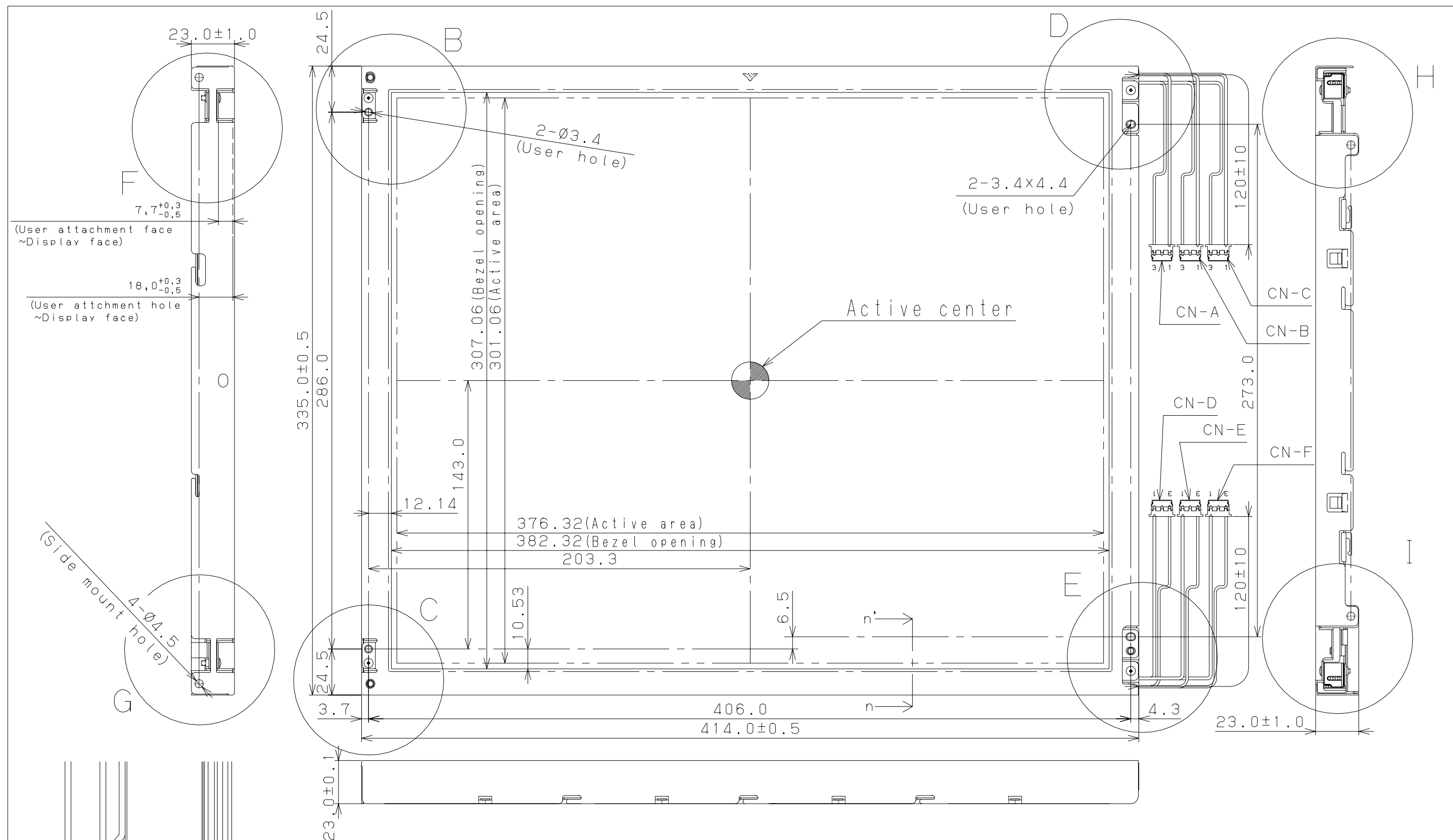
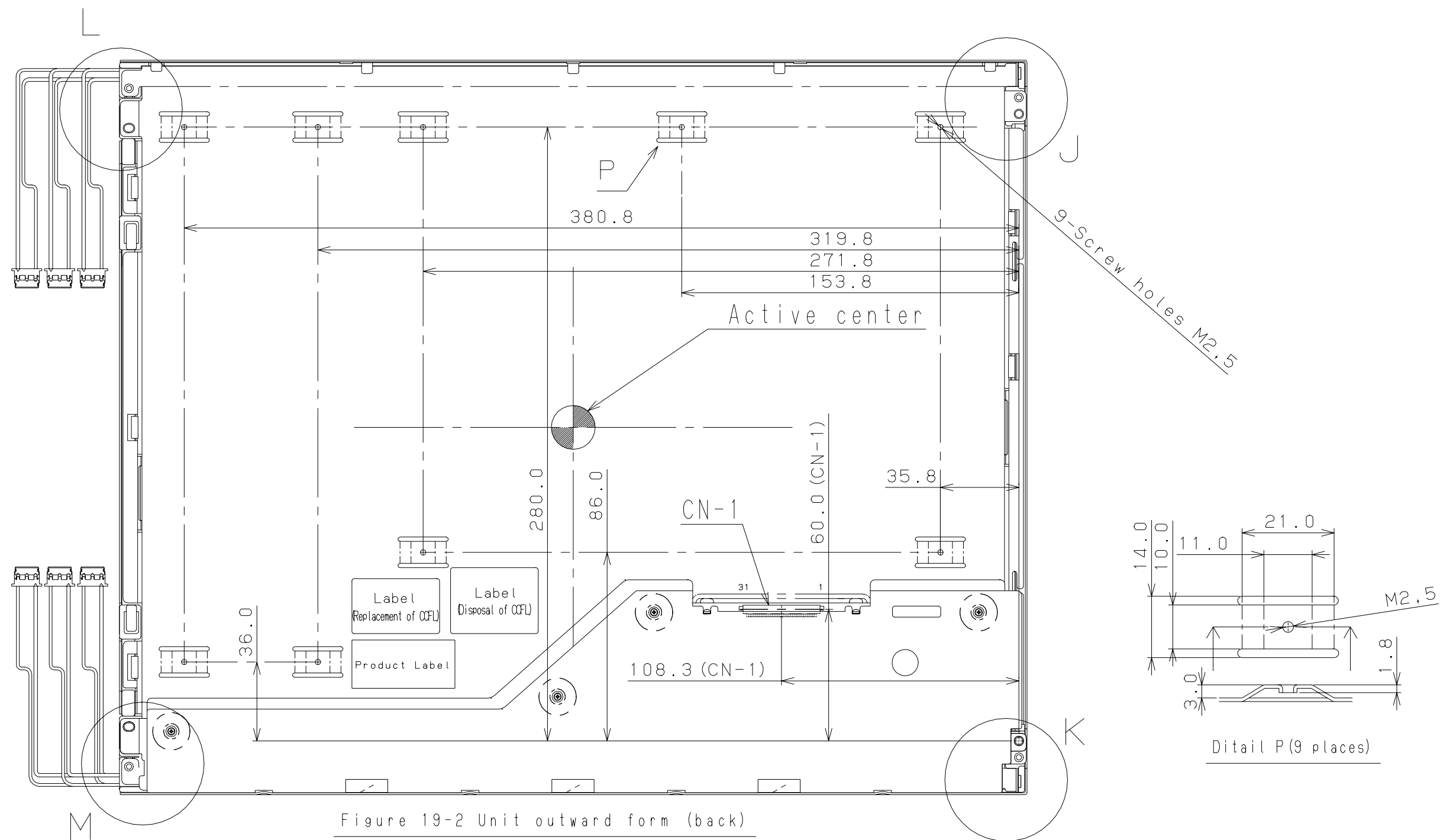


Figure 19-1 Unit outward form (front)

NOTE
1) Unspecified tolerance to be ±0.5

n-n' Cross section

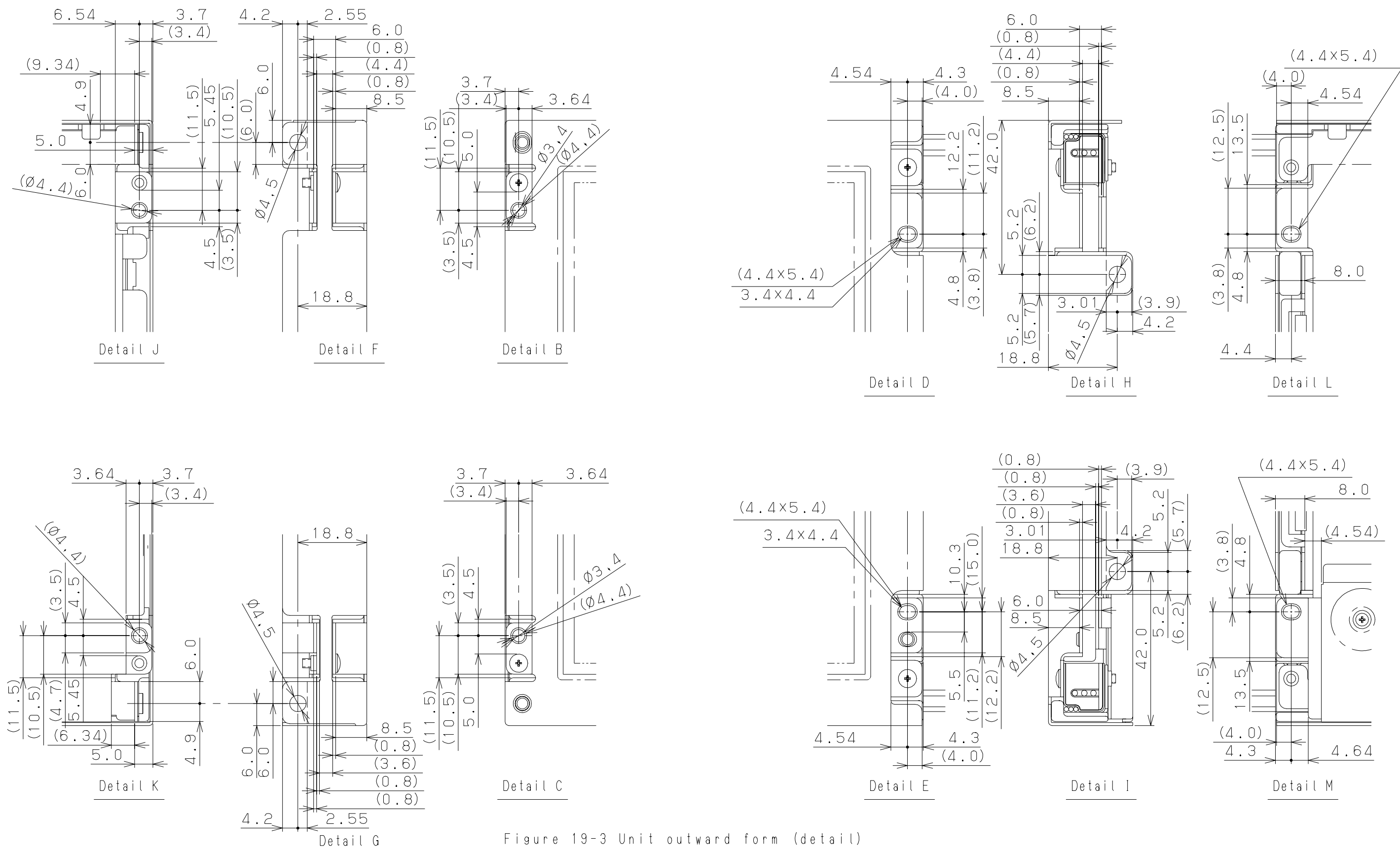
					TITLE Specification of LCD module		
					DRAW. NO. Tech Bes LCD-0161		
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NOTE

2) The height of interface connector does not include that of a counterpart connector.

					TITLE		Specification of LCD module	
					DRAW. NO.		Tech Bes LCD-0161	
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NOTE

3) This page is referende.
(Not guarantee)

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