

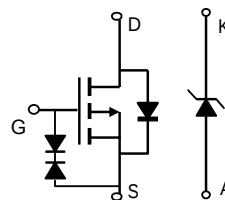
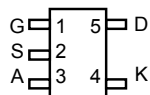
AO3701
**P-Channel Enhancement Mode Field Effect Transistor
with Schottky Diode**
General Description

The AO3701 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. A Schottky diode is provided to facilitate the implementation of a bidirectional blocking switch, or for DC-DC conversion applications. It is ESD protected. *Standard Product AO3701 is Pb-free (meets ROHS & Sony 259 specifications). AO3701L is a Green Product ordering option. AO3701 and AO3701L are electrically identical.*

Features

$V_{DS} (V) = -20V$
 $I_D = -3A (V_{GS} = -10V)$
 $R_{DS(ON)} < 80m\Omega (V_{GS} = -10V)$
 $R_{DS(ON)} < 100m\Omega (V_{GS} = -4.5V)$
 $R_{DS(ON)} < 145m\Omega (V_{GS} = -2.5V)$
 ESD Rating: 2000V HBM
SCHOTTKY
 $V_{DS} (V) = 20V, I_F = 1A, V_F < 0.5V @ 0.5A$

SOT-23-5
Top View


Absolute Maximum Ratings $T_A = 25^\circ C$ unless otherwise noted

Parameter	Symbol	MOSFET	Schottky	Units
Drain-Source Voltage	V_{DS}	-20		V
Gate-Source Voltage	V_{GS}	± 12		V
Continuous Drain Current ^A	$T_A = 25^\circ C$	-3		A
	$T_A = 70^\circ C$	-2.3		
Pulsed Drain Current ^B	I_{DM}	-10		
Schottky reverse voltage	V_{KA}		20	V
Continuous Forward Current ^A	$T_A = 25^\circ C$	I_F	2	A
	$T_A = 70^\circ C$		1	
Pulsed Forward Current ^B	I_{FM}		10	
Power Dissipation	$T_A = 25^\circ C$	P_D	1.14	W
	$T_A = 70^\circ C$		0.72	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	-55 to 150	$^\circ C$

Parameter: Thermal Characteristics MOSFET		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{θJA}	80.3	110	°C/W
Maximum Junction-to-Ambient ^A	Steady-State		117	150	
Maximum Junction-to-Lead ^C	Steady-State	R _{θJL}	43	80	
Thermal Characteristics Schottky					
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{θJA}	109.4	135	°C/W
Maximum Junction-to-Ambient ^A	Steady-State		136.5	175	
Maximum Junction-to-Lead ^C	Steady-State	R _{θJL}	58.5	80	

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=-250\mu\text{A}$, $V_{GS}=0\text{V}$	-20			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-16\text{V}$, $V_{GS}=0\text{V}$ $T_J=55^{\circ}\text{C}$			-0.5 -5	μA
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}$, $V_{GS}=\pm 10\text{V}$			± 1	μA
		$V_{DS}=0\text{V}$, $V_{GS}=\pm 12\text{V}$			± 10	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	-0.6	-0.9	-1.4	V
$I_{D(ON)}$	On state drain current	$V_{GS}=-4.5\text{V}$, $V_{DS}=-5\text{V}$	-10			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-3\text{A}$ $T_J=125^{\circ}\text{C}$		65 91	80 110	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-2\text{A}$		82	100	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}$, $I_D=-1\text{A}$		117	145	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS}=-5\text{V}$, $I_D=-3\text{A}$		6.8		S
V_{SD}	Diode Forward Voltage	$I_S=-1\text{A}$, $V_{GS}=0\text{V}$	-0.65	-0.8	-0.95	V
I_S	Maximum Body-Diode Continuous Current				-2	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}$, $V_{DS}=-10\text{V}$, $f=1\text{MHz}$		512	620	pF
C_{oss}	Output Capacitance			77		pF
C_{rss}	Reverse Transfer Capacitance			62		pF
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $f=1\text{MHz}$		9.2	13	Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	$V_{GS}=-4.5\text{V}$, $V_{DS}=-10\text{V}$, $I_D=-3\text{A}$		5.5	6.6	nC
Q_{gs}	Gate Source Charge			0.8		nC
Q_{gd}	Gate Drain Charge			1.9		nC
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=-10\text{V}$, $V_{DS}=-10\text{V}$, $R_L=2.8\Omega$, $R_{GEN}=3\Omega$		5		ns
t_r	Turn-On Rise Time			6.7		ns
$t_{D(off)}$	Turn-Off DelayTime			28		ns
t_f	Turn-Off Fall Time			13.5		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=-3\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		9.8	12	ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=-3\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		2.7		nC
SCHOTTKY PARAMETERS						
V_F	Forward Voltage Drop	$I_F=0.5\text{A}$		0.39	0.45	V
I_{rm}	Maximum reverse leakage current	$V_R=16\text{V}$			0.1	mA
		$V_R=16\text{V}$, $T_J=125^{\circ}\text{C}$			20	
C_T	Junction Capacitance	$V_R=10\text{V}$		34		pF
t_{rr}	Schottky Reverse Recovery Time	$I_F=1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		5.2	10	ns
Q_{rr}	Schottky Reverse Recovery Charge	$I_F=1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		0.8		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C: The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

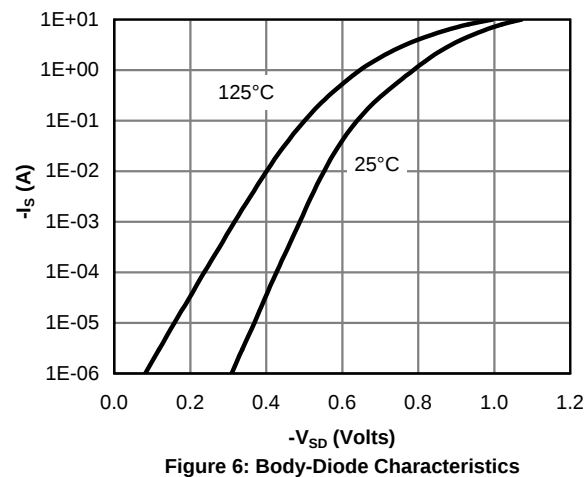
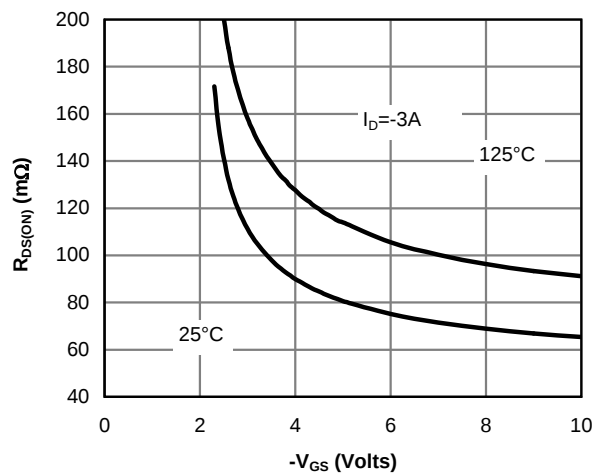
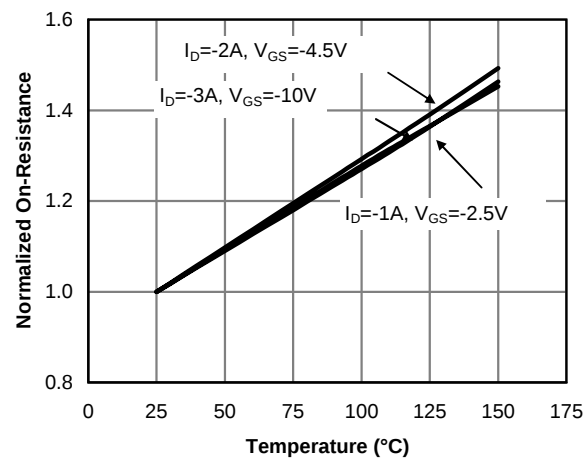
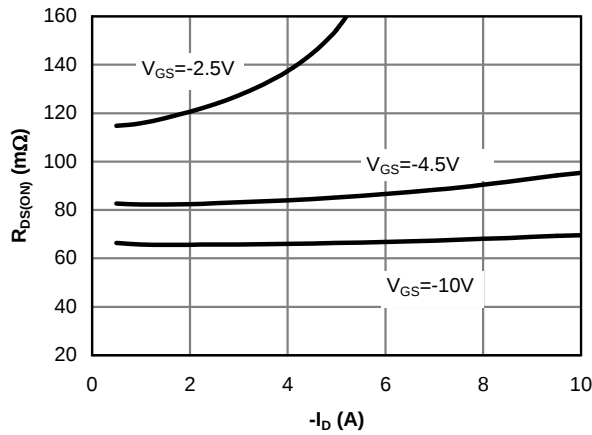
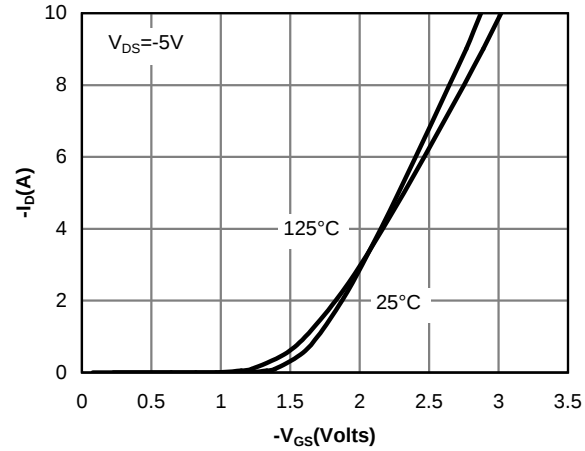
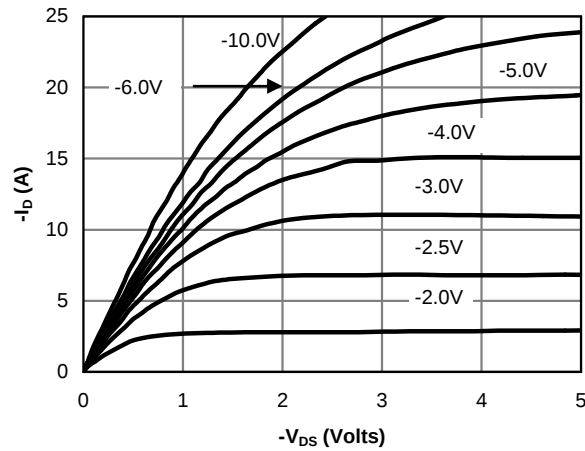
D: The static characteristics in Figures 1 to 6 are obtained using 80 μs pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The SOA curve provides a single pulse rating.

Rev1: May 2006

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



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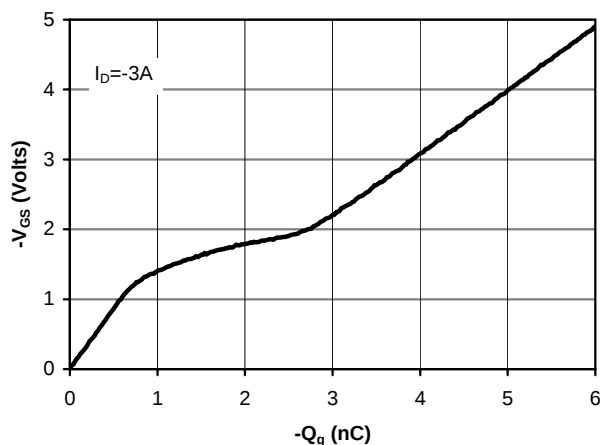


Figure 7: Gate-Charge Characteristics

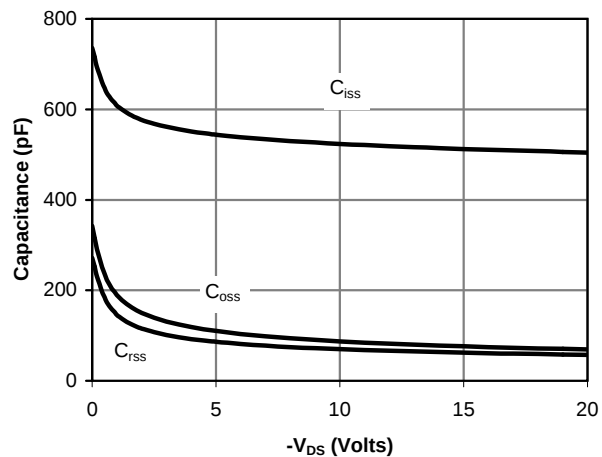


Figure 8: Capacitance Characteristics

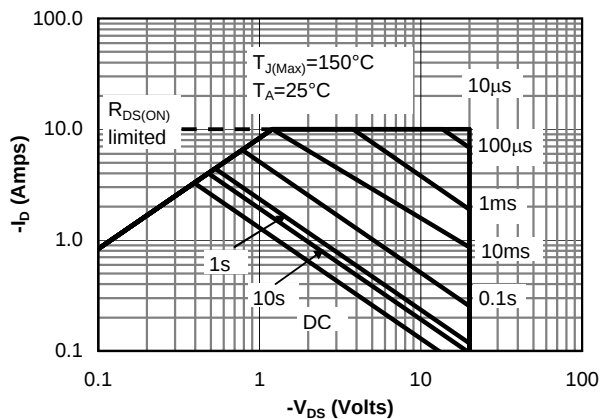


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

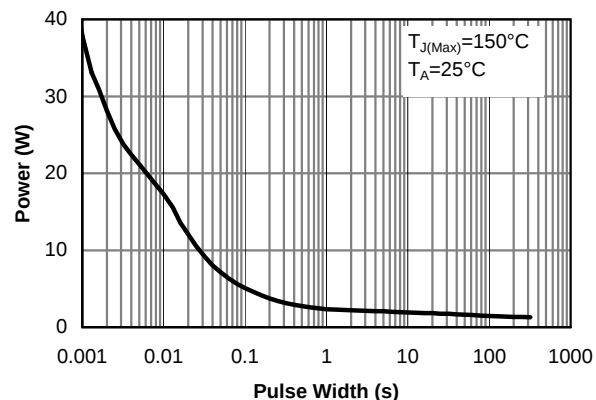


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

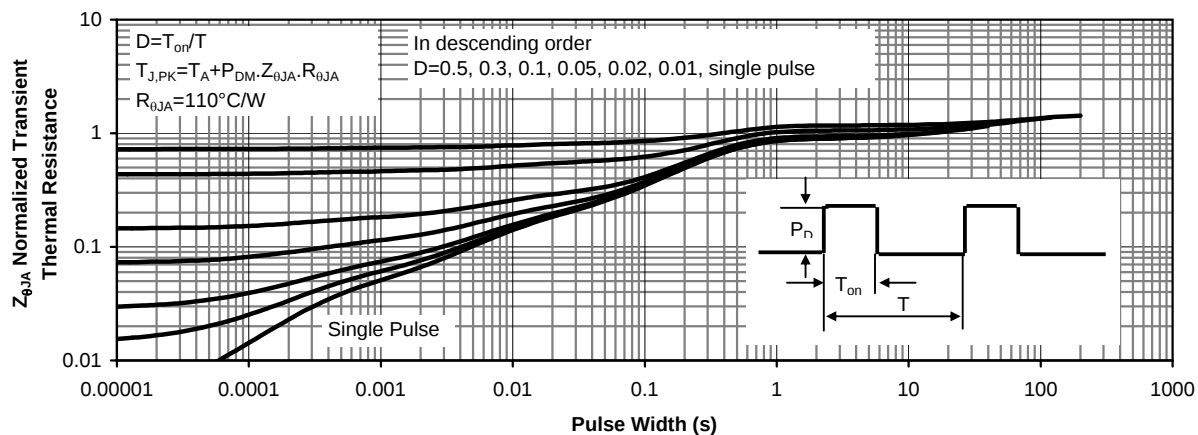


Figure 11: Normalized Maximum Transient Thermal Impedance

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: SCHOTTKY

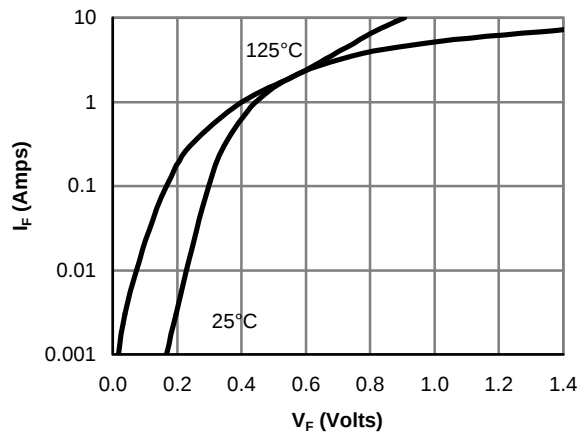


Figure 12: Schottky Forward Characteristics

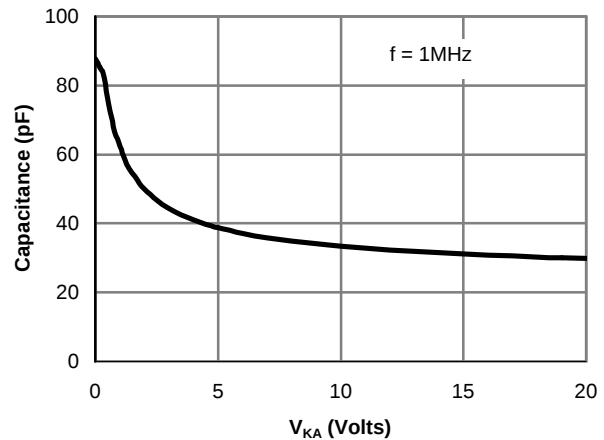


Figure 13: Schottky Capacitance Characteristics

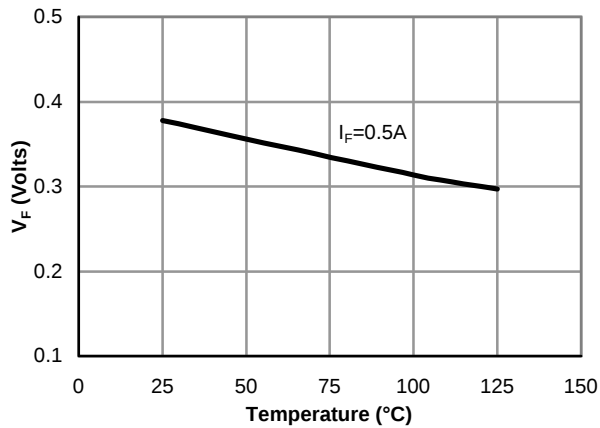


Figure 14: Schottky Forward Drop vs. Junction Temperature

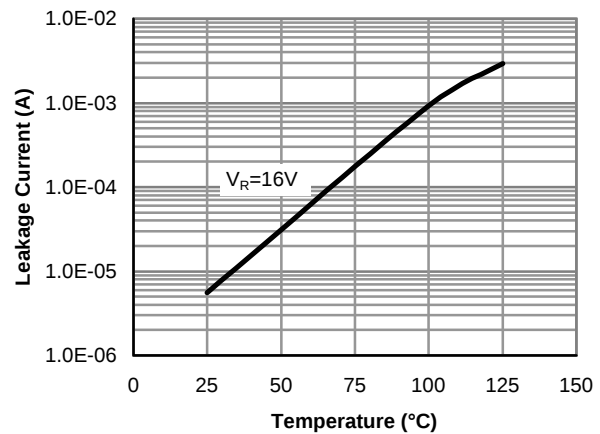


Figure 15: Schottky Leakage current vs. Junction Temperature

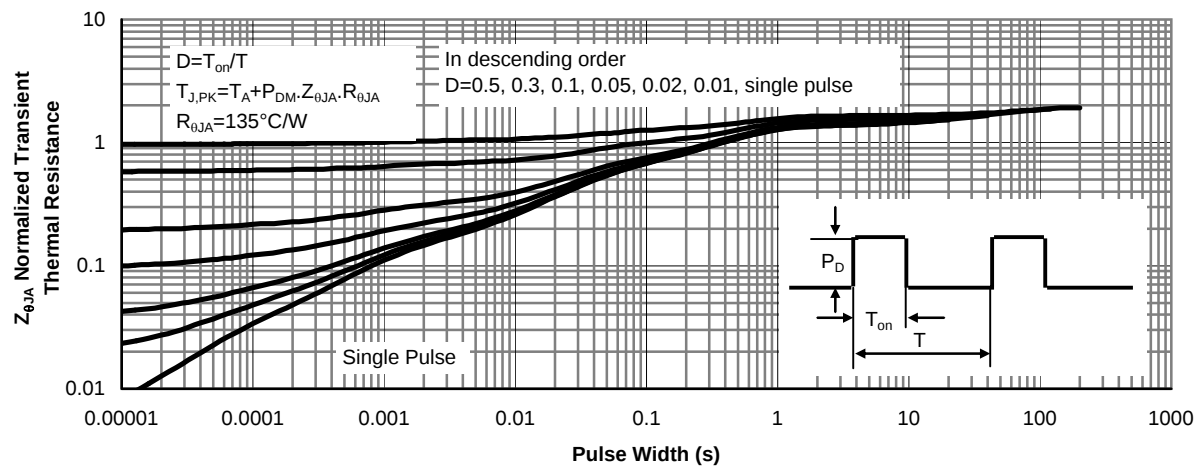


Figure 15: Schottky Normalized Maximum Transient Thermal Impedance