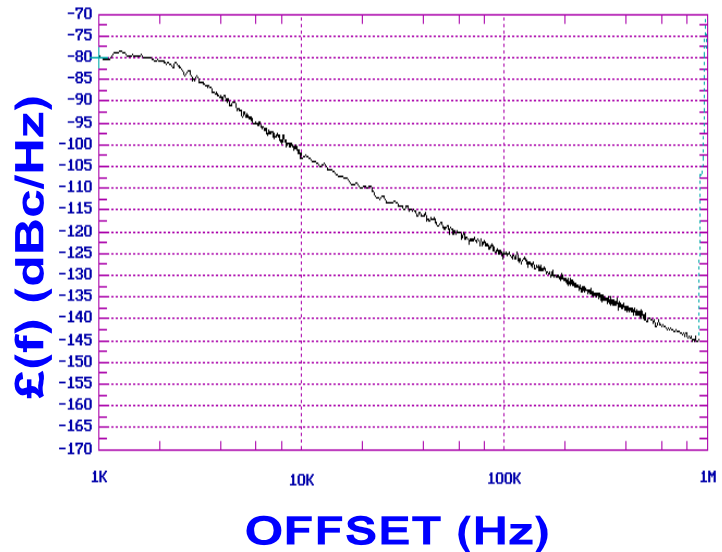




PHASE NOISE (1 Hz BW, typical)



FEATURES

- Frequency Range: 200 - 230 MHz
- Step Size: 100 KHz
- PLL - Style Package

APPLICATIONS

- Telecommunications
- Satellite
- Telemetry

PERFORMANCE SPECIFICATIONS

	VALUE	UNITS
Frequency Range	200 - 230	MHz
RMS Phase Error (100 Hz - 100 KHz)	0.5	°
Harmonic Suppression (2nd, typ.)	-10	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	3.5±2.5	dBm
Load Impedance	50	Ω
Step Size	100	KHz
Charge Pump Output Current	250	μA
Switching Speed (typ., adjacent channel)	1	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to +85	°C
Package Style	PLL	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	25	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

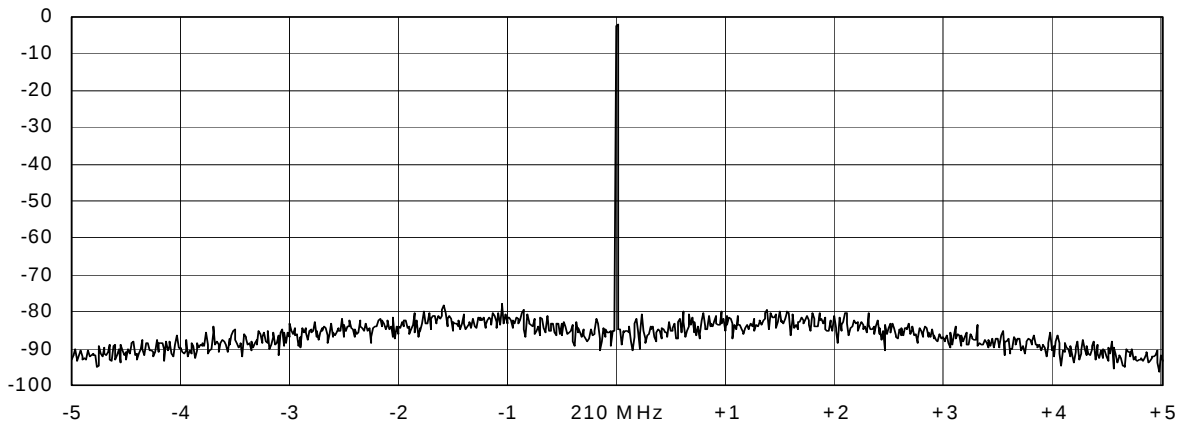
- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

NOTES:

Reference Oscillator Signal: $5 \text{ MHz} < f_{\text{osc}} < 40 \text{ MHz}$
 Frequency Synthesizer: National Semiconductor - LMX2306

PLL OUTPUT SPECTRUM

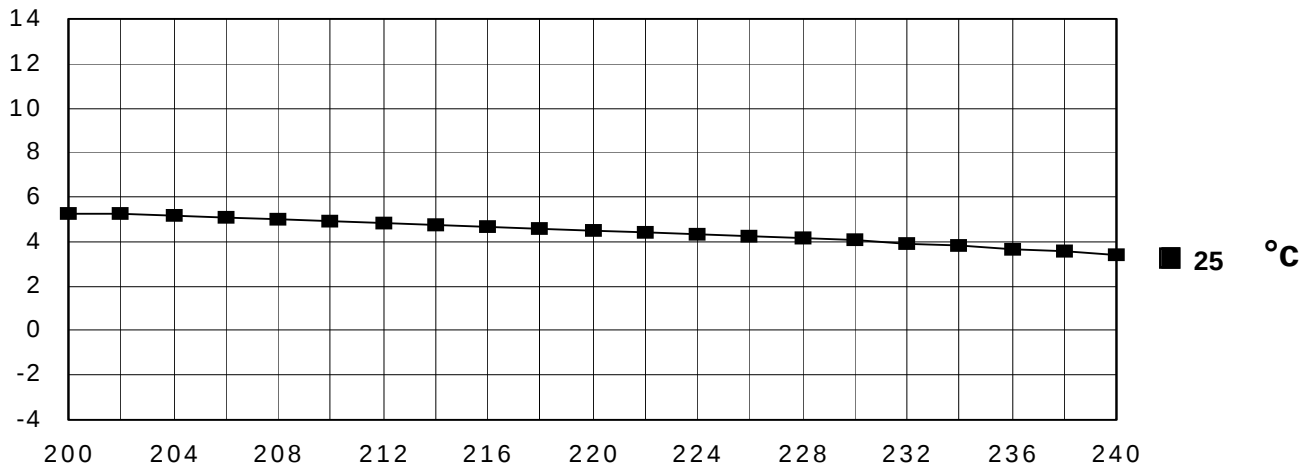
POWER



FREQUENCY OFFSET (KHz)

POWER CURVE, typ.

POWER OUTPUT (dBm)



FREQUENCY (MHz)

PHYSICAL DIMENSIONS

