



### **60V 1A STEP-DOWN LED DRIVER**

### Description

The AL8862 is a step-down DC/DC converter designed to drive LEDs with a constant current. The AL8862 operates with an input supply voltage from 5V to 60V and provides an externally adjustable output current up to 1A. Series connection of the LEDs provides identical LED currents resulting in uniform brightness and eliminating the need for ballast resistors. The AL8862 switches at frequency up to 1MHz. This allows the use of smaller size external components, hence minimizing the PCB size.

The AL8862 integrates the power switch and a high-side output current sensing circuit. Maximum output current of AL8862 is set via an external resistor connected between the VIN and SET input pins. Dimming is achieved by applying either a DC voltage or a PWM signal at the CTRL input pin. The soft-start time can be adjusted using an external capacitor from the CTRL pin to ground. An input voltage of 0.3V or lower at CTRL pin will shut down the power switch.

#### **Features**

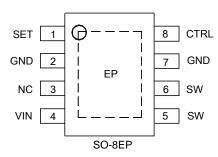
- Wide Input Voltage Range: 5V to 60V
- Output Current up to 1A
- Internal 60V NDMOS Switch
- Typical 4% Output Current Accuracy
- Single Pin for On/Off and Brightness Control by DC Voltage or PWM Signal
- High Efficiency (Up to 97%)
- LED Short-Circuit Protection
- Inherent Open-Circuit LED Protection
- Current-Sense Resistor Short-Circuit Protection
- Over Temperature Shutdown
- Up to 1MHz Switching Frequency
- SO-8EP Packages Available in Green Molding Compound (No Br, Sb)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

### **Pin Assignments**

### (Top View)

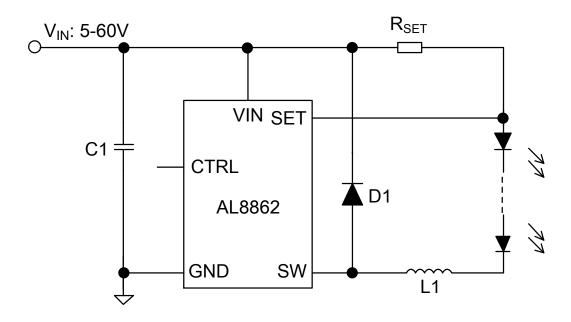


### **Applications**

- Commercial & Industrial Lighting
- Appliances Interior Lighting
- Architecture Detail Lighting
- External Driver with Multiple Channels and Smart Lighting



## **Typical Applications Circuit**

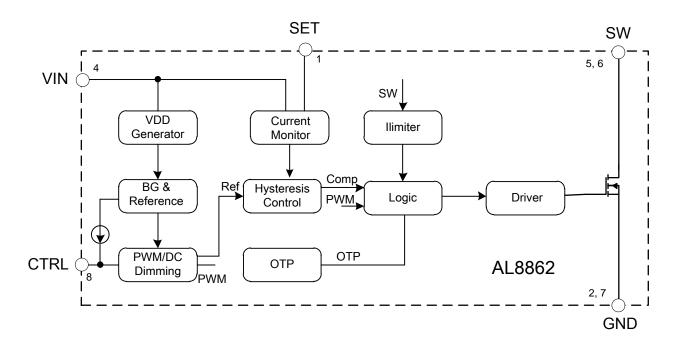


### **Pin Descriptions**

Pin Number	Pin Name	Function
1	SET	Set Nominal Output Current Pin. Connect resistor R <sub>SET</sub> from this pin to VIN to define nominal average output current.
2,7	GND	Ground of IC
3	NC	No connection
4	VIN	Input voltage (5V to 60V). Decouple to ground with 10µF or higher X7R ceramic capacitor close to device.
5,6	SW	Switch Pin. Connect inductor/freewheeling diode here, minimizing track length at this pin to reduce EMI.
8	CTRL	Multi-function On/Off and brightness control pin:  Leave floating for normal operation.  Drive to voltage below 0.3V to turn off output current  Drive with DC voltage (0.4V < VSET< 2.5V) to adjust output current from 10% to 100% of IOUT_NOM  Drive with an analog voltage >2.6V output current will be 100% of IOUT_NOM  A PWM signal (Low level <0.3V, High level >2.6V, transition times less than 1us) allows the output current to be adjusted over a wide range up to 100%  Connect a capacitor from this pin to ground to increase soft-start time.  (Default soft-start time = 0.1ms. Additional soft-start time is approx. 1.5ms/1nF)
EP	EP	Exposed pad/TAB connects to GND and thermal mass for enhanced thermal impedance.



### **Functional Block Diagram**



### **Absolute Maximum Ratings** (Note 4)

Symbol	Parameter	Rating	Unit
VIN	Input Voltage	-0.3 to 65	V
V <sub>SW</sub> , V <sub>SET</sub>	SW, SET Pin Voltage	-0.3 to 65	V
$V_{CTRL}$	CTRL Pin Input Voltage	-0.3 to 6	V
T <sub>A</sub>	Operating Ambient Temperature	-40 to +105	°C
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
T <sub>LEAD</sub>	Lead Temperature (Soldering, 10sec)	+300	°C

Note: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to Absolute Maximum Ratings for extended periods may affect device reliability.



## **ESD** Ratings

Symbol	Parameter	Rating	Unit
$V_{ESD}$	Human-Body Model (HBM)	2000	V
VESD	Charged-Device Model (CDM)	500	V

## **Recommended Operating Conditions**

Symbol	Parameter	Min	Max	Unit
VIN	Input Voltage	5	60	V
F <sub>SW</sub>	Switching Frequency	-	1	MHz
I <sub>OUT</sub>	Continuous Output Current	=	1	Α
V <sub>CTRL</sub>	Voltage Range for 10% to 100% DC Dimming Relative to GND	0.4	2.5	V
V <sub>CTRL_HIGH</sub>	Voltage High for PWM Dimming Relative to GND	2.6	5.5	V
$V_{CTRL\_LOW}$	Voltage Low for PWM Dimming Relative to GND	0	0.3	V
T <sub>A</sub>	Operating Ambient Temperature	-40	+105	°C
TJ	Operating Junction Temperature	-40	+125	°C

## **Thermal Information** (Note 5)

Symbol Parameter		Rating	Unit
$\theta_{JA}$	Junction-To-Ambient Thermal Resistance	65	°C/W
$\theta_{JC}$	Junction-To-Case(Top) Thermal Resistance	22.4	°C/W

Note: 5. Device mounted on  $2'' \times 2''$  FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.



## **Electrical Characteristics** ( $T_A = +25$ °C, unless otherwise noted.)

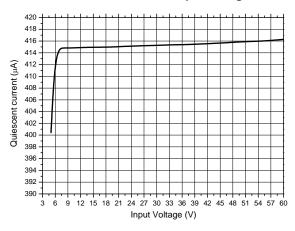
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
SUPPLY VOLT	AGE		•			•
V <sub>IN</sub>	Input Voltage	-	5.0	-	60	V
IQ	Quiescent Current	CTRL Pin Floating, V <sub>IN</sub> =16V	-	450	-	μΑ
$V_{UVLO}$	Under Voltage Lockout	V <sub>IN</sub> Rising	-	4.8	-	V
V <sub>UVLO_HYS</sub>	UVLO Hysteresis	-	-	200	-	mV
HYSTERESTIC	CONTROL	•		•		
VSET	Mean Current Sense Threshold Voltage	Measured on SET Pin with Respect to V <sub>IN</sub>	96	100	104	mV
VSET_HYS	Sense Threshold Hysteresis	-	-	±13	-	%
ISET	ISET Pin Input Current	V <sub>SET</sub> = V <sub>IN</sub> -0.1	-	8	-	μA
<b>ENABLE AND</b>	DIMMING		•			I
Vctrl	Voltage Range on CTRL Pin	For Analog Dimming	0.4	-	2.5	V
-	Analog Dimming Range	-	10	-	100	%
VCTRL_ON	DC Voltage on CTRL Pin for Analog Dimming on	VCTRL Rising	-	0.45	-	V
VCTRL_OFF	DC Voltage on CTRL Pin for Analog Dimming off	VCTRL Falling	-	0.40	-	V
SWITCHING O	PERATION			•		
Ron	SW Switch On Resistance	@I <sub>SW</sub> = 100mA	-	0.4	-	Ω
ISW_LEAK	SW Switch Leakage Current		-	-	8	μΑ
t <sub>SS</sub>	Soft Start Time	$V_{IN} = 16V$ , $C_{CTRL} = 1nF$	-	1.5	ı	ms
Fsw	Operating Frequency	$V_{IN} = 16V, V_O = 9.6 V (3 LEDs)$ L = 47µH, $\Delta I = 0.25A (I_{LED} = 1A)$	-	250	-	kHz
Fsw_max	Recommended Maximum Switch Frequency	-	-	-	1	MHz
ton_rec	Recommended Minimum Switch ON Time	For 4% Accuracy	=	500	-	ns
t <sub>PD</sub> Internal Comparator Propagation Delay (Note 6)		-	-	100	-	ns
THERMAL SH	UTDOWN					
Тотр	Over Temperature Protection	-	-	+150	-	°C
Totp_hys	Temp Protection Hysteresis	-	-	+30	-	°C
Isw_max	Current Limit	Peak Inductor Current	-	3	-	Α

Note: 6 .Guaranteed by design.

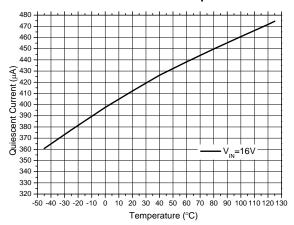


### Typical Performance Characteristics (T<sub>A</sub> = +25°C, V<sub>IN</sub> = 16V, unless otherwise noted.)

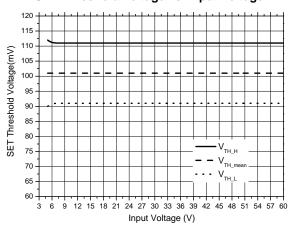
### **Quiescent Current vs. Input Voltage**



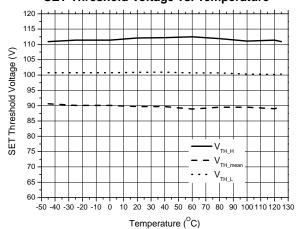
### **Quiescent Current vs. Temperature**



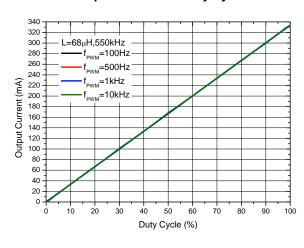
### SET Threshold Voltage vs. Input Voltage



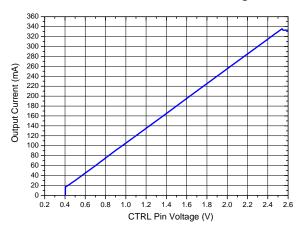
### **SET Threshold Voltage vs. Temperature**



### PWM Dimming (V<sub>IN</sub>=16V, 3LEDs, 68μH, Rs=0.3Ω) Output Current vs. Duty Cycle

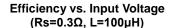


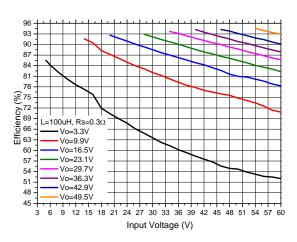
# Analog Dimming ( $V_{IN}$ =16V, 3LEDs, 47 $\mu$ H, Rs=0.3 $\Omega$ ) LED Current vs. CTRL Pin Voltage



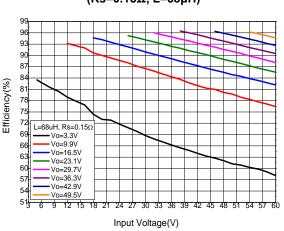


### Typical Performance Characteristics (Cont.) ( $T_A = +25$ °C, $V_{IN} = 16V$ , unless otherwise noted.)

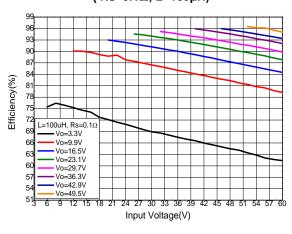




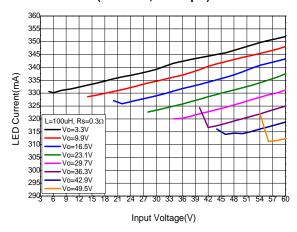
Efficiency vs. Input Voltage (Rs=0.15Ω, L=68μH)



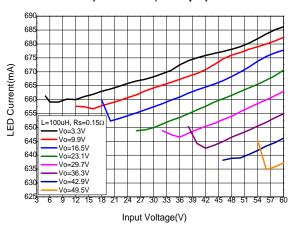
Efficiency vs. Input Voltage (Rs=0.1Ω, L=100μH)



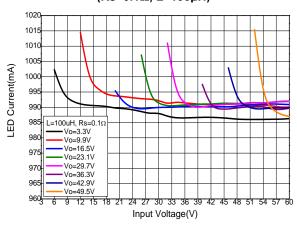
LED Current vs. Input Voltage (Rs=0.3Ω, L=100μH)



LED Current vs. Input Voltage (Rs=0.15Ω, L=68μH)



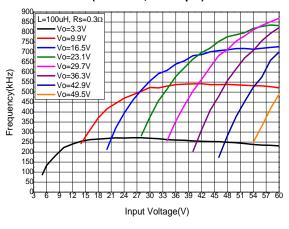
LED Current vs. Input Voltage (Rs=0.1Ω, L=100μH)



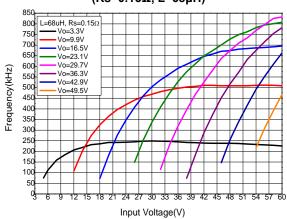


### Typical Performance Characteristics (Cont.) (T<sub>A</sub> = +25°C, V<sub>IN</sub> = 16V, unless otherwise noted.)

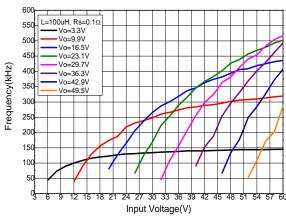
## Operating Frequency vs. Input Voltage (Rs=0.3Ω, L=100μH)



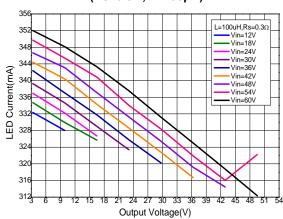
# Operating Frequency vs. Input Voltage (Rs=0.15 $\Omega$ , L=68 $\mu$ H)



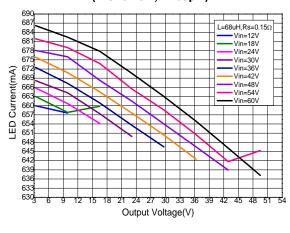
## Operating Frequency vs. Input Voltage (Rs=0.1Ω, L=100μH)



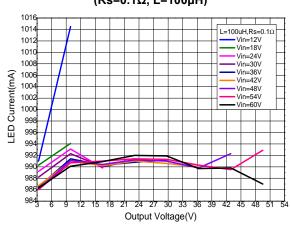
LED Current vs. Output Voltage (Rs=0.3Ω, L=100μH)



## LED Current vs. Output Voltage (Rs=0.15Ω, L=68μH)



LED Current vs. Output Voltage (Rs=0.1Ω, L=100μH)





### **Application Information**

### AL8862 Operation

In normal operation, when normal input voltage is applied at VIN, the AL8862 internal switch will turn on. Current starts to flow through sense resistor R<sub>SET</sub>, inductor L1, and the LEDs. The current ramps up linearly, and the ramp-up rate is determined by the input voltage VIN, VOUT and the inductor L1.

This rising current produces a voltage ramp across  $R_{SET}$ . The internal circuit of the AL8862 senses the voltage across  $R_{SET}$  and applies a proportional voltage to the input of the internal comparator. When this voltage reaches an internally set upper threshold, the internal switch is turned off. The inductor current continues to flow through  $R_{SET}$ , L1, LEDs and diode D1, and back to the supply rail, but it decays, with the rate determined by the forward voltage drop of LEDs and the diode D1.

This decaying current produces a falling voltage on R<sub>SET</sub>, which is sensed by the AL8862. A voltage proportional to the sense voltage across R<sub>SET</sub> will be applied at the input of internal comparator. When this voltage falls to the internally set lower threshold, the internal switch is turned on again.

This switch-on-and-off cycle continues to provide the average LED current set by the sense resistor R<sub>SET</sub>.

### **LED Current Configuration**

The nominal average output current in the LED(s) is determined by the value of the external current sense resistor (R<sub>SET</sub>) connected between VIN and SET and is given by:

$$I_{OUT(NOM)} = \frac{0.1}{R_{SET}}$$

The table below gives values of nominal average output current for several preferred values of current setting resistor (R<sub>SET</sub>) in the typical application circuit shown on page 1.

R <sub>SET</sub> (Ω)	Nominal Average Output Current (mA)
0.1	1000
0.15	667
0.3	333

The above values assume that the CTRL pin is floating and at a nominal reference voltage for internal comparator. It is possible to use different values of  $R_{\text{SET}}$  if the CTRL pin is driven by an external dimming signal.

### **Analog Dimming**

Apply a DC voltage from 0.4V to 2.5V on CTRL pin can adjust output current from 10% to 100% of I<sub>OUT\_NOM</sub> linearly, as shown in Figure 1. If the CTRL pin is brought higher than 2.5V, the LED current will be clamped to 100% of I<sub>OUT\_NOM</sub> while if the CTRL voltage falls below 0.3V, the output switch will turn off.

### PWM Dimming

LED current can be adjusted digitally, by applying a low frequency pulse-width-modulated (PWM) logic signal to the CTRL pin to turn the device on and off. This will produce an average output current proportional to the duty cycle of the control signal. To achieve a high resolution the PWM frequency is recommended to be lower than 500Hz, however higher dimming frequencies can be used, at the expense of dimming dynamic range and accuracy. Typically, for a PWM frequency of 500Hz the accuracy is better than 1% for PWM ranging from 1% to 100%.

The accuracy of the low duty cycle dimming is affected by both the PWM frequency and also the switching frequency of the AL8862. For best accuracy/resolution the switching frequency should be increased while the PWM frequency should be reduced.

The CTRL pin is designed to be driven by both 3.3V and 5V logic levels directly from a logic output with either an open drain output or push pull output stage.



### **Application Information (Cont.)**

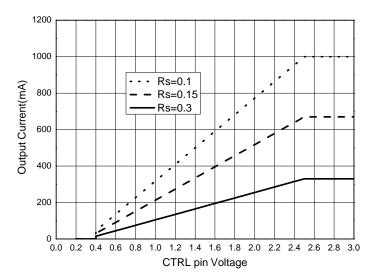


Figure 1. Analog Dimming Curve

#### Soft Start

The default soft start time for the AL8862 is 0.1ms - this provides very fast turn on of the output, improving PWM dimming accuracy.

Nevertheless, adding an external capacitor from the CTRL pin to ground will provide a longer soft-start delay. This is achieved by increasing the time for the CTRL voltage rising to the turn-on threshold and by slowing down the rising rate of the control voltage at the input of hysteresis comparator. The additional soft start time is related to the capacitance between CTRL and GND, the typical value will be 1.5ms/nF.

### Capacitor Selection

A low ESR capacitor should be used for input decoupling, as the ESR of this capacitor appears in series with the supply source impedance and will lower overall efficiency. This capacitor has to supply the relatively high peak current to the coil and smooth the ripple on the input current.

The minimum capacitance needed is determined by input power, cable's length and peak current.  $4.7 \sim 10 \mu F$  is a common used value for most of cases. A higher value will improve performance at lower input voltages, especially when the source impedance is high. The input capacitor should be placed as close as possible to the IC.

For maximum stability over temperature and voltage, capacitors with X7R, X5R, or better dielectric are recommended. Capacitors with Y5V dielectric are not suitable for decoupling in this application and should NOT be used.

#### **Diode Selection**

For maximum efficiency and performance, the freewheeling diode (D1) should be a fast low capacitance Schottky diode with low reverse leakage current. It also provides better efficiency than silicon diodes, due to lower forward voltage and reduced recovery time.

It is important to select parts with a peak current rating above the peak coil current and a continuous current rating higher than the maximum output load current. It is very important to consider the reverse leakage current of the diode when operating above +85°C. Excess leakage current will increase power dissipation.

The higher forward voltage and overshoot due to reverse recovery time in silicon diodes will increase the peak voltage on the SW output. If a silicon diode is used, more care should be taken to ensure that the total voltage appearing on the SW pin including supply ripple, won't exceed the specified maximum value.



### **Application Information (Cont.)**

### **Inductor Selection**

Recommended inductor value for the AL8862 are in the range 33µH to 100µH. Higher inductance are recommended at higher supply voltages in order to minimize output current tolerance due to switching delays, which will result in increased ripple and lower efficiency. Higher inductance also results in a better line regulation. The inductor should be mounted as close to the device as possible with low resistance connections to SW pins.

The chosen coil should have saturation current higher than the peak output current and a continuous current rating above the required mean output current.

The inductor value should be chosen to maintain operating duty cycle and switch 'on'/'off' times within the specified limits over the supply voltage and load current range. The following equations can be used as a guide.

SW Switch 'On' time

$$t_{ON} = \frac{L\Delta I}{V_{IN} - V_{LED} - I_{LED}(R_{SET} + R_L + R_{sw})}$$

SW Switch 'Off' time

$$t_{OFF} = \frac{L\Delta I}{V_{LED} + V_D + I_{LED}(R_{SET} + R_L)}$$

Where: L is the coil inductance;  $R_L$  is the coil resistance;  $R_{SET}$  is the current sense resistance;  $I_{LED}$  is the required LED current;  $\Delta I$  is the coil peak-peak ripple current (Internally set to 0.26 x  $I_{LED}$ ); VIN is the supply voltage;  $V_{LED}$  is the total LED forward voltage;  $R_{SW}$  is the switch resistance (0.55 $\Omega$  nominal);  $V_D$  is the diode forward voltage at the required load current.

### Thermal Protection

The AL8862 includes Over-Temperature Protection (OTP) circuitry that will turn off the device if its junction temperature gets too high. This is to protect the device from excessive heat damage. The OTP circuitry includes thermal hysteresis that will cause the device to restart normal operation once its junction temperature has cooled down by approximately +30°C.

### **Open-Circuit LED Protection**

The AL8862 has by default open LED protection. If the LEDs should become open circuit the AL8862 will stop oscillating; the SET pin will rise to VIN and the SW pin will then fall to GND. No excessive voltages will be seen by the AL8862.

### **LED Short-Circuit Protection**

If the LED string should become shorted together (the anode of the top LED becomes shorted to the cathode of the bottom LED) the AL8862 will continue to switch and the current through the AL8862's internal switch will still be at the expected current - so no excessive heat will be generated within the AL8862. However, the duty cycle at which it operates will change dramatically and the switching frequency will most likely decrease. See Figure 2 for an example of this behavior at 24V input voltage driving 3 LEDs.

The on-time of the internal power MOSFET switch is significantly reduced because almost all of the input voltage is now developed across the inductor. The off-time is significantly increased because the reverse voltage across the inductor is now just the Schottky diode voltage (See Figure 2) causing a much slower decay in inductor current.



### **Application Information (Cont.)**

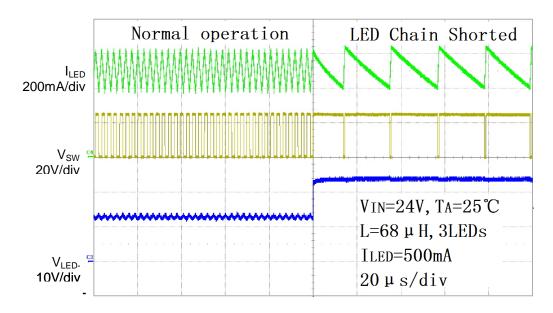


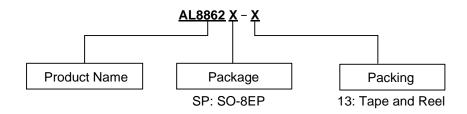
Figure 2. Switching Characteristics (Normal Operation to LED String Shorted)

### **Current Sense Resistor Short-Circuit Protection**

The AL8862 has an internal current limit at about 3A. If current-sense resistor R<sub>SET</sub> is shorted, current limit is triggered for accumulated 8 times and the switch will shut down and latch up.

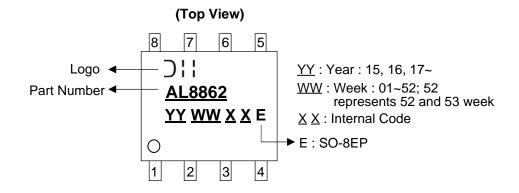


### **Ordering Information**



Dord Novellon	Bashama Oada	Dankana	13" Tape and Reel		
Part Number	Package Code	Package	Quantity	Part Number Suffix	
AL8862SP-13	SP	SO-8EP	2500/Tape & Reel	-13	

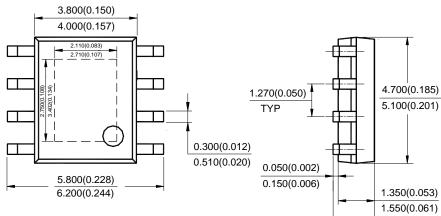
### **Marking Information**

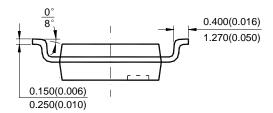




## Package Outline Dimensions (All dimensions in mm.)

### Package Type: SO-8EP



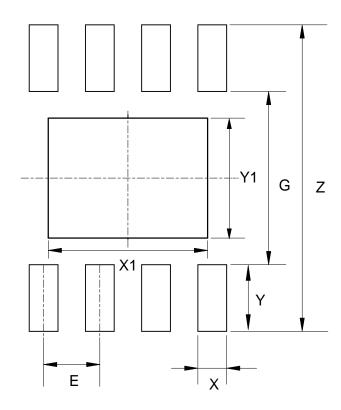


Note: Eject hole, oriented hole and mold mark is optional.



## Suggested Pad Layout

Package Type: SO-8EP



Dimensions	Z	G	Х	Υ	X1	Y1	E
	(mm)/(inch)						
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	3.600/0.142	2.700/0.106	1.270/0.050



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