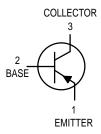
# **High Voltage Transistors PNP Silicon**



#### **MAXIMUM RATINGS**

Rating	Symbol	MPSA92	MPSA93	Unit
Collector-Emitter Voltage	VCEO	-300	-200	Vdc
Collector-Base Voltage	VCBO	-300	-200	Vdc
Emitter-Base Voltage	VEBO	-5.0		Vdc
Collector Current — Continuous	IC	-500		mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12		Watts mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150		°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	°C/W

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS		•			
Collector-Emitter Breakdown Voltage <sup>(1)</sup> (I <sub>C</sub> = -1.0 mAdc, I <sub>B</sub> = 0)	MPSA92 MPSA93	V(BR)CEO	-300 -200		Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = -100 μAdc, I <sub>E</sub> = 0)	MPSA92 MPSA93	V(BR)CBO	-300 -200	_ _	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = -100 μAdc, I <sub>C</sub> = 0)		V(BR)EBO	-5.0	_	Vdc
Collector Cutoff Current $(V_{CB} = -200 \text{ Vdc}, I_{E} = 0)$ $(V_{CB} = -160 \text{ Vdc}, I_{E} = 0)$	MPSA92 MPSA93	ІСВО	_ _	-0.25 -0.25	μAdc
Emitter Cutoff Current (V <sub>EB</sub> = -3.0 Vdc, I <sub>C</sub> = 0)		IEBO	_	-0.1	μAdc

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

Preferred devices are Motorola recommended choices for future use and best overall value.



\*Motorola Preferred Device





## MPSA92 MPSA93

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)		•			
DC Current Gain ( $I_C = -1.0 \text{ mAdc}$ , $V_{CE} = -10 \text{ Vdc}$ ) ( $I_C = -10 \text{ mAdc}$ , $V_{CE} = -10 \text{ Vdc}$ )	Both Types Both Types	hFE	25 40		_
$(I_C = -30 \text{ mAdc}, V_{CE} = -10 \text{ Vdc})$	MPSA92 MPSA93		25 25	_ _	
Collector-Emitter Saturation Voltage (I <sub>C</sub> = -20 mAdc, I <sub>B</sub> = -2.0 mAdc)	MPSA92 MPSA93	VCE(sat)		-0.5 -0.4	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = -20 mAdc, I <sub>B</sub> = -2.0 mAdc)		V <sub>BE(sat)</sub>	_	-0.9	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain — Bandwidth Product (IC = -10 mAdc, VCE = -20 Vdc, f = 100 MHz)		fΤ	50	_	MHz
Collector–Base Capacitance (V <sub>CB</sub> = -20 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	MPSA92 MPSA93	C <sub>cb</sub>		6.0 8.0	pF

<sup>1.</sup> Pulse Test: Pulse Width  $\leq 300~\mu s$ , Duty Cycle  $\leq 2.0\%$ .

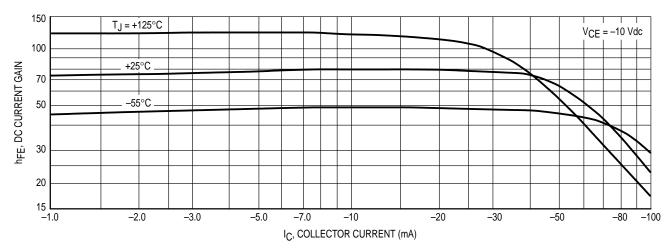


Figure 1. DC Current Gain

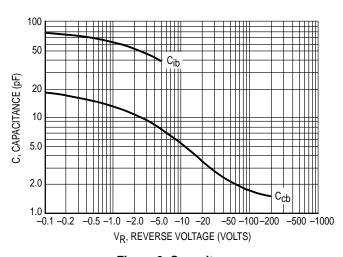


Figure 2. Capacitances

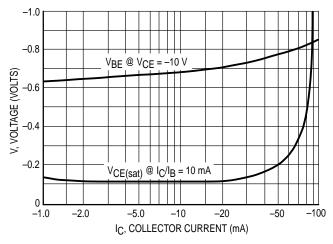


Figure 4. "On" Voltages

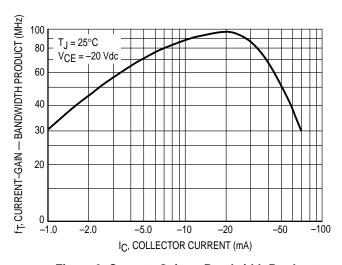


Figure 3. Current-Gain — Bandwidth Product

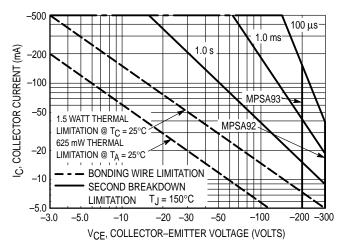
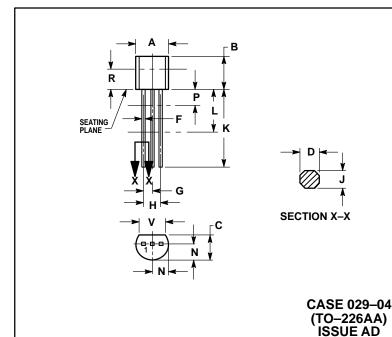


Figure 5. Active Region — Safe Operating Area

#### PACKAGE DIMENSIONS



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L. DIMENSION F APPLIES BETWEEN F AIND L.
  DIMENSION D AND J APPLY BETWEEN L AND K
  MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

STYLE 1: PIN 1. EMITTER

BASE 3. COLLECTOR

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