

Automotive N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY

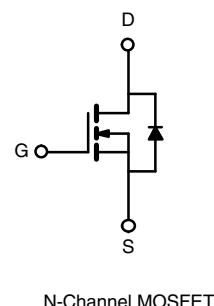
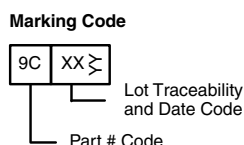
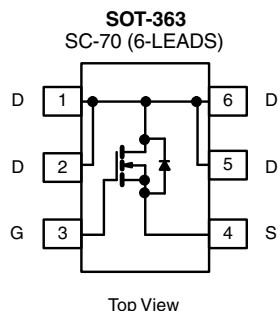
V_{DS} (V)	30
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5$ V	0.065
$R_{DS(on)}$ (Ω) at $V_{GS} = 2.5$ V	0.095
I_D (A)	2.8
Configuration	Single

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- AEC-Q101 Qualified^d
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE



ORDERING INFORMATION

Package	SC-70
Lead (Pb)-free and Halogen-free	SQ1470EH-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	
Continuous Drain Current ^a	I_D	$T_C = 25$ °C	A
		$T_C = 125$ °C	
Continuous Source Current (Diode Conduction) ^a	I_S	2.8	
Pulsed Drain Current ^b	I_{DM}	11	
Single Pulse Avalanche Current	I_{AS}	10	
Single Pulse Avalanche Energy	E_{AS}	5	mJ
Maximum Power Dissipation ^b	P_D	$T_C = 25$ °C	W
		$T_C = 125$ °C	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	125	°C/W
Junction-to-Foot (Drain)	R_{thJF}	45	

Notes

- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.

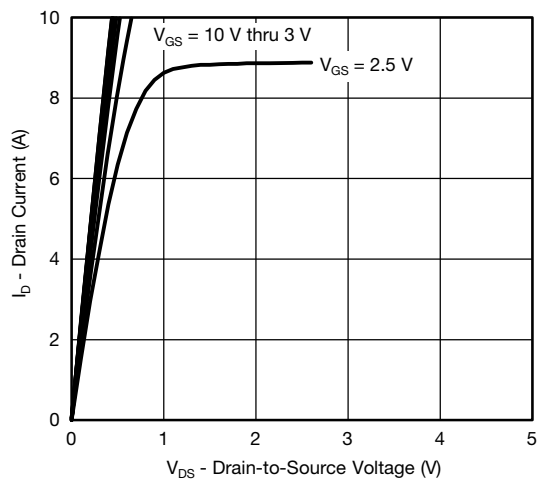
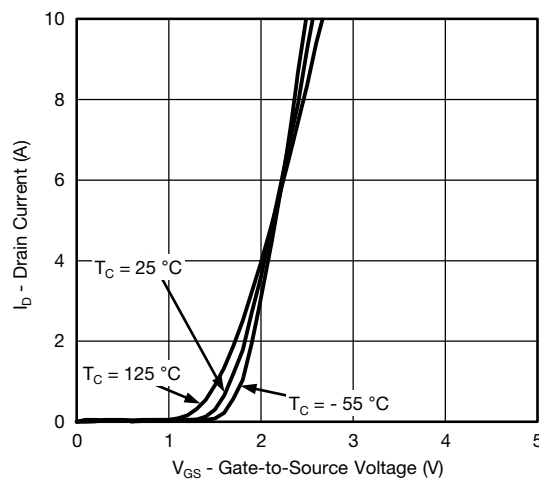
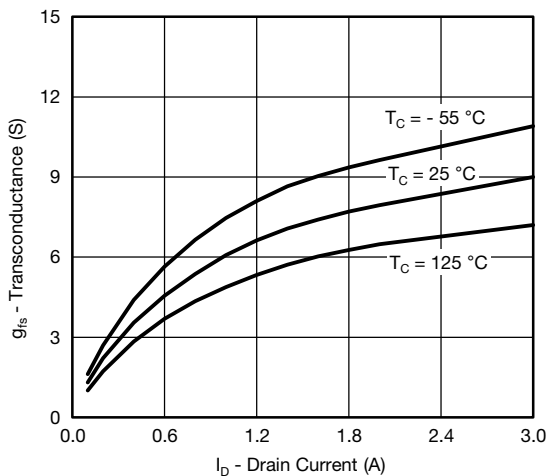
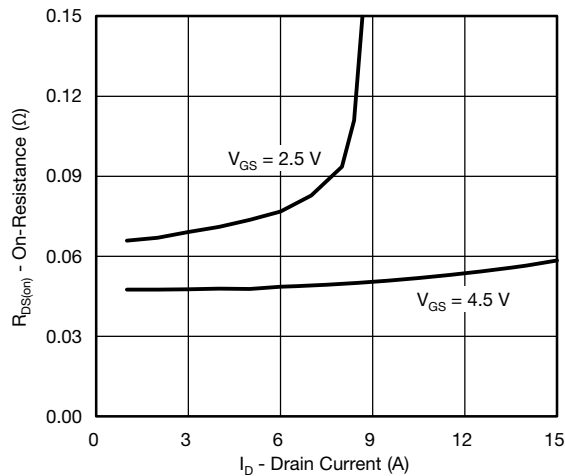
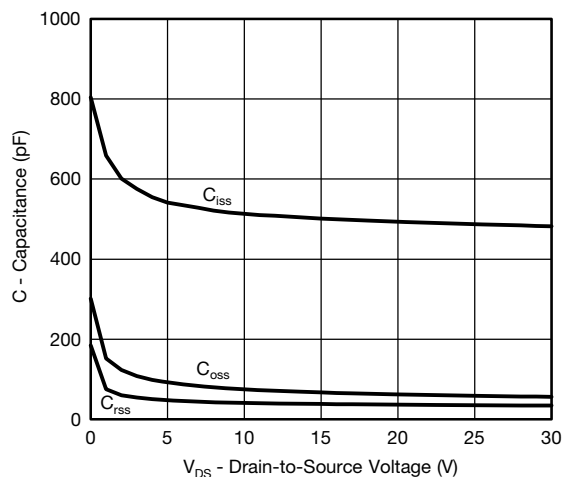
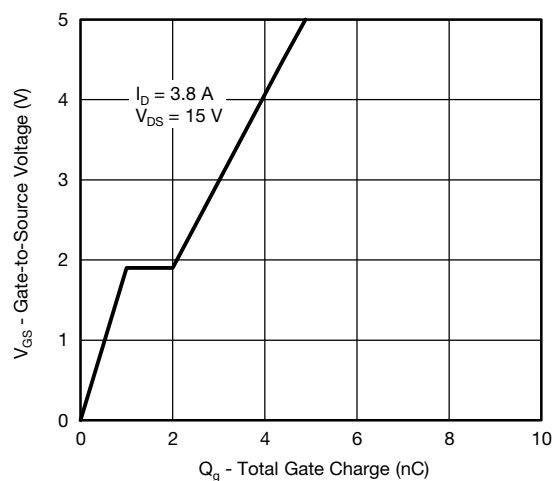


SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA		30	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		0.6	1.0	1.6	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 12 V		-	-	± 500	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 30 V	-	-	1	μA
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 175 °C	-	-	150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 4.5 V	V _{DS} ≥ 5 V	5	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V	I _D = 3.8 A	-	0.050	0.065	Ω
		V _{GS} = 4.5 V	I _D = 3.8 A, T _J = 125 °C	-	-	0.097	
		V _{GS} = 4.5 V	I _D = 3.8 A, T _J = 175 °C	-	-	0.115	
		V _{GS} = 2.5 V	I _D = 3.1 A	-	0.070	0.095	
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 2 A		-	8	-	S
Dynamic ^b							
Input Capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 25 V, f = 1 MHz	-	488	610	pF
Output Capacitance	C _{oss}			-	60	75	
Reverse Transfer Capacitance	C _{rss}			-	36	45	
Total Gate Charge ^c	Q _g	V _{GS} = 4.5 V	V _{DS} = 15 V, I _D = 3.8 A	-	4.4	6.6	nC
Gate-Source Charge ^c	Q _{gs}			-	1	-	
Gate-Drain Charge ^c	Q _{gd}			-	1	-	
Gate Resistance	R _g	f = 1 MHz		3	6.35	9.7	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 15 V, R _L = 3.9 Ω I _D ≥ 3.8 A, V _{GEN} = 4.5 V, R _g = 1 Ω		-	8	12	ns
Rise Time ^c	t _r			-	13	20	
Turn-Off Delay Time ^c	t _{d(off)}			-	14	21	
Fall Time ^c	t _f			-	8	12	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	11	A
Forward Voltage	V _{SD}	I _F = 2.5 A, V _{GS} = 0 V		-	0.8	1.2	V

Notes

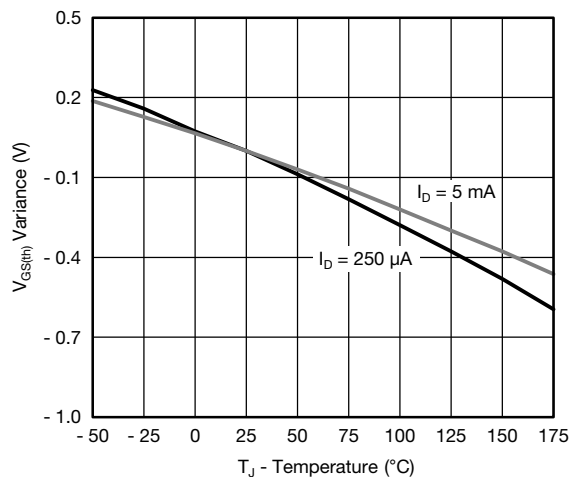
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

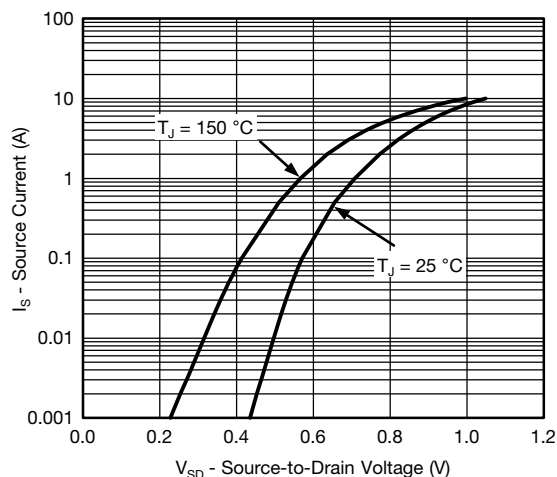
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Output Characteristics

Transfer Characteristics

Transconductance

On-Resistance vs. Drain Current

Capacitance

Gate Charge



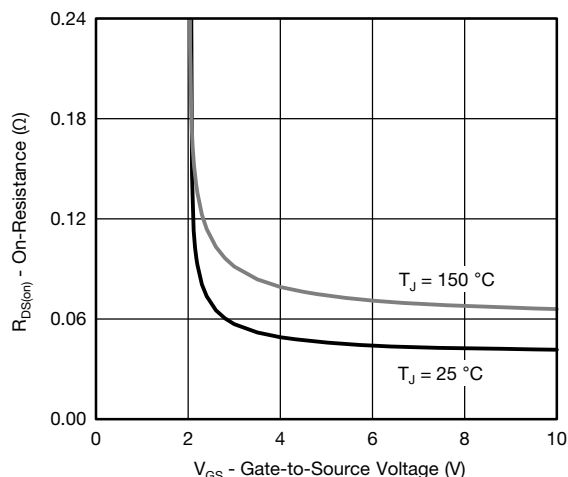
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



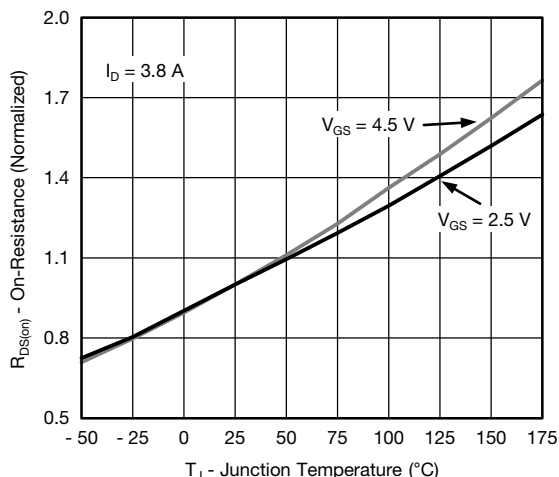
Threshold Voltage



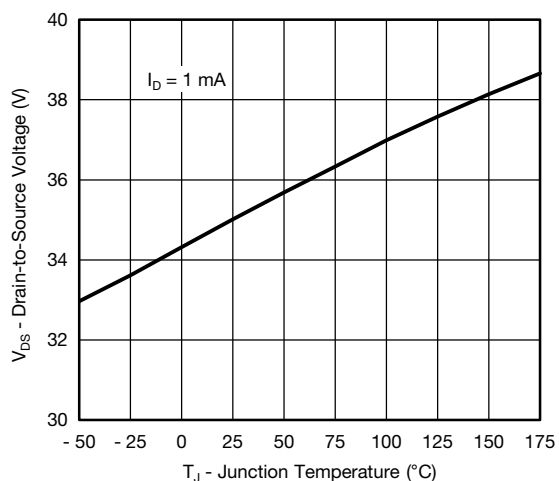
Source Drain Diode Forward Voltage



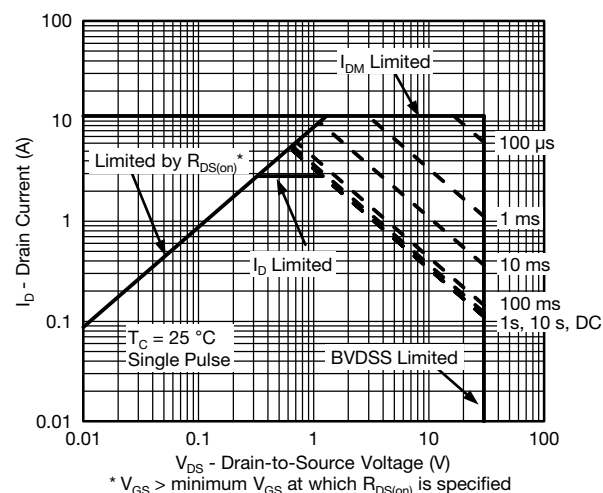
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature



Drain Source Breakdown vs. Junction Temperature

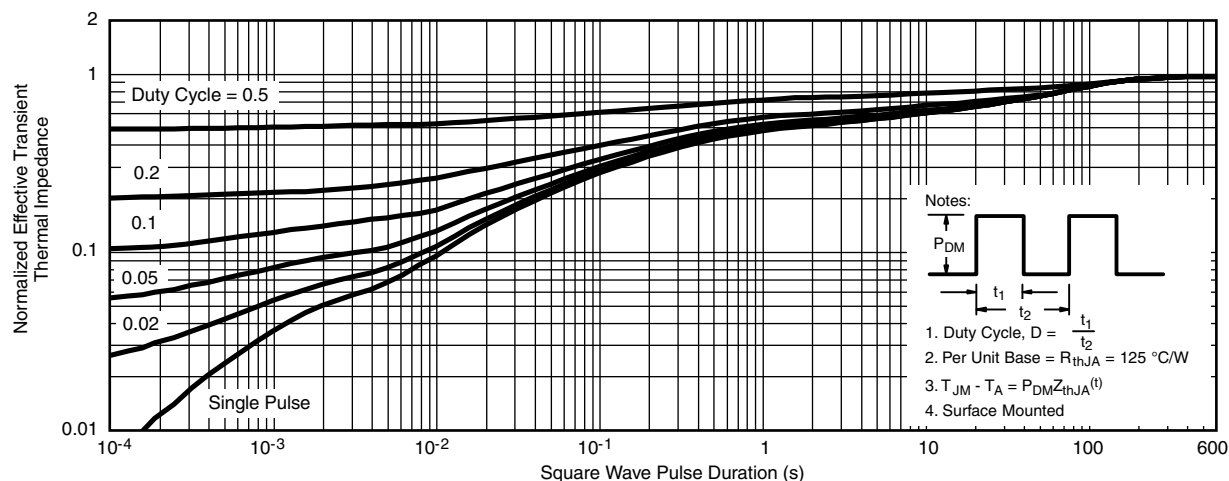


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

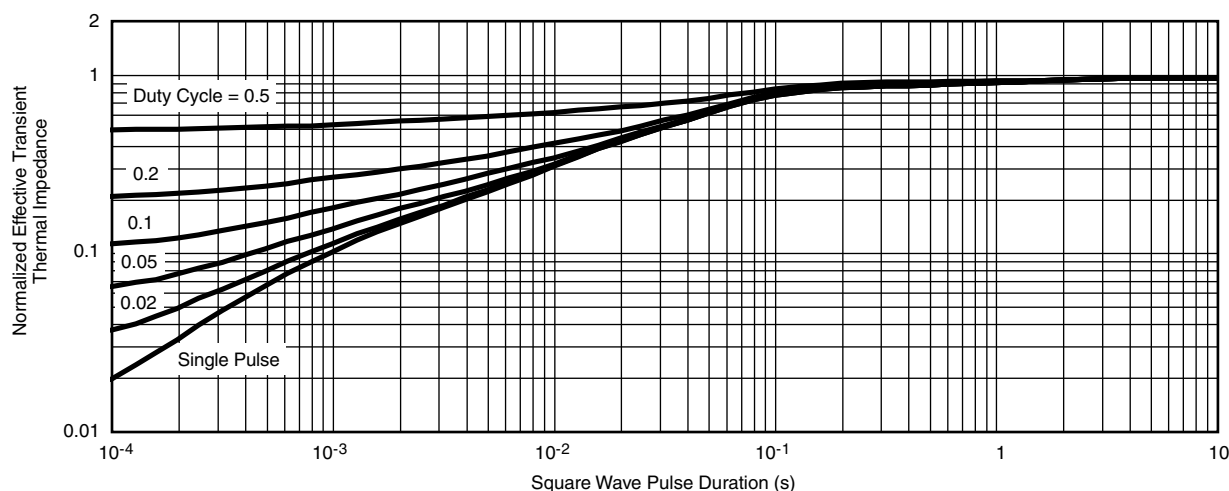
Safe Operating Area



THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



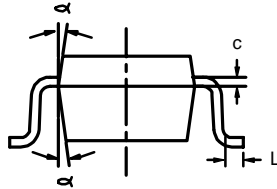
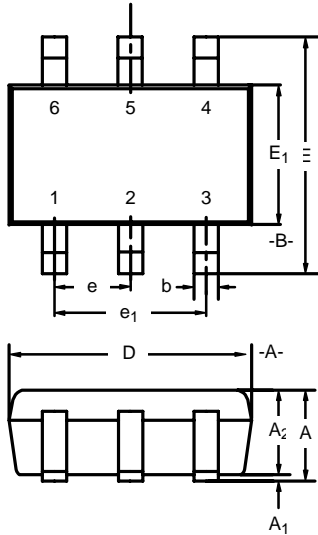
Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Foot ($25\text{ }^{\circ}\text{C}$)are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size $1\text{''} \times 1\text{''} \times 0.062\text{''}$, double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

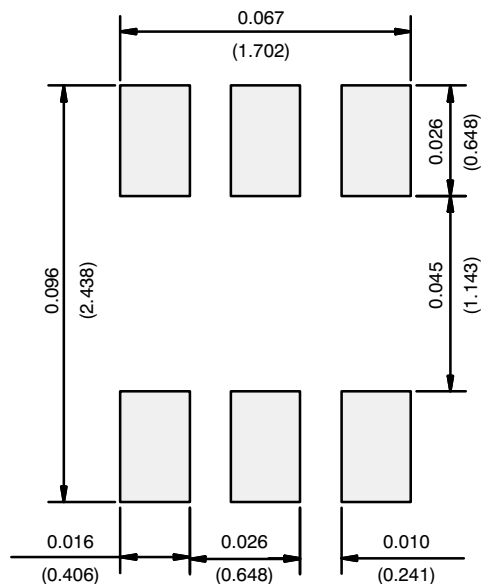
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SC-70: 6-LEADS



	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
A	0.90	—	1.10	0.035	—	0.043
A ₁	—	—	0.10	—	—	0.004
A ₂	0.80	—	1.00	0.031	—	0.039
b	0.15	—	0.30	0.006	—	0.012
c	0.10	—	0.25	0.004	—	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071	0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
e	0.65BSC			0.026BSC		
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
α	7°Nom			7°Nom		
ECN: S-03946—Rev. B, 09-Jul-01						
DWG: 5550						

RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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