

FDPF15N65

N-Channel UniFET™ MOSFET

650 V, 15 A, 440 mΩ

Features

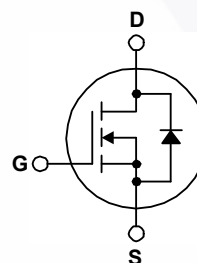
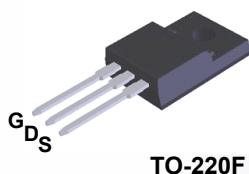
- $R_{DS(on)} = 360 \text{ m}\Omega$ (Typ.) @ $V_{GS} = 10 \text{ V}$, $I_D = 7.5 \text{ A}$
- Low Gate Charge (Typ. 48.5 nC)
- Low C_{rss} (Typ. 23.6 pF)
- 100% Avalanche Tested

Applications

- LCD/LED/PDP TV and Monitor
- Uninterruptible Power Supply

Description

UniFET™ MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter		FDPF15N65	Unit
V_{DSS}	Drain-Source Voltage		650	V
I_D	Drain Current	- Continuous ($T_C = 25^\circ\text{C}$)	15*	A
		- Continuous ($T_C = 100^\circ\text{C}$)	9.5*	A
I_{DM}	Drain Current	- Pulsed (Note 1)	60*	A
V_{GSS}	Gate-Source voltage		± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		637	mJ
I_{AR}	Avalanche Current (Note 1)		15	A
E_{AR}	Repetitive Avalanche Energy (Note 1)		25.0	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	38.5	W
		- Derate Above 25°C	0.3	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	$^\circ\text{C}$

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FDPF15N65	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	3.3	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDPF15N65	FDPF15N65	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA, T _J = 25°C	650	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.65	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V V _{DS} = 520 V, T _C = 125°C	-- --	-- --	1 10	μA μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	3.0	--	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 7.5 A	--	0.36	0.44	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 7.5 A	--	19.2	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	--	2380	3095	pF
C _{oss}	Output Capacitance		--	295	385	pF
C _{rss}	Reverse Transfer Capacitance		--	23.6	35.5	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 325 V, I _D = 15 A, V _{GS} = 10 V, R _G = 21.7 Ω (Note 4)	--	65	140	ns
t _r	Turn-On Rise Time		--	125	260	ns
t _{d(off)}	Turn-Off Delay Time		--	105	220	ns
t _f	Turn-Off Fall Time		--	65	140	ns
Q _g	Total Gate Charge	V _{DS} = 520 V, I _D = 15 A, V _{GS} = 10 V (Note 4)	--	48.5	63.0	nC
Q _{gs}	Gate-Source Charge		--	14.0	--	nC
Q _{gd}	Gate-Drain Charge		--	21.2	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	15*	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	60	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 15 A	--	--	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 15 A, dI _F /dt =100 A/μs	--	496	--	ns
Q _{rr}	Reverse Recovery Charge		--	5.69	--	μC

Notes:

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. $L = 5.23\text{ mH}$, $I_{AS} = 15\text{ A}$, $V_{DD} = 50\text{ V}$, $R_G = 25\text{ }\Omega$, starting $T_J = 25^\circ\text{C}$.
3. $I_{SD} \leq 15\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, starting $T_J = 25^\circ\text{C}$.
4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

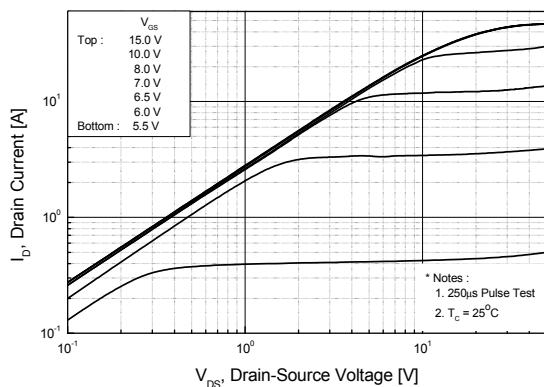


Figure 2. Transfer Characteristics

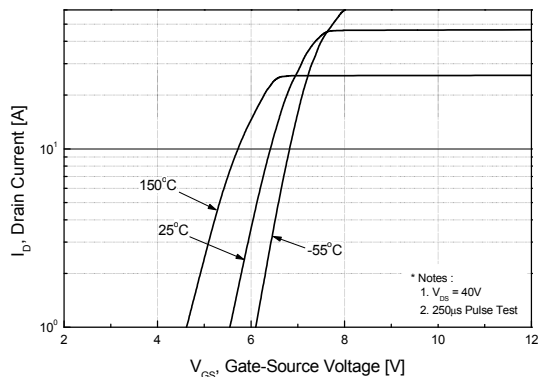


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

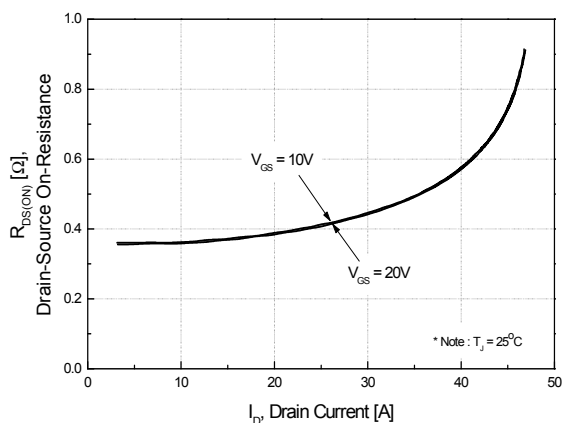


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

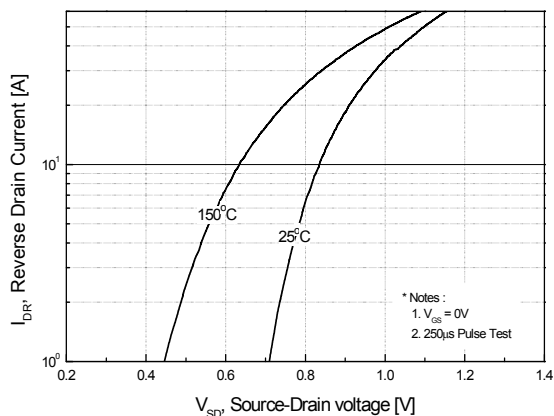


Figure 5. Capacitance Characteristics

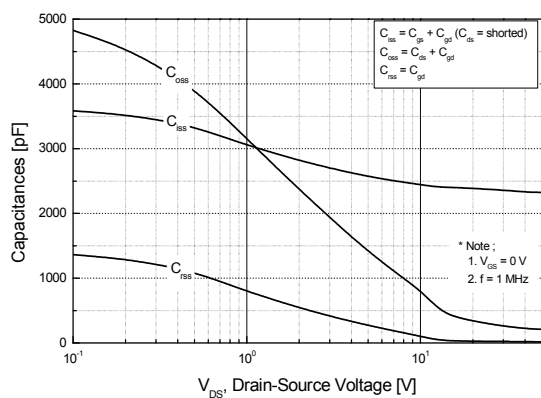
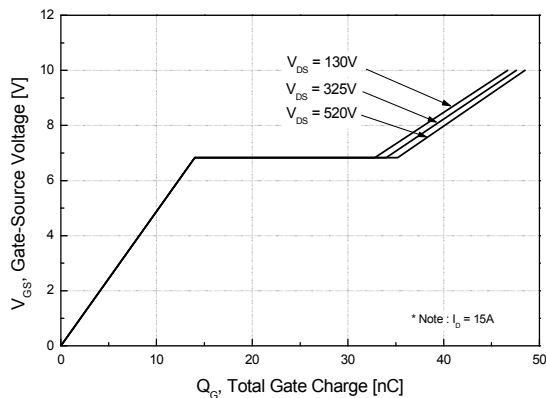


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

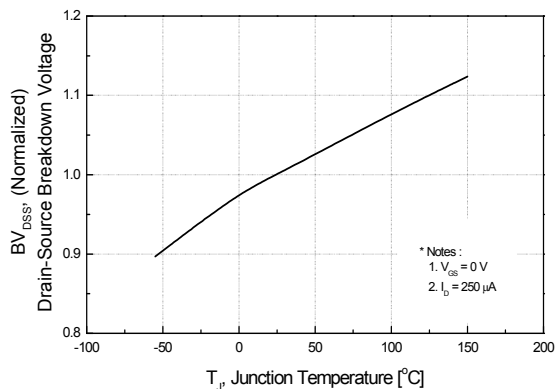


Figure 8. On-Resistance Variation vs. Temperature

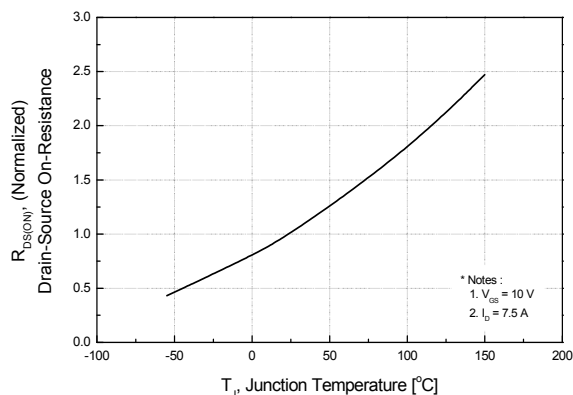


Figure 9. Safe Operating Area

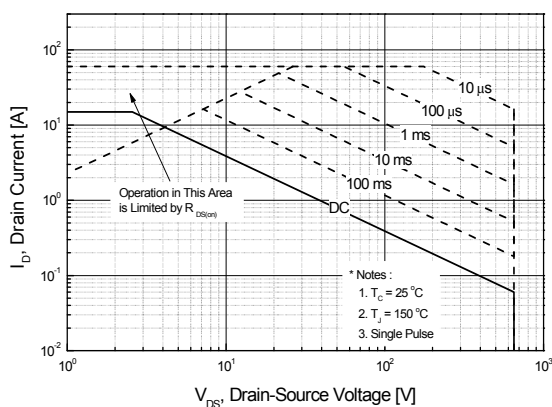


Figure 10. Maximum Drain Current vs. Case Temperature

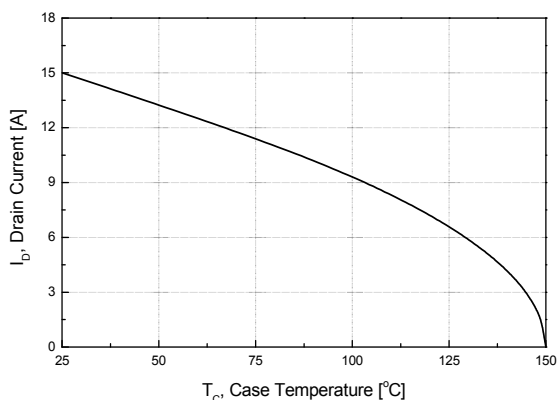
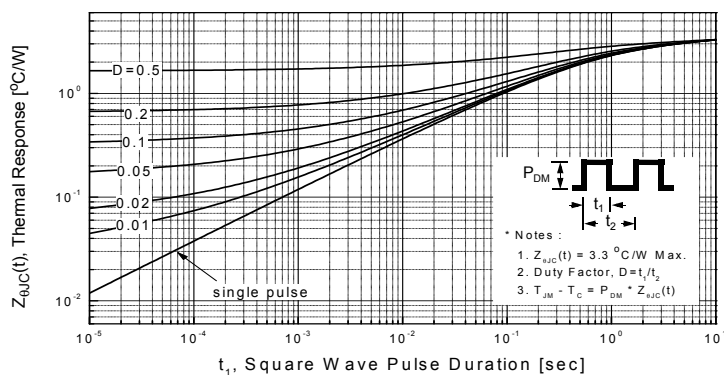


Figure 11. Transient Thermal Response Curve



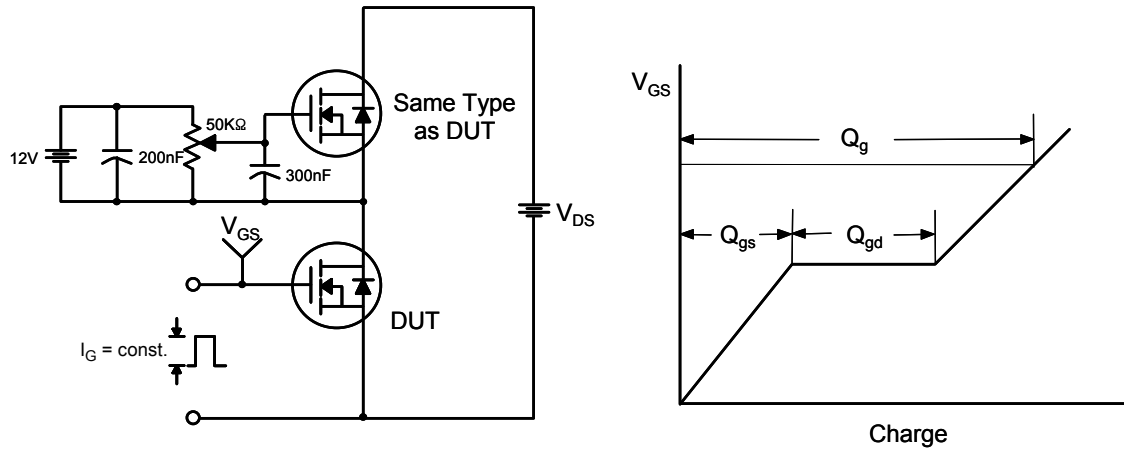


Figure 12. Gate Charge Test Circuit & Waveform

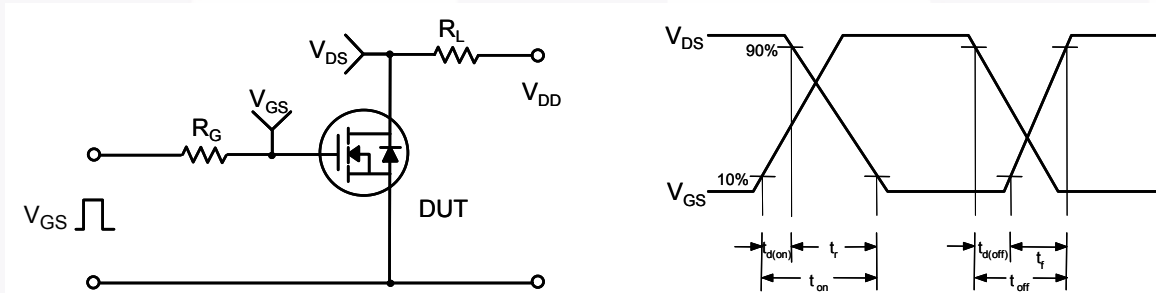


Figure 13. Resistive Switching Test Circuit & Waveforms

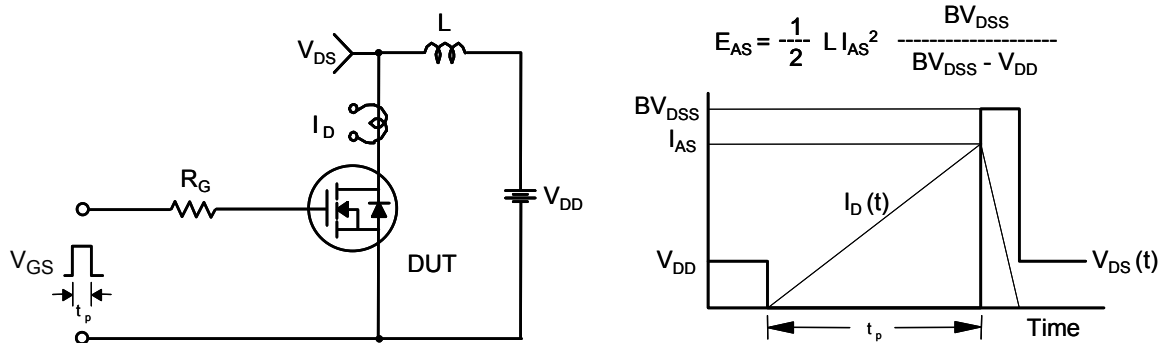


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

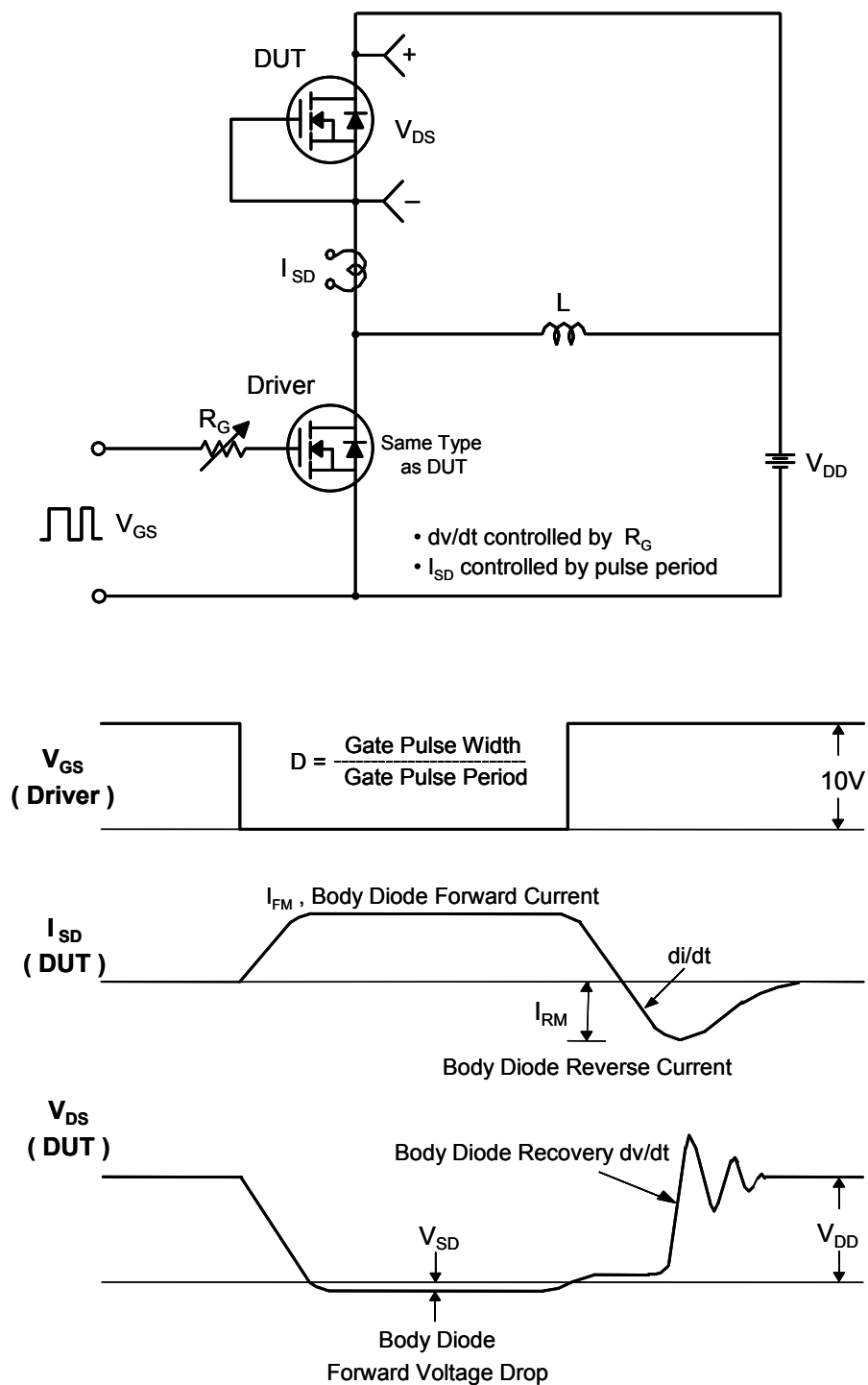


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

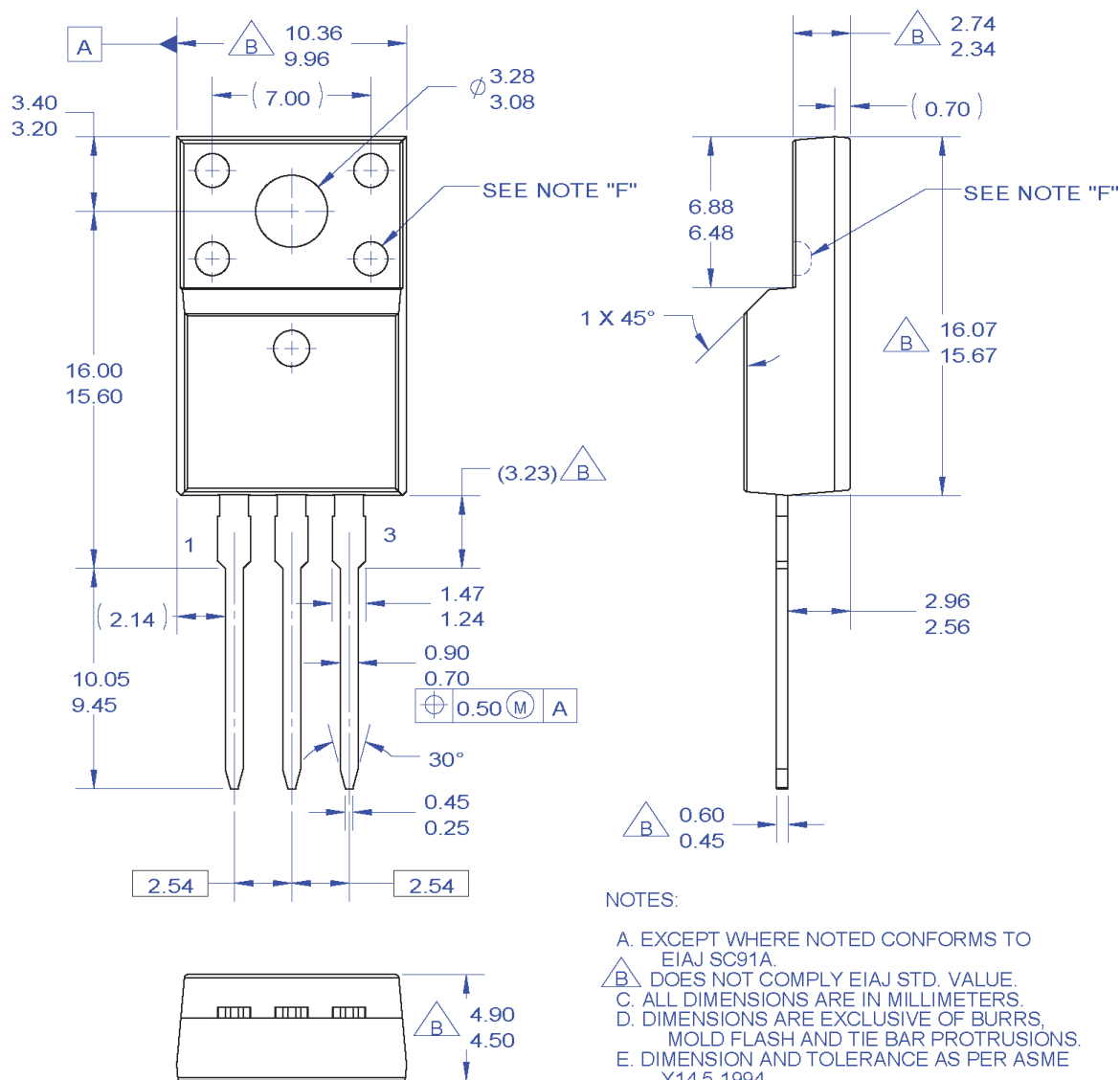


Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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