

TENTATIVE

VERTICAL DEFLECTION OUTPUT CIRCUIT

The KIA6340K is a vertical deflection output IC for TVs and CRT displays with excellent image Quality that use a bus control system signal processing IC.

This IC can drive the direct (even including a DC component) deflection yoke with the sawtooth wave output from the bus control system signal processing IC.

FEATURES

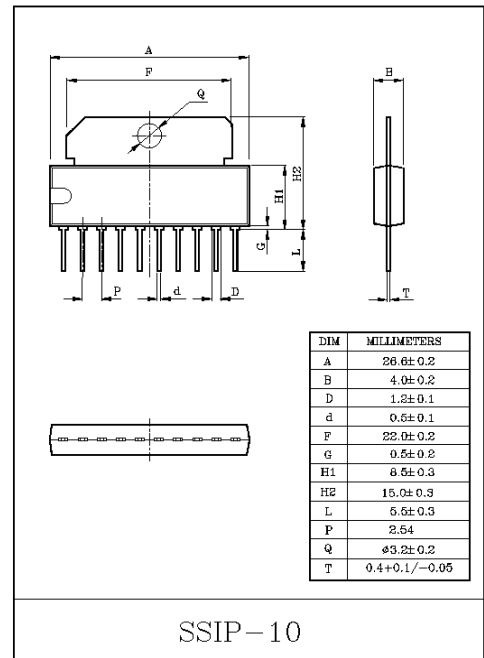
- Low power dissipation due to built-in pump-up circuit
- Vertical output circuit.
- Built-in thermal protection circuit.
- Excellent crossover characteristics.
- DC coupling possible

MAXIMUM RATINGS (Ta=25℃)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{CC8}	34	V
Output Block Supply Voltage	V_{CC5}	70	V
Deflection Output Current	$I_{4(max)}$	-1.5~1.5	A
Thermal resistance	θ_{j-c}	4.0	℃/W
Allowable Power Dissipation	$P_D \text{ max}$	9	W
Operating Temperature	T_{opr}	-20~85	℃
Storage Temperature	T_{stg}	-40~150	℃

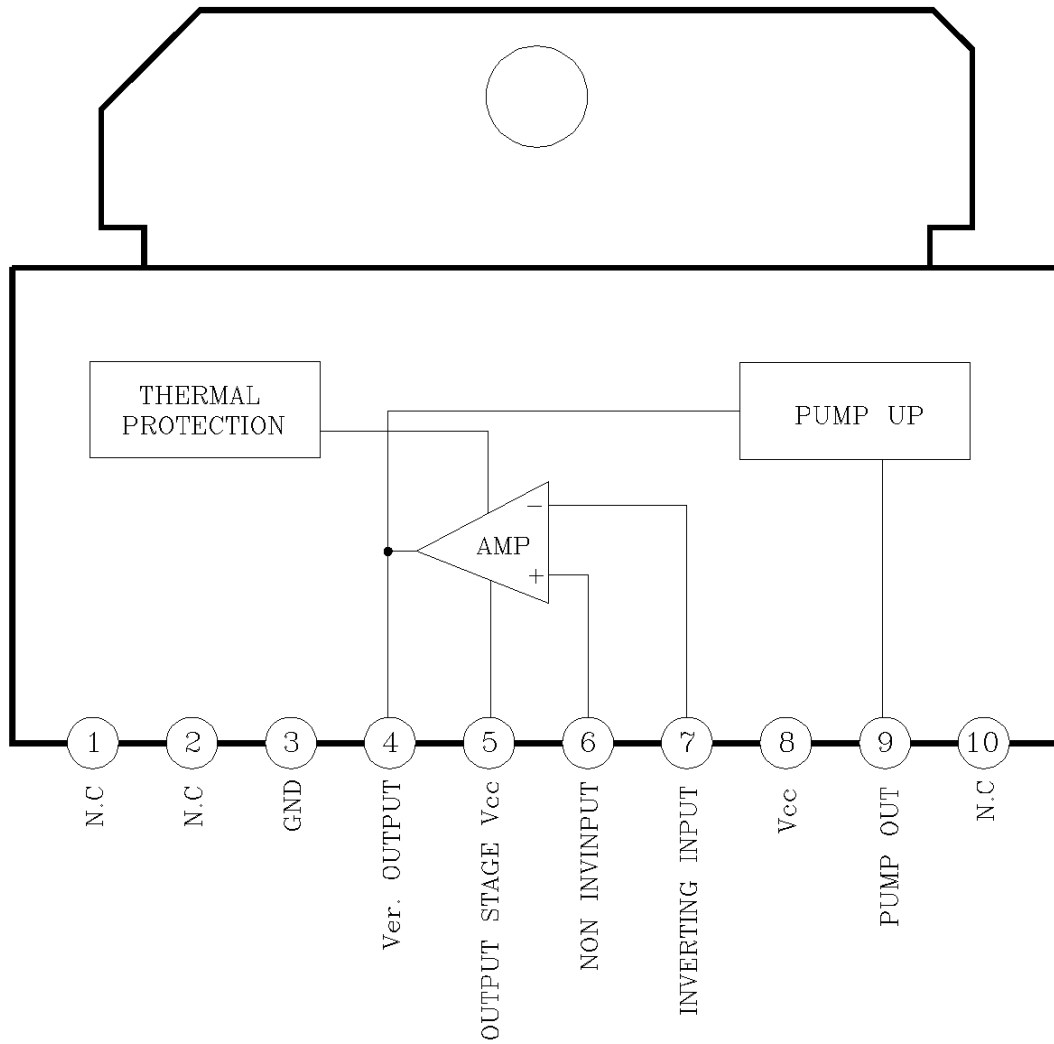
Operating Conditions (Ta=25℃)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Recommended Supply Voltage	V_{CC8}	24	V
Operating Supply Voltage Range	$V_{CC8 \text{ OP}}$	16~33	V
Recommended deflection Output Current	I_{4p-p}	1.8	A _{P-P}



KIA6340K

BLOCK DIAGRAM



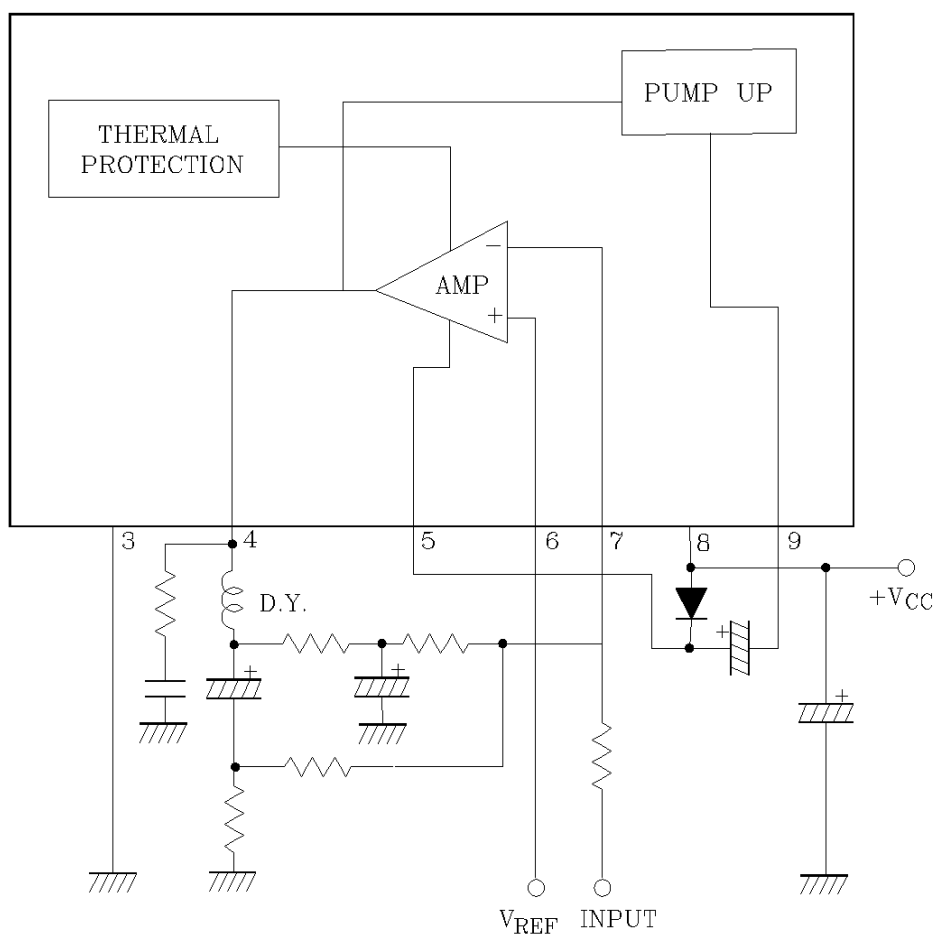
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ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, $V_{CC}=24V$, $T_a=25^{\circ}C$)

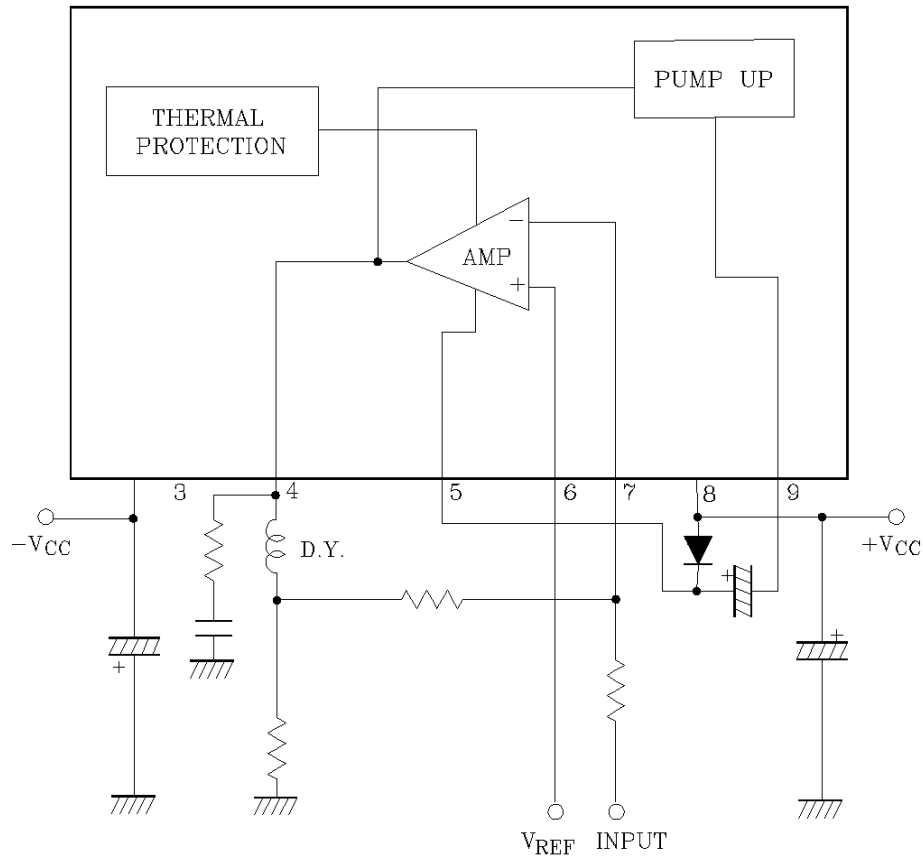
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Pump-up Charge Saturation Voltage	V_{S9-3}	$I_9=20mA$	-	-	1.8	V
Pump-up Discharge Saturation Voltage	V_{S8-9}	$I_9=-0.9A$	-	-	3.0	V
Deflection Output Saturation Voltage (lower)	V_{S4-3}	$I_4=0.9A$	-	-	1.3	V
Deflection Output Saturation Voltage (upper)	V_{S5-4}	$I_4=-0.9A$	-	-	3.2	V
Idling Current	I_{DL}		35	-	65	mA
Midpoint Voltage	V_{MD}		11.0	12.0	13.0	V

APPLICATION CIRCUIT (SINGLE POWER SUPPLY)



KIA6340K

APPLICATION CIRCUIT (Double power supply)



APPLICATION CIRCUIT

