

# MULTIPLIER - DIVIDER

## **FEATURES**

- DIFFERENTIAL INPUTS
- LASER-TRIMMED
- GUARANTEED ACCURACY 0.5% and 1%
- SELF-CONTAINED
   No additional parts required
- LOW NOISE 120Vrms, 10Hz – 10kHz
- DIP PACKAGE

## APPLICATIONS

- MULTIPLICATION
- DIVISION
- SQUARING
- SQUARE ROOTING
- ADAPTIVE CONTROL
- ALGEBRAIC COMPUTATION
- POWER COMPUTATION

# DESCRIPTION

The 4214 family of multipliers are low-cost integrated circuit multiplier/dividers designed for general purpose usage. In addition to four-quadrant multiplication, they also perform division and square rooting of analog signals. They do not require use of additional amplifiers to perfrom these functions. The 4214 is laser-trimmed prior to final packaging and is guaranteed to its rated accuracy with no external components—a distinct advantage from standpoints of cost and reliability.

The 4214 contains its own zener-regulated references and, as a result, is much less sensitive to supply voltage variation than were earlier IC multipliers. The multipliers' output noise is only 120Vrms in a 10Hz to 10kHz bandwidth.

The unit is packaged in a 14-pin ceramic DIP package and available in both -25°C to +85°C and -55°C to +125°C specification temperature ranges.

# **SPECIFICATIONS**

## **ELECTRICAL**

Typical performance at +25°C with rated power supplies unless otherwise noted.

MODEL	4214AP/RM	4214BP/SM	
OUTPUT FUNCTION	(X <sub>1</sub> - X <sub>2</sub> ) (	$\frac{(X_1 - X_2)(Y_1 - Y_2)}{(X_1 - X_2)(Y_1 - Y_2)} + Z_2$	
TOTAL ERROR(1)			
Without Trimming	i% max	0.5% max	
Error vs Temperature (-25°C to +85°C), (AP and BP) (-55°C to +125°C), (RM and SM)		0.008%/°C typ., 0.02%/°C max 0.025%/°C typ., 0.05% °C max	
Error vs Supply	0.05%/%		
INDIVIDUAL ERRORS			
Output Offset	10mV typ	7mV typ	
onput ontal	50mV max		
vs Temperature	0.7mV/°C typ		
	2mV/°C max		
vs Supply		nV/%	
Scale Factor Error		2%	
vs Temperature		08% °C	
vs Supply		%/%	
Nonlinearity			
$X(X = 20V p-p, Y = \pm 10VDC)$	±0.	08%	
$Y(Y = 20V p - p, X = \pm 10VDC)$	±0.	01%	
Feedthrough at 50 Hz	1		
X = 20V p-p, Y = 0	30m	V p-p	
Y = 20V p-p, X = 0	6mV p-p		
vs Temperature	0.1mV p-p: °C		
vs Supply	0.15mV p-p. %		
AC PERFORMANCE	1		
Small Signal ±3dB Flatness		kHz kHz	
Small Signal ±1% Flatness			
Small Signal ±1% Vector Error (0.57" Phase Shift)		7.5 kHz 330 kHz	
Full Power Bandwidth	23V - µ5		
Siew Rate	1.7µs		
Settling Time to 1% (20V step)	+	- μ-	
OUTPUT NOISE (X = Y = 0)			
10 Hz to 10 kHz	120µV rms 700µV rms		
10 Hz to 10 MHz	700	V rms	
INPUT CHARACTERISTICS			
Input Voltage Range	1		
Rated Operation, min.		±10V	
Absolute max	1	±V,	
Input Impedance, X, Y, Z <sup>(2)</sup>		10 MΩ	
Input Bias Current, X, Y, Z	1	.4μA	
OUTPUT CHARACTERISTICS			
Rated Output	*10V at	±5mA min	
Output Impedance		1.50	
Compos Ampedance			
POWER SUPPLY REQUIREMENTS	1		
Rated Voltage	1 :	15V	
	±8.5VDC	to ±20VDC	
Operating Range	±5.5mA		
Operating Range Quiescent Current			
Quiescent Current			
Quiescent Current TEMPERATURE RANGE AP and BP		to +85°C	
Quiescent Current TEMPERATURE RANGE Rated Performance (specification) RM and SM	-55°C	to +125°C	
Quiescent Current TEMPERATURE RANGE AP and BP	-55°C		

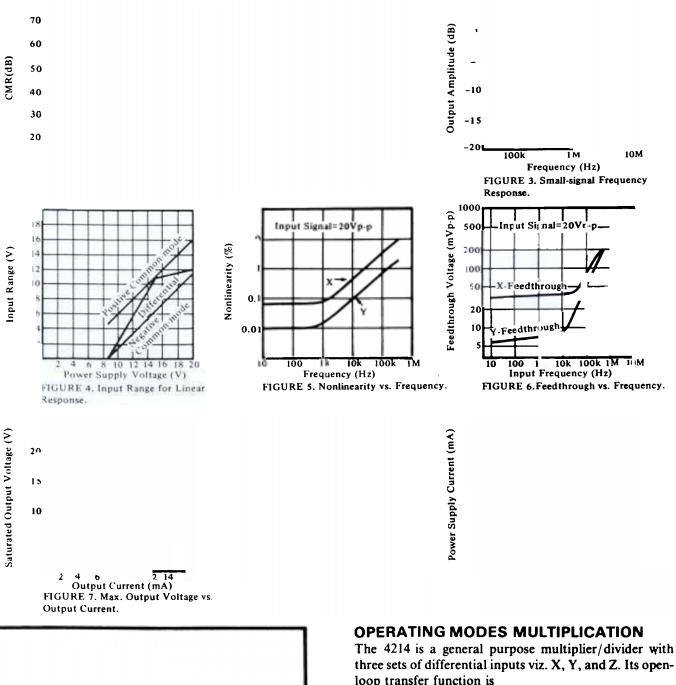
### **MECHANICAL**

	al-In-Li				
NOTE	Lond	e ie teur	o positiir		
			e positio		
within	0.010"	(0.25m)	e position) R at		
within at sea	0.010" ting pla	(0.25m ane.	m) R at	ммс	
within at sea Pin no	0.010" ting pla umbers	(0.25m ane. shown	m) R at	ммс	
within at sea Pin nu only.	0.010" ting pla umbers Numbe	(0.25m ane. shown rs may	m) R at for refe not be	ммс	
within at sea Pin nu only.	0.010" ting pla umbers Numbe ed on p	(0.25m ane. shown rs may ackage	m) R at for refe not be	MMC	
within at sea Pin no only. marks	0.010" ding pla umbers Numbe ed on p	(0.25m ane. shown rs may ackage. HES	for refe	MMC	
within at sea Pin no only. I market	ting pla umbers Number ed on p	(0.25m ane. shown rs may ackage HES MAX	for reference to the most be MILLIN MIN	MMC rence	
Pin nu only.	0.010" ting pla umbers Numbe ed on p	(0.25m ane. shown rs may ackage HES MAX .710	for reference to the most be MILLIN MIN 17.02	MMC rence ETERS MAX 18.03	
within at sea Pin nu only. I market	0.010" ting pla ting pla umbers Numbe ed on p  INC MIN 670 065	(0.25m ane. shown rs may ackage. HES MAX 710	for reference be MILLIN MIN 17.02	MMC rence ETERS MAX 18.03 4.32	
within at sea Pin no only. I market	0.010" ting pla umbers Numbe ed on p  INC MIN 670 065	(0.25m ane. shown rs may ackage. HES MAX 710 170 021	for reference to the most be MILLIN MIN 17:02 1:65 0:38	MMC rence MAX 18.03 4.32 0.53	
within at sea Pin no only. I market	0.010" ting pla umbers Numbe ed on p  INC MIN 670 066 015	(0.25m ane. shown rs may ackage HES MAX 710 170 021	for reference to the most be much to the m	MMC rence MAX 18.03 4.32 0.53 1.52	
Pin nu only . marke	Numbers Number	(0.25mane. shown rs may ackage. HES MAX P10 170 021 090 SASIC	m) R at for reference be motibe MILLIN 17:02 1:65 0:38 1:14 2:54 8	MMC erence MAX 18.03 4.32 0.53 1.52 ASIC	
DIM A C D F G H	0.010" ting plu umbers Numbe ed on p  INC MIN 670 066 015 046 100 8	(0.25mane. shown rs may ackage. HES MAX P10 170 021 090 6ASIC	m) R at for reference be motibe MILLIN 17:02 1:65 0:38 1:14 2:54 8 0:64	MMC erence MAX 18:03 4:32 0.53 1:52 ASIC 1.78	
within at sea Pin no only. market DIM A C D F G H A	0.010" ting plu umbers Numbe ed on p  INC MIN 670 065 015 100 6 025	(0.25m ane. shown rs may ackage HES MAX P10 021 060 6ASIC 012	mi) R at for rete not be MILLIN MIN 17:02 1:65 0:38 1:14 2:54 8 0:64	MMC erence MAX 18:03 4:32 0:53 1:52 ASIC 1.78 0:30	
DIM A C D F G H	0.010" ting plu umbers Numbe ed on p  INC MIN 670 066 015 046 100 8 025	(0.25m ane. shown rs may ackage HES MAX P10 021 060 6ASIC 012	m) Flat for refe not be MILLIM MIN 17:02 1:65 0:38 1:14 2:54 8 0:64 0:20 3:06	MMC erence MAX 18:03 4:32 0:53 1:52 ASIC 1.78 0:30	
within at sea Pin no only. marke	0.010" ting plu umbers Numbe ed on p  INC MIN 670 066 015 046 100 8 025	(0.25mane. shown rs may ackage HES MAX .710 .170 .021 .080 .ASIC .012 .240	m) Flat for refe not be MILLIM MIN 17:02 1:65 0:38 1:14 2:54 8 0:64 0:20 3:06	MMC enence MAX 18.03 4.32 0.53 1.52 ASIC 1.78 0.30 6.10	

## **CONNECTION DIAGRAM**

- 1. Total error is the maximum allowed value of the sum of the individual errors.
- 2.  $Z_2$  input impedance is 10 M $\Omega$  typ with Pin 11 open circuit. If Pin 11 is grounded or used for optional offset adjustment the  $Z_2$  input impedance may become as low as 25k $\Omega$ .

# TYPICAL PERFORMANCE CURVES



three sets of differential inputs viz. X, Y, and Z. Its openloop transfer function is

$$e_0 = A \left[ \frac{(X_1 - X_2)(Y_1 - Y_2)}{10} - (Z_1 - Z_2) \right]$$

where, A is the open-loop gain of the internal output amplifier (see the simplified equivalent circuit, Figure 10). Due to very high gain  $(A \rightarrow \infty)$  of the output amplifier the feedback from the output to any of the inputs will establish the relationship

$$Z_1 - Z_2 = (X_1 - X_2) (Y_1 - Y_2) / 10$$

Taking output at Z<sub>1</sub> the multiplication mode transfer function is obtained and is expressed as

$$e_0 = \frac{(X_1 - X_2)(Y_1 - Y_2)}{10} + Z_2.$$

This connection of 4214 is shown on page 2.

#### DIVISION

The 4214 may be used as a two quadrant divider, without the need for an external op amp. Note that the maximum output error in the divide mode is given approximately by,

Divider error 
$$\simeq \frac{10 \in m}{X_1 - X_2}$$
, where  $\in m$  is

the total error specified for the multiply mode. The divider error, as shown above, becomes excessively large for small values of  $(X_1 - X_2)$ . A 10:1 denominator range is usually the practical limit. This is true for all such units, where a multiplier is used in voltage feedback mode to generate "divide" function.

If more accurate division is required over wide range of denominator voltages, the Burr-Brown model 4291 is recommended (0.25% max error over 100:1 range).

For optimum performance, the Z offset should be nulled by letting the input be zero and adjusting  $R_1$  for zero output. This offset adjustment will improve the divider error to about  $3 \in_{\mathbf{M}}$  for  $(X_1 - X_2)$  much less than 10V.

FIGURE 11. Divide Mode Connections—4214.

#### **SQUARE ROOT**

By applying feedback from the output to both the X and Y inputs, the square root function can be obtained. The errors in the square root mode become large for small values of Z input. The actual output is approximately

Square root output  $e_0 = \sqrt{10(Z_1 - Z_2) + 10} \in m$ where  $\in m$  is the total error for the multiply mode.

Burr-Brown's multifunction converter model 4302 is recommended for applications requiring more accuracy over wider dynamic range.

The output offset should be nulled for optimum performance by allowing the input to be its smallest expected value and adjusting  $R_1$  for the proper output voltage.

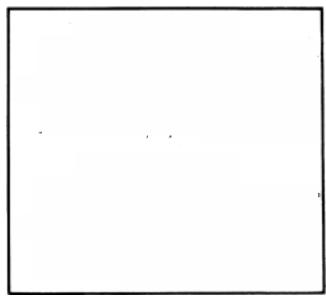


FIGURE 12. Square Root Mode Connections—4214.

#### SINE FUNCTION GENERATOR

Two 4214's can be connected with implicit feedback as shown in Figure 13 to implement the following sine function approximation.

$$e_0 = \frac{1.5715 e_1 - 0.004317 e_1^3}{1 + 0.001398 e_1^2} = 10 \sin 9 e_1$$

The theory and procedures for developing virtually any function generator or linearization circuit can be found in the new Burr-Brown/McGraw Hill book "FUNCTION CIRCUIT - Design and Applications."

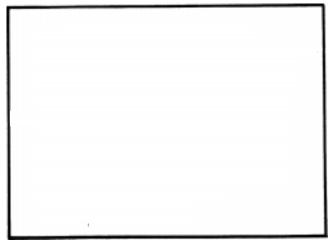


FIGURE 13. Sine Function Connections—4214.

The information provided herein is believed to be reliable; however, BURR-BROWN assumes no responsibility for inaccuracies or omissions. BURR-BROWN assumes no responsibility for the use of this information, and all use of such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. BURR-BROWN does not authorize or warrant any BURR-BROWN product for use in life support devices and/or systems.





3-Oct-2003

## **PACKAGING INFORMATION**

ORDERABLE DEVICE	STATUS(1)	PACKAGE TYPE	PACKAGE DRAWING	PINS	PACKAGE QTY
4214AP	NRND	CDIP SB	JD	14	27
4214BP	NRND	CDIP SB	JD	14	27

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

#### **IMPORTANT NOTICE**

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Amplifiers	amplifier.ti.com	Audio	www.ti.com/audio
Data Converters	dataconverter.ti.com	Automotive	www.ti.com/automotive
DSP	dsp.ti.com	Broadband	www.ti.com/broadband
Interface	interface.ti.com	Digital Control	www.ti.com/digitalcontrol
Logic	logic.ti.com	Military	www.ti.com/military
Power Mgmt	power.ti.com	Optical Networking	www.ti.com/opticalnetwork
Microcontrollers	microcontroller.ti.com	Security	www.ti.com/security
		Telephony	www.ti.com/telephony
		Video & Imaging	www.ti.com/video
		Wireless	www.ti.com/wireless

Mailing Address: Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

Copyright © 2003, Texas Instruments Incorporated