

Vishay High Power Products

"Half-Bridge" IGBT INT-A-PAK (Ultrafast Speed IGBT), 200 A



INT-A-PAK

PRODUCT SUMMARY				
V _{CES}	600 V			
I _C DC	265 A			
V _{CE(on)} at 200 A, 25 °C	1.74 V			

FEATURES

- Generation 4 IGBT technology
- Ultrafast: Optimized for high speed 8 kHz to 40 kHz in hard switching, > 200 kHz in resonant mode



- Very low conduction and switching losses
- HEXFRED® antiparallel diodes with ultrasoft recovery
- Industry standard package
- UL approved file E78996
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

BENEFITS

- Increased operating efficiency
- Direct mounting to heatsink
- Performance optimized for power conversion: UPS, SMPS, welding
- Low EMI, requires less snubbing

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		600	V
Cartino and all action and action	,	T _C = 25 °C	265	
Continuous collector current	IC	T _C = 67 °C	200	
Pulsed collector current	I _{CM}		400	A
Peak switching current	I _{LM}		400	
Peak diode forward current	I _{FM}		400	
Gate to emitter voltage	V_{GE}		± 20	V
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	☐
Maximum power dissipation	Б	T _C = 25 °C	625	W
	P _D	T _C = 85 °C	325	- vv

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 \text{ V}, I_{C} = 1 \text{ mA}$	600	-	-		
Callantanta amittana altana	V _{CE(on)}	V _{GE} = 15 V, I _C = 200 A	-	1.74	2.2	V	
Collector to emitter voltage		V _{GE} = 15 V, I _C = 200 A, T _J = 125 °C	-	1.79	2.25		
Gate threshold voltage	V _{GE(th)}	I _C = 0.25 mA	3	4.4	6	1	
Temperature coeff. of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	$V_{CE} = V_{GE}, I_{C} = 0.25 \text{ mA}$	-	- 11	-	mV/°C	
Forward transconductance	9 _{fe}	$V_{CE} = 20 \text{ V}, I_{C} = 200 \text{ A}$	-	220	-	S	
Callanta to anaitte la la la cara a comunit	I _{CES}	V _{GE} = 0 V, V _{CE} = 600 V	-	0.014	1	- mA	
Collector to emitter leakage current		V _{GE} = 0 V, V _{CE} = 600 V, T _J = 125 °C	-	-	10		
Diada famuard valtaga dran	V _{FM}	I _C = 200 A, V _{GE} = 0 V	-	4.2	6.0	V	
Diode forward voltage drop		I _C = 200 A, V _{GE} = 0 V, T _J = 125 °C	-	4.4	6.2] ' '	
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 250	nA	

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GA200TS60UPbF

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge	Qg	I _C = 200 A	-	900	-	nC
Gate to emitter charge	Q _{ge}	I _C = 270 A	-	125	-	
Gate to collector charge	Q_{gc}	V _{GE} = 15 V	-	306	-	
Turn-on delay time	t _{d(on)}		-	220	-	
Rise time	t _r	I _C = 200 A	-	154	-	
Turn-off delay time	t _{d(off)}	V _{CC} = 360 V	-	300	-	ns
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}$	-	180	-	-
Turn-on switching energy	E _{on}	$T_J = 25 ^{\circ}\text{C}$ $R_{\alpha 1} = 15 \Omega$	-	2.2	-	mJ
Turn-off switching energy	E _{off}	$R_{g2} = 0 \Omega$	-	6.6	-	
Total switching energy	E _{ts}		-	8.8	-	
Turn-on delay time	t _{d(on)}	$I_{C} = 200 \text{ A}$ $V_{CC} = 360 \text{ V}$ $V_{GE} = \pm 15 \text{ V}$ $T_{J} = 125 \text{ °C}$ $R_{g1} = 15 \Omega$ $R_{g2} = 0 \Omega$	-	342	-	- ns
Rise time	t _r		-	194	-	
Turn-off delay time	t _{d(off)}		-	366	-	
Fall time	t _f		-	213	-	
Turn-on switching energy	E _{on}		-	5	-	mJ
Turn-off switching energy	E _{off}		-	16	-	
Total switching energy	E _{ts}		-	21	-	
Input capacitance	C _{ies}	V _{GE} = 0 V V _{CC} = 30 V	-	20 068	-	
Output capacitance	C _{oes}		-	1254	-	pF
Reverse transfer capacitance	C _{res}	f = 1.0 MHz	-	261	-	1
Diode reverse recovery time	t _{rr}	I _C = 200 A	-	179	-	ns
Diode peak reverse current	I _{rr}		-	120	-	Α
Diode recovery charge	Q _{rr}	V _{CC} = 360 V dl/dt = 1300 A/us	-	10 714	-	μC
Diode peak rate of fall of recovery during t _b	dI _{(rec)M} /dt	a,, at = 1000 / v μο	-	1922	-	A/µs

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range		T_J	- 40	-	150	°C
Storage temperature range		T _{Stg}	- 40	-	125	
Junction to case	IGBT	D	-	-	0.2	
	Diode	R _{thJC}	-	-	0.4	°C/W
Case to sink per module		R _{thCS}	-	0.1	-	
Mounting torque	case to heatsink		-	-	4	Nm
	case to terminal 1, 2, 3		-	-	3	INIII
Weight			-	200	-	g



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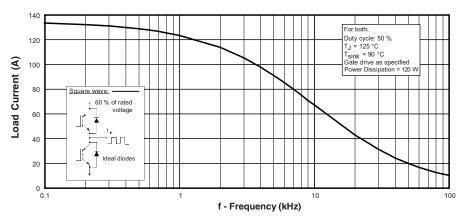


Fig. 1 - Typical Load Current vs. Frequency (Load Current = I_{RMS} of Fundamental)

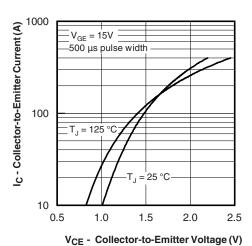


Fig. 2 - Typical Output Characteristics

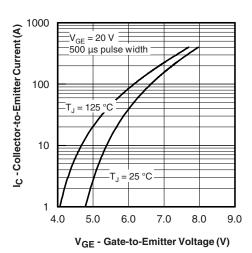
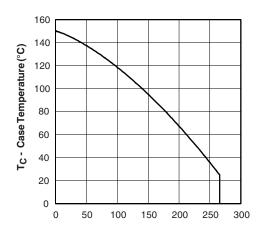


Fig. 3 - Typical Transfer Characteristics



Maximum DC Collector Current (A)

Fig. 4 - Case Temperature vs. Maximum Collector Current

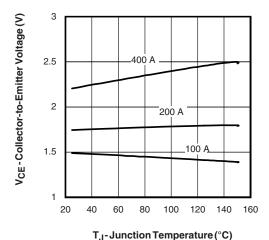


Fig. 5 - Typical Collector to Emitter Voltage vs. Junction Temperature

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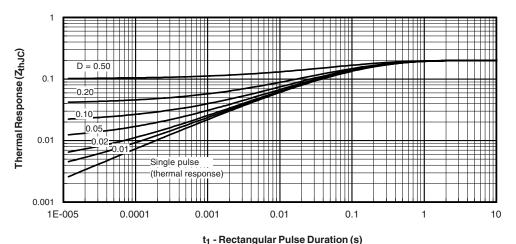
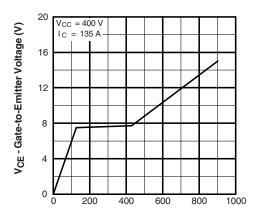


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction to Case



Q_G -Total gate Charge (nC)

Fig. 7 - Typical Gate Charge vs. Gate to Emitter Voltage

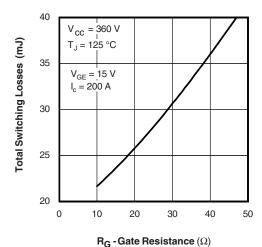
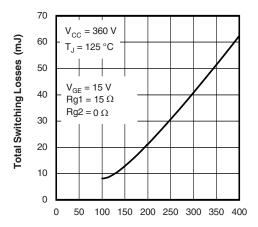
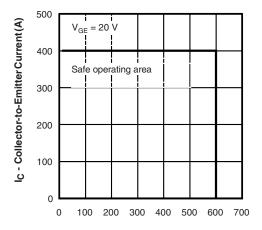


Fig. 8 - Typical Switching Losses vs. Gate Resistance



I_C - Collector-to-Emitter Current (A)

Fig. 9 - Typical Switching Losses vs. Collector to Emitter Current

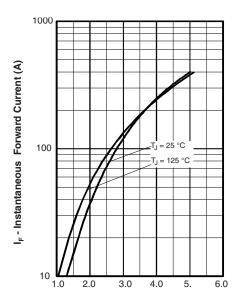


V_{CE} - Collector-to-Emitter Voltage (V)

Fig. 10 - Reverse Bias SOA



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V_{FM} - Forward Voltage Drop (V)
Fig. 11 - Typical Forward Voltage Drop vs.
Instantaneous Forward Current

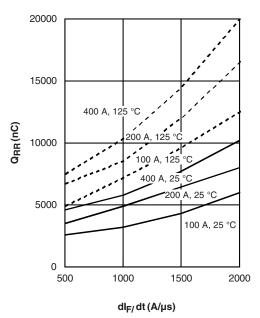


Fig. 12 - Typical Stored Charge vs. dI_F/dt

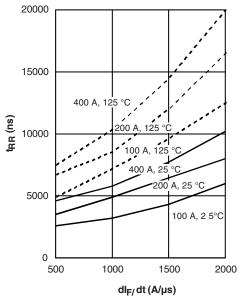


Fig. 13 - Typical Reverse Recovery Time vs. dI_F/dt

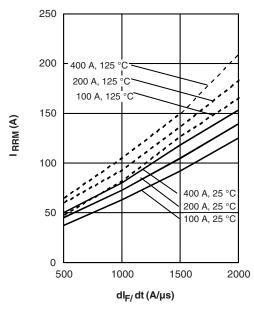
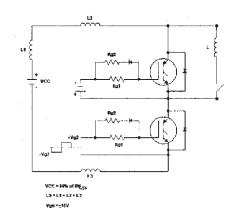
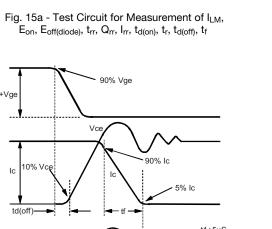


Fig. 14 - Typical Reverse Recovery vs. dI_F/dt

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Vce ic dt

Vce Ic dt

Fig. 15b - Test Waveforms for Circuit of Fig. 18a, Defining $E_{\text{off}},\,t_{\text{d(off)}},\,t_{\text{f}}$

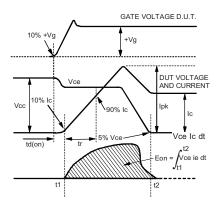


Fig. 15c - Test Waveforms for Circuit of Fig. 18a, Defining $E_{on},\,t_{d(on)},\,t_{r}$

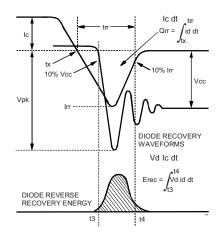


Fig. 15d - Test Waveforms for Circuit of Fig. 18a, Defining E_{rec} , t_{rr} , Q_{rr} , I_{rr}

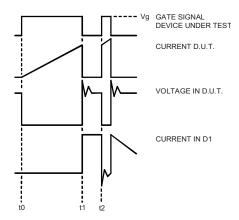


Fig. 15e - Macro Waveforms for Figure 18a's Test Circuit



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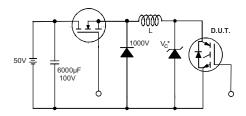


Fig. 16 - Clamped Inductive Load Test Circuit

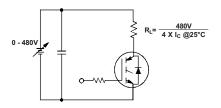
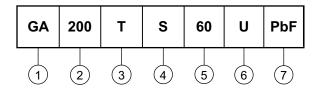


Fig. 17 - Pulsed Collector Current Test Circuit

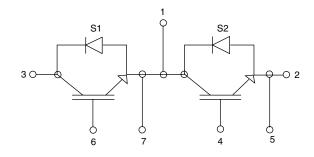
ORDERING INFORMATION TABLE

Device code



- Essential part number IGBT modules
- Current rating (200 = 200 A)
- Circuit configuration (T = Half bridge)
- 4 INT-A-PAK
- 5 Voltage code (60 = 600 V)
- 6 Speed/type (U = Ultrafast IGBT)
- 7 PbF = Lead (Pb)-free

CIRCUIT CONFIGURATION



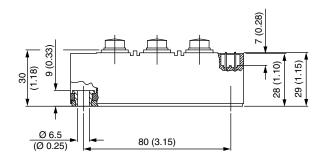
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95173			

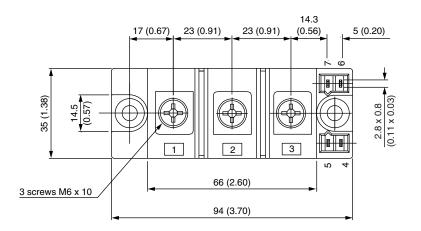


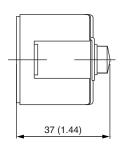
Vishay Semiconductors

INT-A-PAK IGBT

DIMENSIONS in millimeters (inches)









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