

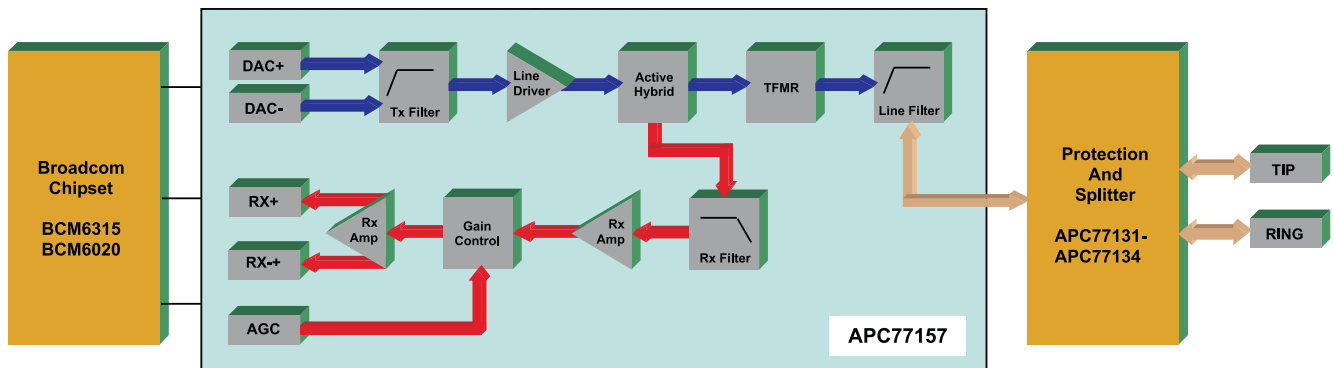
VDSL Hybrid Module

APC77157

FEATURES

- Designed for Broadcom BCM6315 and BCM6020 VDSL chipsets
- Plan998 based symmetric CPE design
- Includes band pass line filter, 2 wire line interface hybrid, TX and RX filters and AGC control
- 1500V line isolation transformer
- Minimum surface mount footprint for increased line card density

BLOCK DIAGRAM

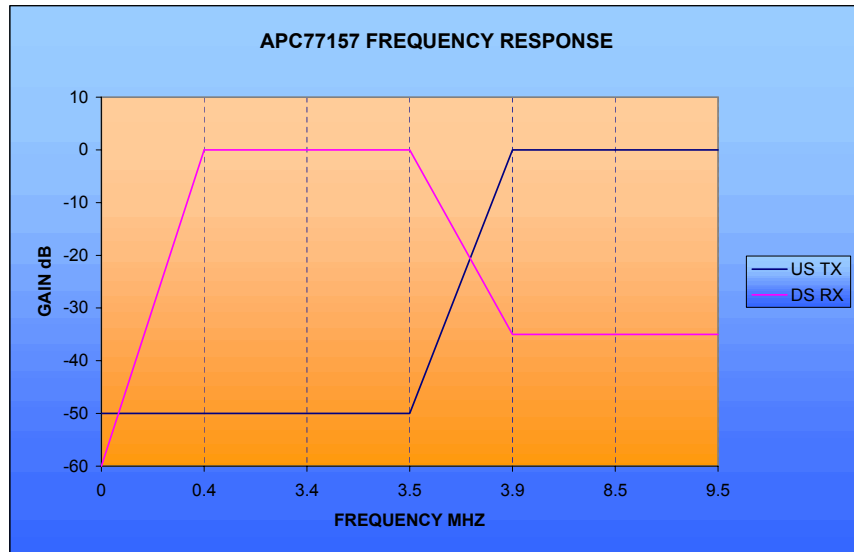


ELECTRICAL SPECIFICATIONS@25°C

OPERATING TEMPERATURE -40°C TO +85°C

Description	Passband Freq	Stopband Freq	Attenuation min
VDSL LPF (TX)	400kHz to 3.5MHz	>3.9MHz	35dB
VDSL HPF (TX)	3.9MHz >	DC to 3.5MHz	35dB

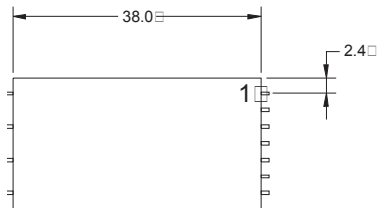
FREQUENCY ALLOCATION



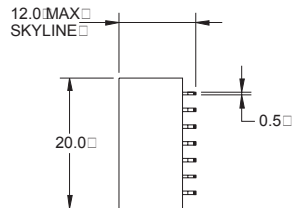
MECHANICAL

APC77157

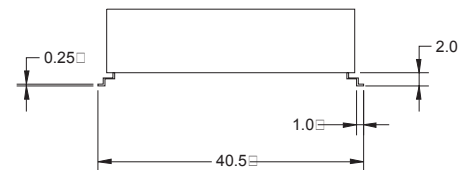
Top View



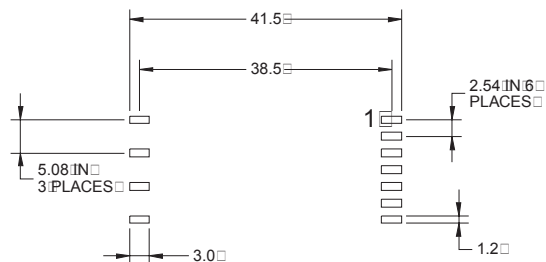
End View



Side View



Recommended PCB Layout



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