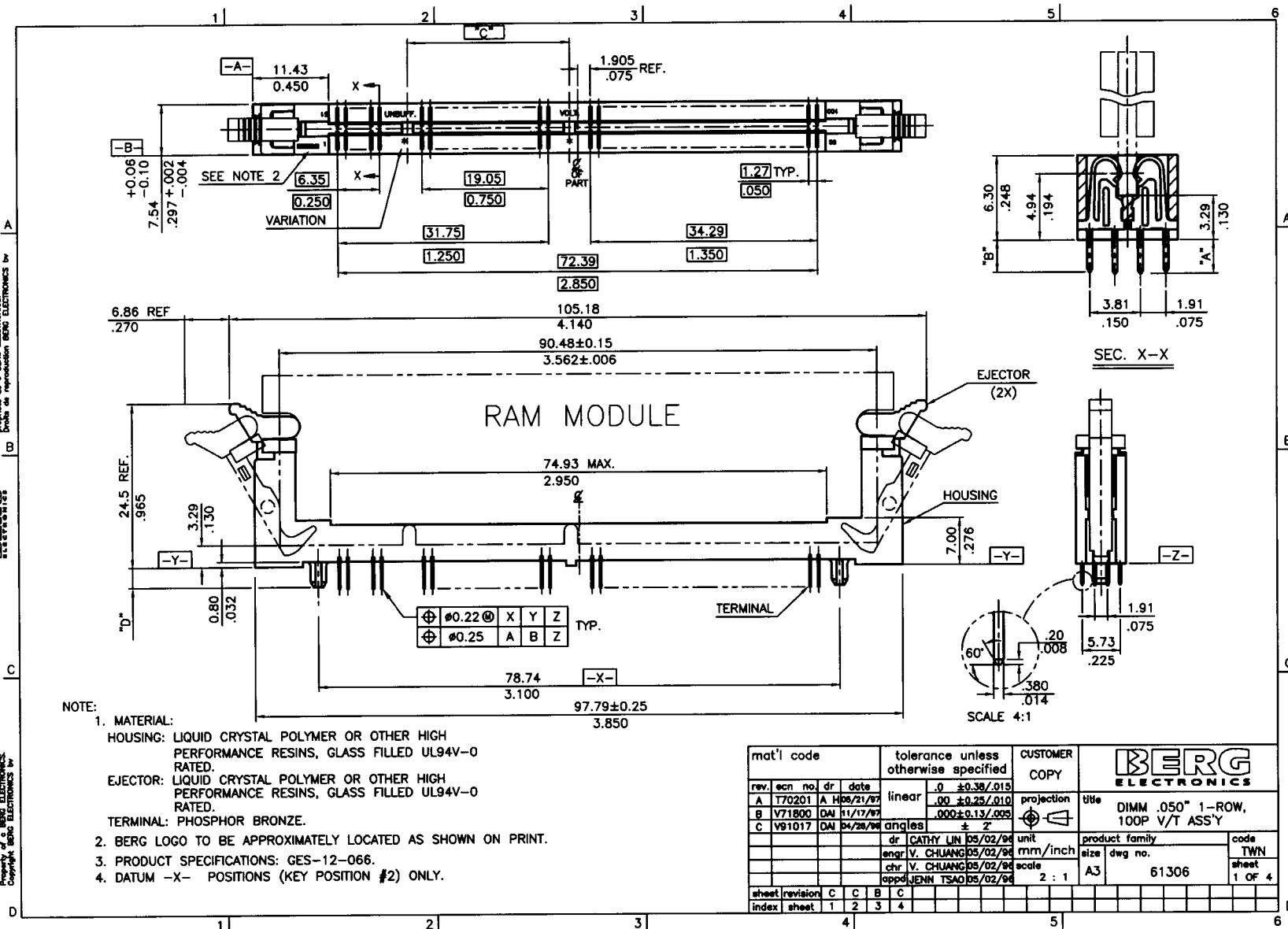
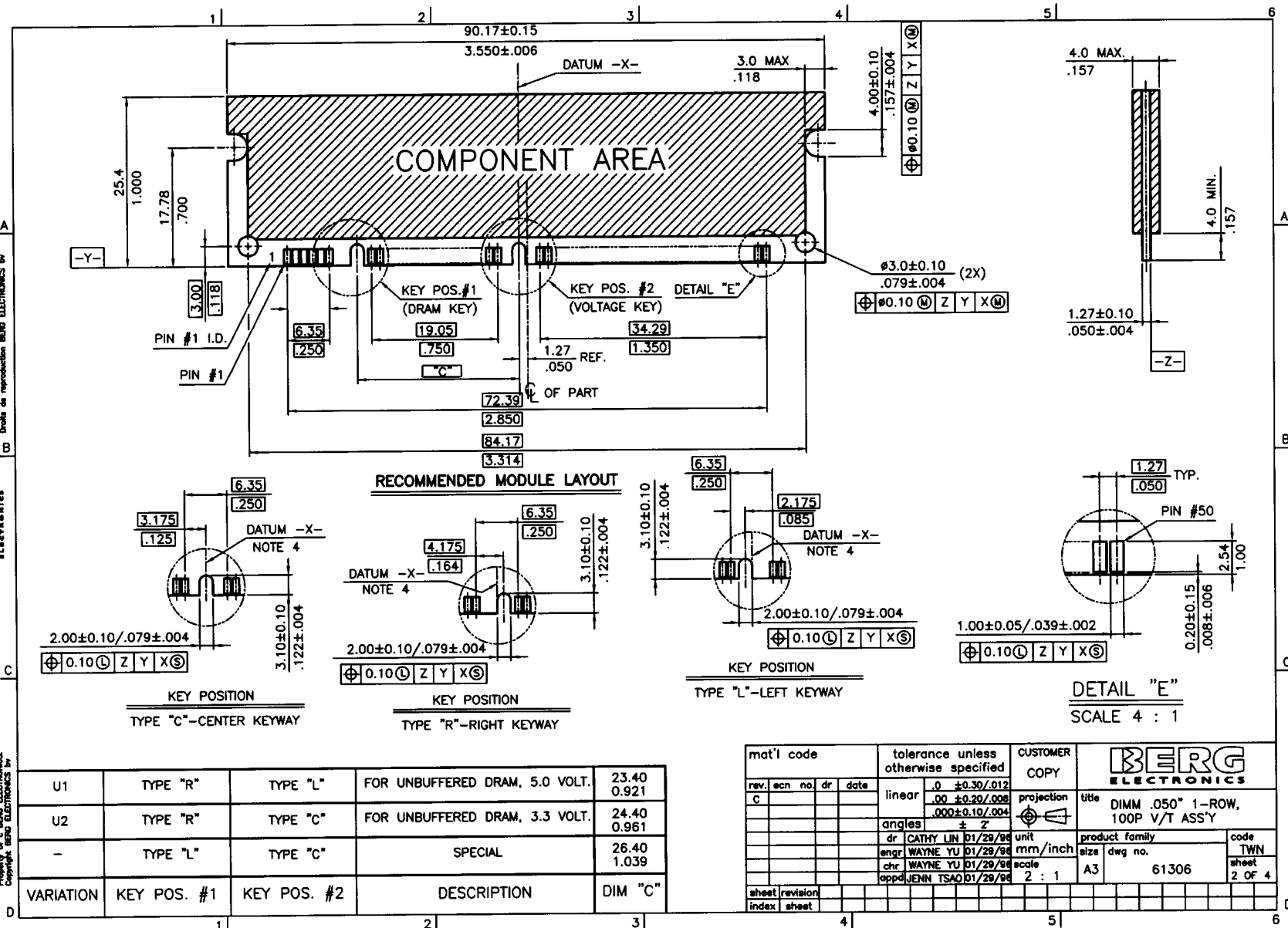


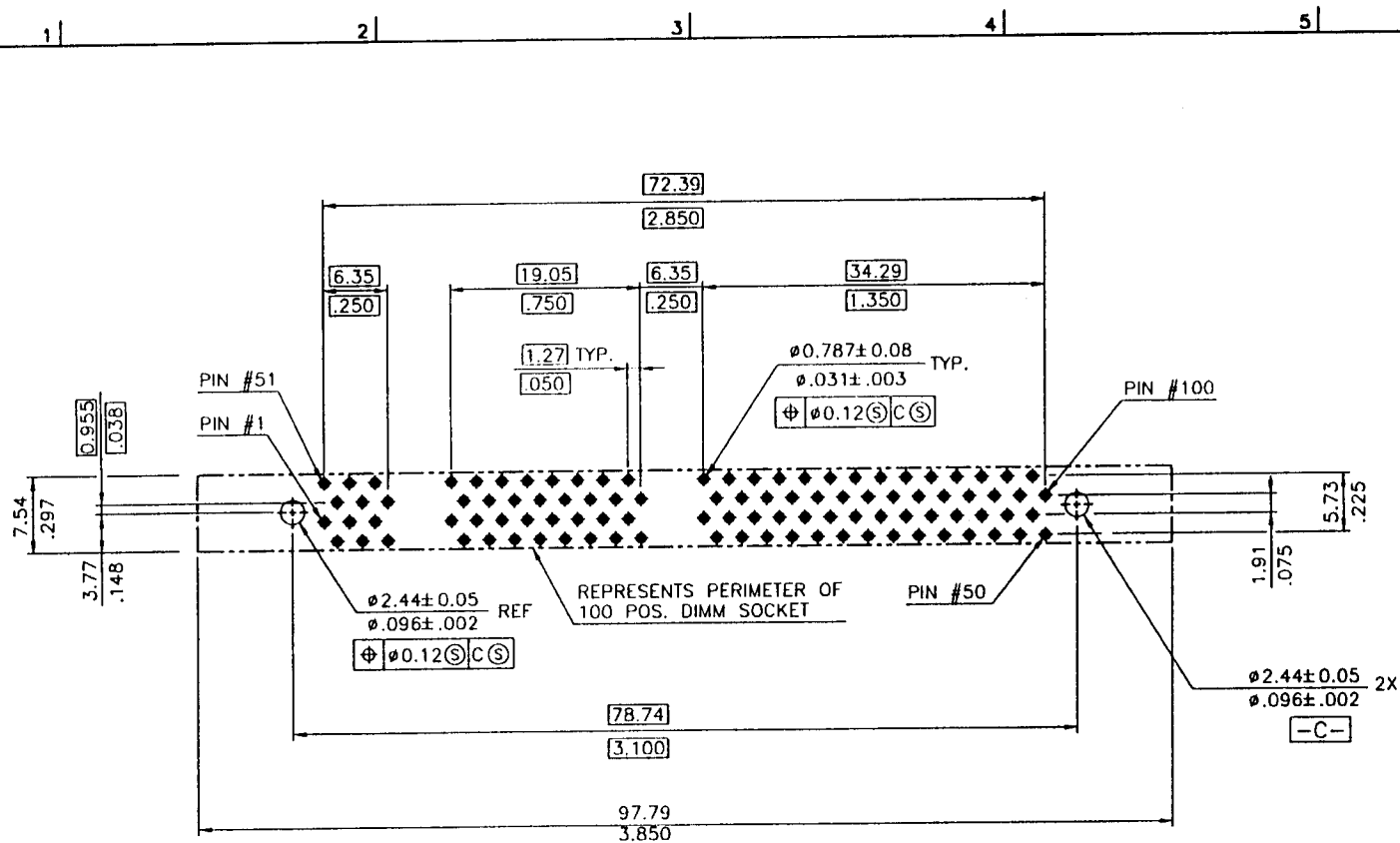


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RECOMMENDED CIRCUIT BOARD HOLE LAYOUT

mat'l code				tolerance unless otherwise specified		CUSTOMER COPY		BERG ELECTRONICS	
rev.	ecn	no.	dr	date	linear	projection	title	product family	code
B					0 ± 0.30 / 0.012 00 ± 0.20 / 0.008 000 ± 0.10 / 0.004	projection	DIMM .050" 1-ROW, 100P V/T ASS'Y		
					angles ± 2'	unit			
					dr CATHY LIN 01/29/96	mm/inch			
					enqr WAYNE YU 01/29/96	scale			
					chr WAYNE YU 01/29/96	2 : 1			
					appd JENN TSAO 01/29/96				
sheet	revision								
index	sheet								

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1 2 3 4 5 6

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STYLE

- 1 : FOR UNBUFFERED DRAM, 3.3 VOLT.
- 2 : FOR UNBUFFERED DRAM, 5.0 VOLT.
- 3 : SPECIAL

PEG TYPE

- 0 : WITH RETENTION PEG DIM "D" = 3.04/.120
- 1 : WITH RETENTION PEG DIM "D" = 3.80/.150

TAIL LENGTH

CODE	DIM. "A"	DIM. "B"
0	2.54 .100	2.39 .094
1	3.30 .130	3.15 .124

PLATING

CODE	CONTACT	SOLDERTAIL	UNDERPLATE
1	150μ" (3.81μm) MIN. TIN/LEAD		
2	15μ" (0.38μm) MIN. GOLD		
3	30μ" (0.76μm) MIN. GOLD		
4	3μ" (0.076μm) MIN. GOLD	100μ" (2.54μm) MIN. TIN/LEAD	50μ" (1.27μm) MIN. NICKEL OVERALL
5	15μ" (0.38μm) MIN. GXT		
6	30μ" (0.76μm) MIN. GXT		
7	20μ" (0.51μm) MIN. GXT		

COLOR OF HOUSING AND EJECTOR

- 0 : BLACK HOUSING + IVORY EJECTOR
- 1 : IVORY HOUSING + BLACK EJECTOR

A

B

C

D

mat'l code				tolerance unless otherwise specified		CUSTOMER COPY		BERG ELECTRONICS	
rev.	ecn	no.	dr	date	linear	projection	title	product family	code
C					.00 ±.005/.015	1	DIMM .050" 1-ROW, 100P V/T ASS'Y		TWN
					.000 ±.013/.005				sheet
					± 2'				4 OF 4
					dr CATHY UN 01/29/98	unit			
					enr WAYNE YU 01/29/98	mm/inch			
					chr WAYNE YU 01/29/98	scale			
					appd JENN TSAO 01/29/98	1 : 1			
sheet	revision								
index	sheet								

1 2 3 4 5 6