Status: **Active**

Product Quick Links

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9P956
Low Skew DDR II Fan-out Buffer

DDR II fanout buffer for VIA Chipset

Features

- Low skew, fanout buffer
- I2C for functional and output control
- Single bank 1-6 differential clock distribution
- 1 pair of differential feedback pins for input to output
- synchronizationSupports up to 2 DDR DIMMs
- 400MHz (DDRII 800) output frequency support
- Programmable skew through SMBus
- Frequency defect control thorugh SMBus
- Individual output control programmable through SMBus

Product Specification

OUTPUTS (#)	OUTPUT TYPE	OUTPUT FREQ RANGE (MHZ)	INPUTS (#)	INPUT SIGNAL	OUTPUT VOLTAGE (V)	OUTPUT SKEW (PS)
6	SSTL-2	125.000000 - 400.000000			1.8	40

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
9P956AFLF	Active	PYG28	SSOP	28	C	Y	Check Inventory
9P956AFLFT	Active	PYG28	SSOP	28	C	Y	Check Inventory

Documents

	TITLE	TYPE	FORMAT	FILE SIZE	DATE
	PC Clocks Contact Info	Misc	PDF	62 KB	05/29/2007

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