

# ERI C€

#### Feature

- Width only 17.5mm (1SU)
- 4:1 ultra wide input range
- -40~+85°C wide working temperature
- No minimum load required
- DC output adjustable ( $\pm 10\%$ )
- · Cooling by free air convection
- · Can be installed on DIN rail TS-35/7.5 or 15
- Protections: Short circuit / Overload / Over voltage / Input reverse polarity / Input under voltage protection
- 4KVdc I/O isolation(Reinforced isolation)
- 3 years warranty











#### Applications

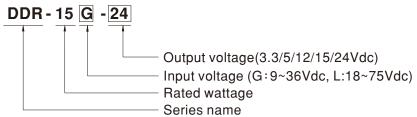
- · Industrial control system
- · Semi-conductor fabrication equipment
- Factory automation
- · Electro-mechanical
- · Wireless network
- · Telecom or datacom system

### Description

DDR-15 series is a 15W DIN Rail type DC-DC converter with main features including DIN rail-type easy installation, ultra slim width (17.5mm), 4: 1 ultra wide input voltage,  $-40 \sim +85^{\circ}$ C wide operating temperature, 4KVdc I/O isolation, adjustable output voltage ( $\pm$  10%) and full protective functions...etc.

This series has two input options:  $9\sim36\text{V}/18\sim75\text{V}$  and various output options: 3.3V/5V/12V/15V/24V and can be used for industrial control, security control, communication system and other fields. Suitable applications are DC buck/boost regulator, increasing system insulation level and voltage drop compensation along cable...etc.







#### **SPECIFICATION**

L		DDR-15G-3.3	DDR-15G-5	DDR-15G-12	DDR-15G-15	DDR-15G-24		
DC '	VOLTAGE	3.3V	5V	12V	15V	24V		
RAT	TED CURRENT	3.5A	3A	1.25A	1A	0.63A		
CUF	JRRENT RANGE	0 ~ 3.5A	0 ~ 3A	0 ~ 1.25A	0 ~ 1A	0 ~ 0.63A		
RAT	ATED POWER	11.6W	15W	15W	15W	15W		
RIP	PPLE & NOISE (max.) Note.2	50mVp-p	50mVp-p	60mVp-p	75mVp-p	100mVp-p		
	DLTAGE ADJ. RANGE	3.0 ~ 3.6V	4.5 ~ 5.5V	9 ~ 13.2V	13.5 ~ 16.5V	21.6 ~ 28V		
	LTAGE TOLERANCE Note.3		±2.0%	±2.0%	±2.0%	±2.0%		
LIN	NE REGULATION	±0.5%	±0.5%	±0.5%	±0.5%	±0.5%		
	AD REGULATION	±1.5%	±1%	±0.5%	±0.5%	±0.5%		
	TUP, RISE TIME	120ms, 85ms at full load						
	OLD UP TIME (Typ.)	G-type: 8ms@24Vdc input						
EXT	TERNAL CAPACITANCE DAD (Max.)	3300 μ F 3300 μ F 1200 μ F 1200 μ F 680 μ F						
	DLTAGE RANGE Note.4	9 ~ 36Vdc	-	<u>'</u>	<u> </u>	<u>'</u>		
EFF	FICIENCY (Typ.)	84%	84%	85%	85%	86%		
	CURRENT (Typ.)	0.8A/24Vdc	0.70		1	1 22.0		
	RUSH CURRENT (Typ.)	15A /24Vdc						
III	ROOM CONNENT (19p.)	110 ~ 150% rated output power						
OVE	/ERLOAD			ers automatically after fault	condition is removed			
		3.8 ~ 4.7V	5.75 ~ 7V	13.8 ~ 16.2V	17.25 ~ 20.25V	20.0 22.41/		
	/ER VOLTAGE				11.20 ~ 20.25V	28.8 ~ 32.4V		
TION	WEDGE DOLADITY	Protection type: Shut down o/p voltage, re-power on to recover						
	EVERSE POLARITY	By internal MOSFET, no damage, recovers automatically after fault condition removed						
	IDER VOLTAGE LOCKOUT	Power ON≥9V, OFF≤8.5V						
	ORKING TEMP.	-40 ~ +85°C (Refer to "Derating Curve")						
_	ORKING HUMIDITY	5 ~ 95% RH non-condensing						
	ORAGE TEMP., HUMIDITY	$-40 \sim +85^{\circ}\text{C}$ , $5 \sim 95\%$ RH non-condensing						
TEN	MP. COEFFICIENT	±0.03%/°C (0~60°C)						
	BRATION	Component: 10 ~ 500Hz, 2G 10min./1cycle, 60min. each along X, Y, Z axes; Mounting: Compliance to IEC60068-2-6						
	PERATING ALTITUDE	2000 meters						
	FETY STANDARDS	IEC 62368-1 (LVD) approved, Design refer to UL508						
WIT	THSTAND VOLTAGE	I/P-O/P:4KVdc						
ISO	OLATION RESISTANCE	I/P-O/P>100M Ohms / 500Vdc / 25°C / 70% RH						
	EMC EMISSION	Parameter		Standard	Test Level / Note			
		Conducted		EN55032	Class B			
EMC		Radiated	EN55032 Class B					
Y &		Voltage Flicker		EN61000-3-3				
۱۵	EMC IMMUNITY	EN55024, EN61000-6	6-2(EN50082-2)					
		Parameter		Standard	Test Level / Note			
		ESD		EN61000-4-2	Level 3, 8KV air ; Level 3, 6KV contact; criteria			
		Radiated		EN61000-4-3	Level 3, 10V/m ; criteria A			
		EFT / Burst		EN61000-4-4	Level 3, 2KV ; criteria A			
EMC		Surge		EN61000-4-5	Level 3, 1KV/Line-Line ; criteria A			
		Conducted		EN61000-4-6	Level 3, 10V; criteria A			
		Magnetic Field		EN61000-4-8	Level 4, 30A/m ; criteria A			
МТЕ	TBF	3	HDBK-217F (25°C)		, , , , , , , , , , , , , , , , , , , ,			
	MENSION							
1. / 2. ! 3 4. ! 5	MENSION  .CKING  . All parameters NOT spectors includes a noise are measured. Tolerance: includes set uportained includes set uportained includes includes set uportained includes includes set uportained includes include	Magnetic Field  EN61000-4-8  Level 4, 30A/m; criteria A  907K hrs min. MIL-HDBK-217F (25°C)  17.5*90*54.5mm (W*H*D)  68g; 160pcs/12Kg/1.19CUFT  cially mentioned are measured at 24VDC input, rated load and 25°C of ambient temperature.  ured at 20MHz of bandwidth by using a 12" twisted pair-wire terminated with a 0.1 μ f & 47 μ f parallel capaci  up tolerance, line regulation and load regulation.  I under low input voltage. Please check the derating curve for more details.  sidered as an independent unit, but the final equipment still need to re-confirm that the whole system complie  guidance on how to perform these EMC tests, please refer to "EMI testing of component power supplies."						



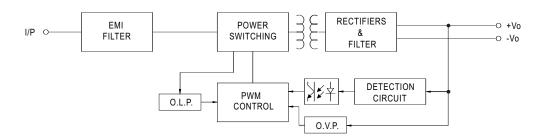
#### **SPECIFICATION**

/OLTAGE ED CURRENT PRENT RANGE ED POWER PLE & NOISE (max.) Note.2 TAGE ADJ. RANGE TAGE TOLERANCE Note.3 E REGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	3.0 ~ 3.6V	5V 3A 0~3A 15W 50mVp-p 4.5~5.5V ±2.0% ±0.5%	12V 1.25A 0 ~ 1.25A 15W 60mVp-p 9 ~ 13.2V ±2.0%	15V 1A 0 ~ 1A 15W 75mVp-p 13.5 ~ 16.5V	24V 0.63A 0 ~ 0.63A 15W 100mVp-p 21.6 ~ 28V		
RENT RANGE ED POWER PLE & NOISE (max.) Note.2 TAGE ADJ. RANGE TAGE TOLERANCE Note.3 E REGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	0~4.5A 15W 50mVp-p 3.0~3.6V ±2.0% ±0.5%	0 ~ 3A 15W 50mVp-p 4.5 ~ 5.5V ±2.0%	0 ~ 1.25A 15W 60mVp-p 9 ~ 13.2V	0 ~ 1A 15W 75mVp-p 13.5 ~ 16.5V	0 ~ 0.63A 15W 100mVp-p		
ED POWER PLE & NOISE (max.) Note.2 TAGE ADJ. RANGE TAGE TOLERANCE Note.3 E REGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	15W 50mVp-p 3.0 ~ 3.6V ±2.0% ±0.5%	15W 50mVp-p 4.5 ~ 5.5V ±2.0%	15W 60mVp-p 9 ~ 13.2V	15W 75mVp-p 13.5 ~ 16.5V	15W 100mVp-p		
PLE & NOISE (max.) Note.2 TAGE ADJ. RANGE TAGE TOLERANCE Note.3 EREGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	50mVp-p 3.0 ~ 3.6V ±2.0% ±0.5%	50mVp-p 4.5 ~ 5.5V ±2.0%	60mVp-p 9 ~ 13.2V	75mVp-p 13.5 ~ 16.5V	100mVp-p		
PLE & NOISE (max.) Note.2 TAGE ADJ. RANGE TAGE TOLERANCE Note.3 EREGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	50mVp-p 3.0 ~ 3.6V ±2.0% ±0.5%	50mVp-p 4.5 ~ 5.5V ±2.0%	60mVp-p 9 ~ 13.2V	75mVp-p 13.5 ~ 16.5V	100mVp-p		
TAGE ADJ. RANGE TAGE TOLERANCE Note.3 EREGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	3.0 ~ 3.6V ±2.0% ±0.5%	4.5 ~ 5.5V ±2.0%	9 ~ 13.2V	13.5 ~ 16.5V			
TAGE TOLERANCE Note.3 EREGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	±2.0% ±0.5%	±2.0%			21.0 LOV		
EREGULATION D REGULATION UP, RISE TIME D UP TIME (Typ.)	±0.5%		- 2.0 /0	±2.0%	±2.0%		
D REGULATION UP, RISE TIME D UP TIME (Typ.)			±0.5%	±0.5%	±0.5%		
UP, RISE TIME D UP TIME (Typ.)	⊥ 1.3 /0	±1%	±0.5%	±0.5%	±0.5%		
D UP TIME (Typ.)	120ms, 85ms at full load						
( )1 /	L-type: 16ms@48Vdc input						
ERNAL CAPACITANCE D (Max.)	3300 μ F	3300 μ F	1200 μ F	1200 μ F	680 μ F		
TAGE RANGE Note.4	18 ~ 75Vdc						
ICIENCY (Typ.)	84%	85%	86%	86%	87%		
CURRENT (Typ.)	0.4A /48Vdc						
JSH CURRENT (Typ.)	15A /48Vdc						
DI GAD	110 ~ 150% rated output power						
OVERLOAD			ers automatically after fault	condition is removed			
OVER VOLTAGE	3.8 ~ 4.7V	5.75 ~ 7V			28.8 ~ 32.4V		
		down o/p voltage, re-		11.20 20.201			
ERSE POLARITY							
ER VOLTAGE LOCKOUT							
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	, ,						
ATION RESISTANCE							
EMC EMISSION							
	0		EN61000-3-3				
EMC IMMUNITY		5-2(EN50082-2)					
				100120101111010			
				Level 3, 8KV air ; Level 3, 6KV contact; criteria			
				Level 3, 10V/m ; criteria A			
	EFT / Burst		EN61000-4-4	Level 3, 2KV ; criteria A			
	Surge		EN61000-4-5	Level 3, 1KV/Line-Line ; criteria A			
	Conducted		EN61000-4-6	Level 3, 10V ; criteria A			
	Magnetic Field		EN61000-4-8	Level 4, 30A/m ; criteria A			
F	907K hrs min. MIL-HDBK-217F (25°C)						
ENSION	17.5*90*54.5mm (W*H*D)						
KING	68g; 160pcs/12Kg/1.19	9CUFT					
IN R R E E RICHER R P. R. F. E IN .	COEFFICIENT ATION RATION RESISTANCE  EMISSION  MMUNITY  AUGUST  MING  MI	15A /48Vdc	SH CURRENT (Typ.)   15A/48Vdc   110 ~ 150% rated output power   Protection type : Hiccup mode, recover   3.8 ~ 4.7V   5.75 ~ 7V   Protection type : Shut down o/p voltage, recovers   RSE POLARITY   By internal MOSFET, no damage, recovers   R VOLTAGE LOCKOUT   Power ON ≥ 18V , OFF ≤ 17V   40 ~ +85°C (Refer to "Derating Curve")   5 ~ 95% RH non-condensing   40 ~ +85°C , 5 ~ 95% RH non-condensing   40 ~	15A/48Vdc	15A /48Vdc		

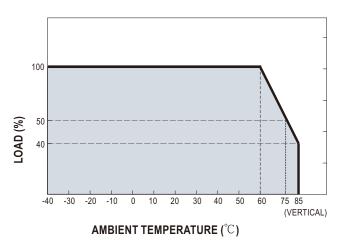


#### ■ Block Diagram

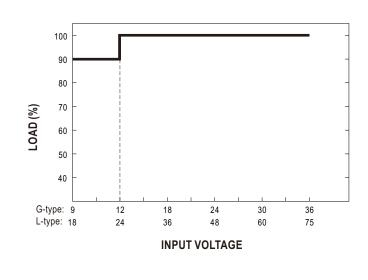
fosc: 100KHz



## ■ Derating Curve



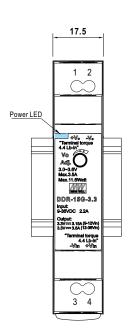
#### ■ Output derating VS input voltage

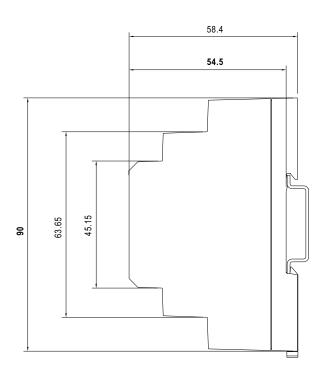


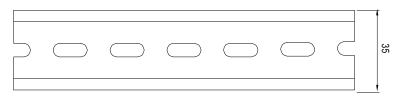


#### ■ Mechanical Specification

(Unit: mm, tolerance ± 0.5mm)







ADMISSIBLE DIN-RAIL:TS35/7.5 OR TS35/15

Terminal Pin No. Assignment

Pin No.	Assignment
1	DC Output +Vo
2	DC Output -Vo
3	DC Input -Vin
4	DC Input +Vin

#### ■ Installation Manual

Please refer to : http://www.meanwell.com/manual.html