

## LOW-VOLTAGE OPERATION DUAL C-MOS OPERATIONAL AMPLIFIER

### ■ GENERAL DESCRIPTION

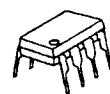
The NJU7018 is a low voltage single-power-supply and low operating current dual C-MOS operational amplifier.

The input bias current is as low as less than 1pA consequently the very small signal around the ground level can be amplified.

The minimum operating voltage is 1V and the output stage permits output signals to swing between both of the supply rails.

Furthermore, the NJU7018 is packaged with a various small one therefore it can be especially applied to portable items.

### ■ PACKAGE OUTLINE



NJU7018D



NJU7018M



NJU7018V



NJU7018R

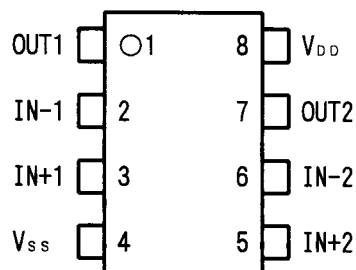


NJU7018RB1

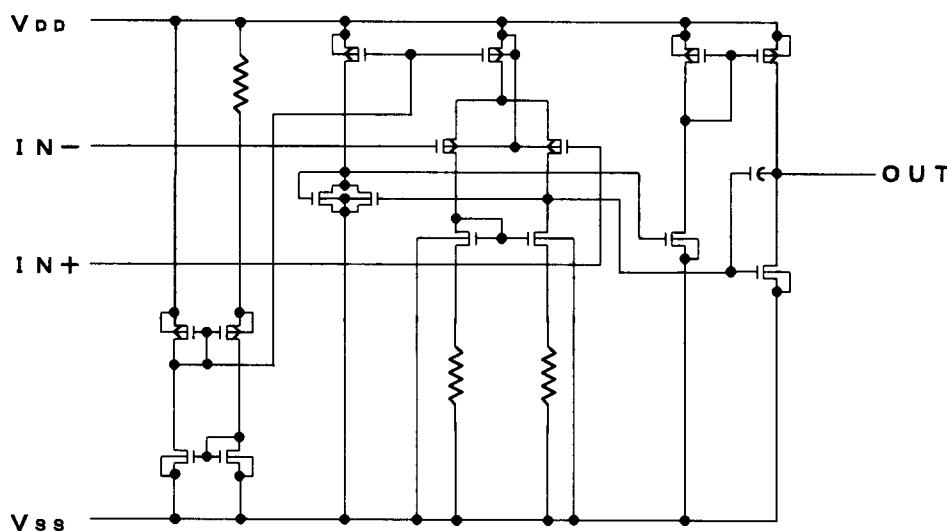
### ■ FEATURES

- Single-Power-Supply
- Wide Operating Voltage ( $V_{DD}=1\sim5.5V$ )
- Wide Output Swing Range ( $V_{OM}=2.9V$  min. @ 3.0V)
- Low Operating Current ( $I_{DD}=0.75mA$  typ./circuit)
- Low Bias Current ( $I_{IB}=1pA$  typ.)
- Internal Compensation Capacitor
- C-MOS Technology
- Package Outline DIP8,DMP8,SSOP8,VSP8,TVSP8

### ■ PIN CONFIGURATION



### ■ EQUIVALENT CIRCUIT



## ■ ABSOLUTE MAXIMUM RATINGS

( Ta=25°C )

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{IN}$	7	V
Differential Input Voltage	$V_{ID}$	$\pm 7$ ( note1 )	V
Common Mode Input Voltage	$V_{IC}$	-0.3~7	V
Power Dissipation	$P_D$	( DIP8 ) 500 ( DMP8 ) 300 ( SSOP8 ) 250 ( TVSP8 ) 320	mW
Operating Temperature Range	$T_{opr}$	-40~+85	°C
Storage Temperature Range	$T_{stg}$	-55~+125	°C

( note1 ) If the supply voltage (  $V_{DD}$  ) is less than 7V, the input voltage must not over the  $V_{DD}$  level though 7V is limit specified.

( note2 ) Decoupling capacitor should be connected between  $V_{DD}$  and  $V_{SS}$  due to the stabilized operation for the circuit.

## ■ ELECTRICAL CHARACTERISTICS

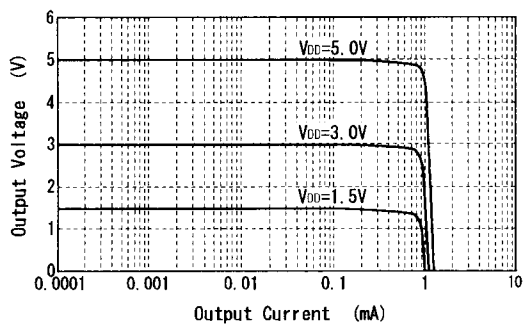
( Ta=25°C,  $V_{DD}=3.0V$ ,  $R_L=\infty$  )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	$V_{IO}$	$V_{IN}=1/2V_{DD}$	-	-	10	mV
Input Offset Current	$I_{IO}$		-	1	-	pA
Input Bias Current	$I_{IB}$		-	1	-	pA
Input Impedance	$R_{IN}$		-	1	-	TΩ
Large Signal Voltage Gain	$A_{VD}$		60	70	-	dB
Input Common Mode Voltage Range	$V_{ICM}$		0~2.5	-	-	V
Maximum Output Swing Voltage	$V_{OM1}$	$R_L=16k\Omega$	$V_{DD}-0.1$	-	-	V
	$V_{OM2}$	$R_L=16k\Omega$	-	-	$V_{SS}+0.1$	V
Common Mode Rejection Ratio	CMR	$V_{IN}=1/2V_{DD}$	55	65	-	dB
Supply Voltage Rejection Ratio	SVR	$V_{DD}=1.5\sim 5.5V$	60	70	-	dB
Operating Current	$I_{DD}$	Per Circuit	-	0.75	1.5	mA
Slew Rate	SR		-	3.7	-	V/μs
Unity Gain Bandwidth	$F_t$	$A_v=40dB, C_L=10pF$	-	1.0	-	MHz

( note3 ) The source current is less than 181μA ( at  $V_{OM}/R_L=2.9V/16k\Omega$  ).

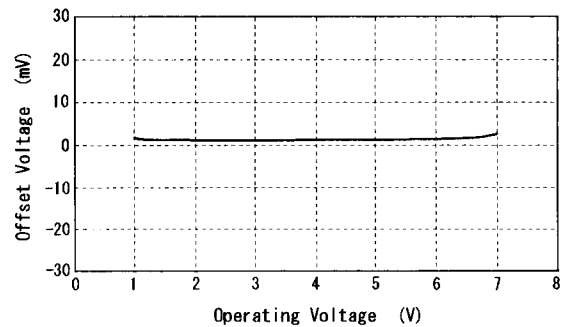
## ■ TYPICAL CHARACTERISTICS

Output Voltage vs. Output Current (SOURCE)



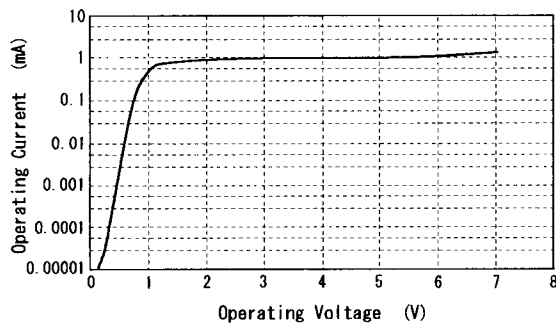
Offset Voltage vs. Operating Voltage

$V_{IN}=0.1V$



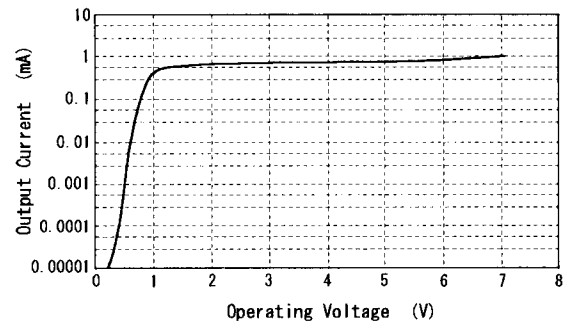
Operating Current vs. Operating Voltage

$V_{IN}=0.1V$

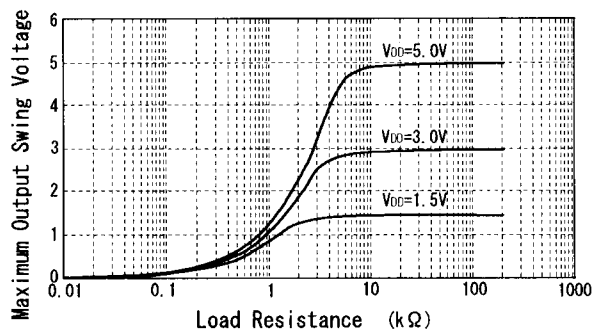


Output Current vs. Operating Voltage

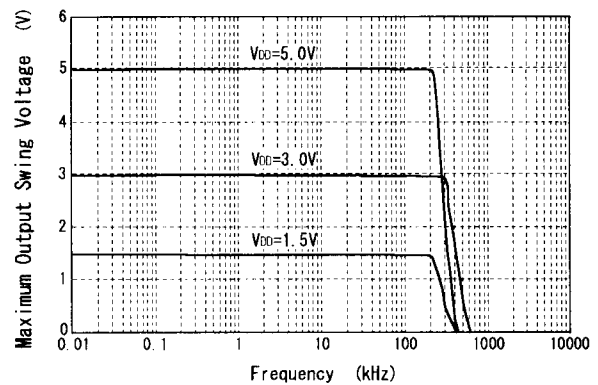
$V_{IN}=0.1V$

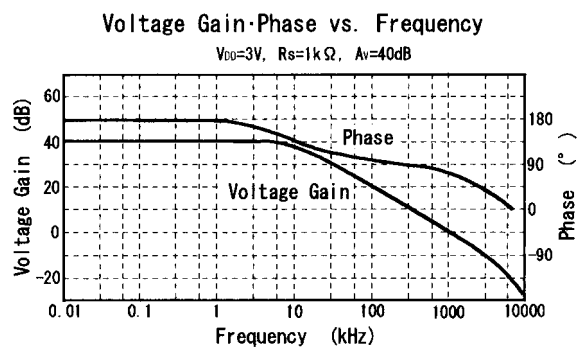


Maximum Output Swing Voltage vs. Load Resistance



Maximum Output Swing Voltage vs. Frequency





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