

**SURFACE MOUNT GLASS PASSIVATED
HIGH EFFICIENCY SILICON RECTIFIER**
VOLTAGE RANGE 50 to 600 Volts CURRENT 1.0 Ampere

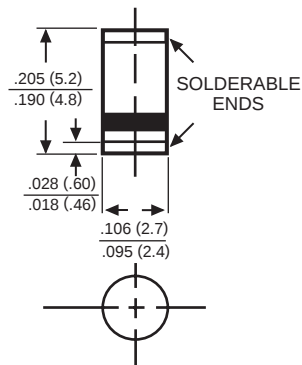
FEATURES

- * Fast switching
- * Glass passivated device
- * Ideal for surface mounted applications
- * Low leakage current
- * Metallurgically bonded construction
- * Mounting position: Any
- * Weight: 0.015 gram

MECHANICAL DATA

- * Epoxy : Device has UL flammability classification 94V-0

MELF



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified.
Single phase, half wave, 60 Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

MAXIMUM RATINGS (At TA = 25°C unless otherwise noted)

RATINGS	SYMBOL	HSM101	HSM102	HSM103	HSM104	HSM105	HSM106	UNITS
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	300	400	600	Volts
Maximum RMS Volts	V _{RMS}	35	70	140	210	280	420	Volts
Maximum DC Blocking Voltage	V _{DC}	50	100	200	300	400	600	Volts
Maximum Average Forward Current at T _A = 50°C	I _O	1.0						Amps
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	30						Amps
Typical Junction Capacitance (Note 2)	C _J	15				12		pF
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to + 175						°C

ELECTRICAL CHARACTERISTICS (At TA = 25°C unless otherwise noted)

CHARACTERISTICS	SYMBOL	HSM101	HSM102	HSM103	HSM104	HSM105	HSM106	UNITS
Maximum Instantaneous Forward Voltage at 1.0A DC	V _F	1.0			1.3		1.70	Volts
Maximum DC Reverse Current at Rated DC Blocking Voltage T _A = 25°C	I _R	5.0						uAmps
Maximum Full Load Reverse Current Average, Full Cycle .375" (9.5mm) lead length at T _L = 55°C		100						uAmps
Maximum Reverse Recovery Time (Note 1)	trr	50					75	nSec

NOTES : 1. Test Conditions: IF=0.5A, IR=-1.0A, IRR=-0.25A.

2. Measured at 1 MHz and applied reverse voltage of 4.0 volts.

RATING AND CHARACTERISTIC CURVES (HSM101 THRU HSM106)

FIG. 1 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

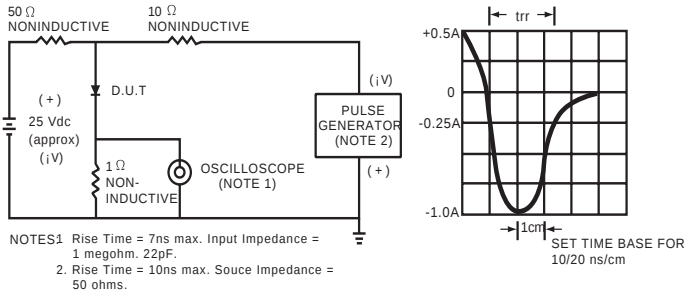


FIG. 2 - TYPICAL FORWARD CURRENT DERATING CURVE

