

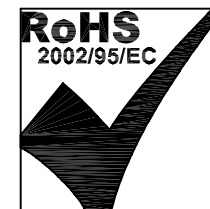
ELECTRICAL SPECIFICATIONS:

1.0 INDUCTANCE: P1–J1=P2–J2 ; P4–J4=P5–J5 : 65 uH 0.01V, 10 KHz, nominal.

2.0 DC RESISTANCE: P1–J1 : .5 ohm MAX
P2–J2 : .5 ohm MAX
P4–J4 : .5 ohm MAX
P5–J5 : .5 ohm MAX

3.0 COMMON MODE:

<u>FREQUENCY (MHz)</u>	<u>CMA (–dB)</u>	
	<u>TYPICAL</u>	<u>MIN</u>
1	8	5
5	17	15
20	23	15
70	25	20
200	22	17
500	11	6



Bel Stewart Connector
11118 Susquehanna Trail, South
Glen Rock, Pa 17327-9199
717.234.7512

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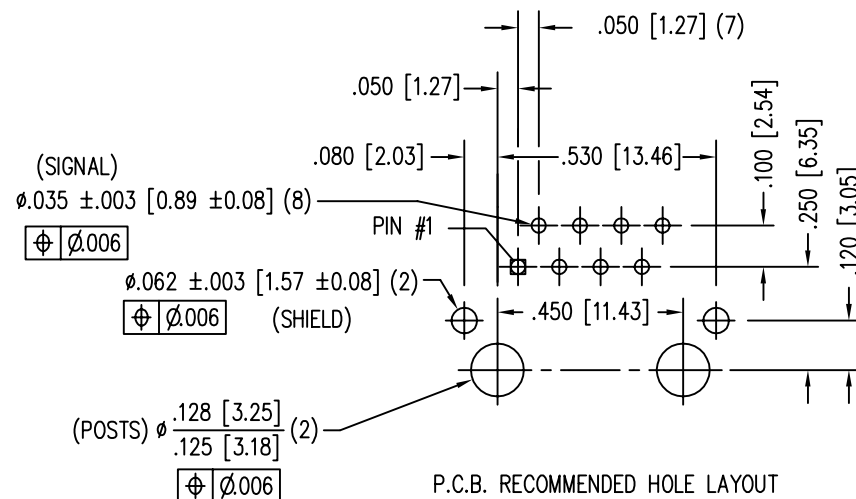
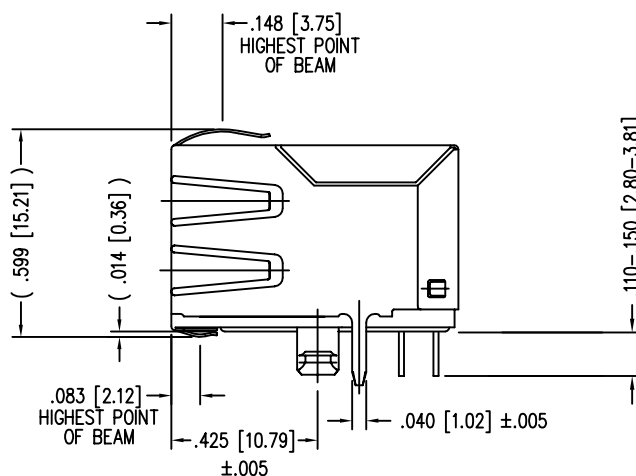
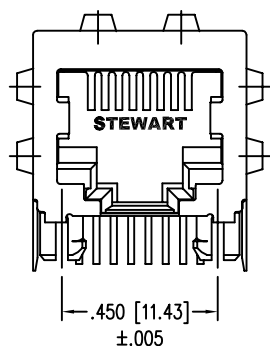
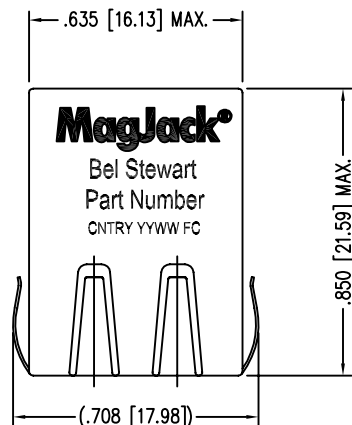
<http://www.stewartconnector.com>

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SHEET
1 OF 3

DRAWING NO.
SI-60089-F

REV.
03



P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.



NOTES:

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ± 0.005 [0.13]
5. WAVE SOLDER COMPATIBLE - PREHEAT 125°C/90SECS.

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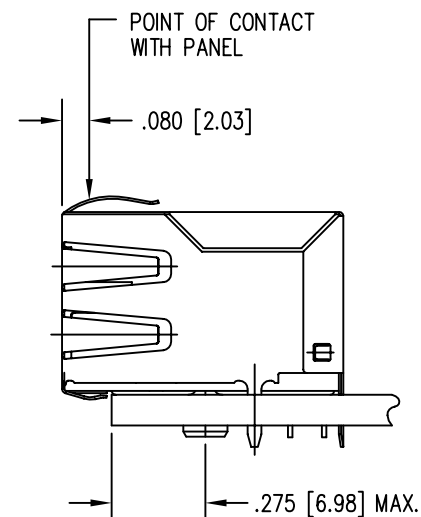
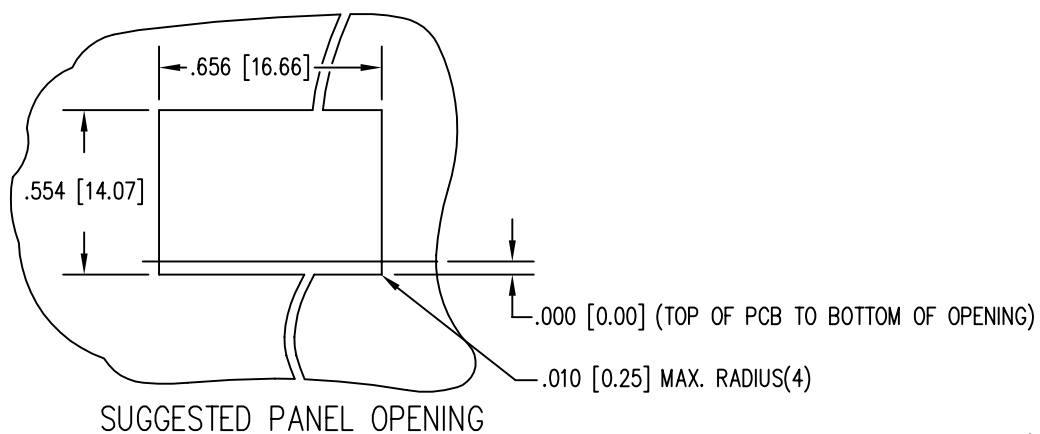
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SHEET
2 OF 3

DRAWING NO.
SI-60089-F

REV.
10



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ± 0.005 [0.13]

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