

## **DDTC (R1-ONLY SERIES) E**

#### NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

#### **Features**

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistor, R1 only
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Part Number	R1 (NOM)	Marking
DDTC113TE	1kΩ	N01
DDTC123TE	2.2kΩ	N03
DDTC143TE	4.7kΩ	N07
DDTC114TE	10kΩ	N12
DDTC124TE	22kΩ	N16
DDTC144TE	47kΩ	N19
DDTC115TE	100kΩ	N23
DDTC125TE	200kΩ	N25

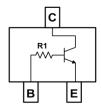
SOT523



Top View

### **Mechanical Data**

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish.
- Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.002 grams (Approximate)



Device Schematic - Top View

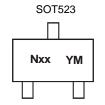
#### Ordering Information (Notes 4 & 5)

Part Number	Compliance	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTC113TE-7-F	AEC-Q101	7	8	3,000
DDTC123TE-7-F	AEC-Q101	7	8	3,000
DDTC143TE-7-F	AEC-Q101	7	8	3,000
DDTC114TE-7-F	AEC-Q101	7	8	3,000
DDTC124TE-7-F	AEC-Q101	7	8	3,000
DDTC124TEQ-7-F	Automotive	7	8	3,000
DDTC144TE-7-F	AEC-Q101	7	8	3,000
DDTC115TE-7-F	AEC-Q101	7	8	3,000
DDTC125TE-7-F	AEC-Q101	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



Nxx = Product Type Marking Code (See Table in Features) YM = Date Code Marking Y = Year (ex: C = 2015) M = Month (ex: 9 = September)

Date Code Key

Year	2015	2016	20	17	2018	2019	2020	2021	20	)22	2023	2024
Code	С	D	E		F	G	Η	ı	,	J	K	L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Emitter-Base Voltage	$V_{EBO}$	5	V
Collector Current	I <sub>C(MAX)</sub>	100	mA

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation	$P_{D}$	150	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{ hetaJA}$	833	°C/W
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-55 to +150	°C

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	50	_	_	V	$I_C = 50\mu A$
Collector-Emitter Breakdown Voltage	$BV_{CEO}$	50	_	_	V	I <sub>C</sub> = 1mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	5	_	_	V	I <sub>E</sub> = 50μA
Collector Cutoff Current	I <sub>CBO</sub>		_	0.5	μA	V <sub>CB</sub> = 50V
Emitter Cutoff Current	I <sub>EBO</sub>		_	0.5	μΑ	V <sub>EB</sub> = 4V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>		_	0.3	V	$\begin{split} &  _{C}/ _{B} = 10 \text{mA}/1 \text{mA} & \text{DDTC113TE} \\ &  _{C}/ _{B} = 5 \text{mA}/0.5 \text{mA} & \text{DDTC123TE} \\ &  _{C}/ _{B} = 2.5 \text{mA}/.25 \text{mA} & \text{DDTC143TE} \\ &  _{C}/ _{B} = 1 \text{mA}/.1 \text{mA} & \text{DDTC114TE} \\ &  _{C}/ _{B} = 5 \text{mA}/0.5 \text{mA} & \text{DDTC124TE} \\ &  _{C}/ _{B} = 2.5 \text{mA}/.25 \text{mA} & \text{DDTC144TE} \\ &  _{C}/ _{B} = 1 \text{mA}/0.1 \text{mA} & \text{DDTC115TE} \\ &  _{C}/ _{B} = .5 \text{mA}/.05 \text{mA} & \text{DDTC125TE} \\ \end{split}$
DC Current Transfer Ratio	h <sub>FE</sub>	100	250	600	_	$I_C = 1$ mA, $V_{CE} = 5$ V
Input Resistor (R <sub>1</sub> ) Tolerance	$\Delta R_1$	-30	_	+30	%	_
Gain-Bandwidth Product (Note 7)	f⊤	_	250	_	MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz

Notes: 6. Mounted on FR4 PC Board with minimum recommended pad layout.

7. Transistor only.



## Typical Curves - DDTC114TE

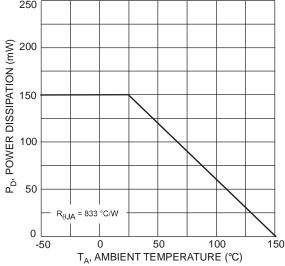


Fig. 1 Power Dissipation vs. Ambient Temperature

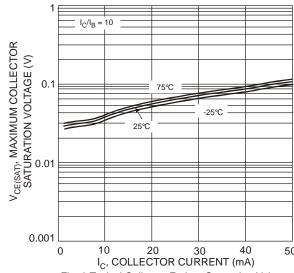


Fig. 3 Typical Collector Emitter Saturation Voltage vs. Collector Current

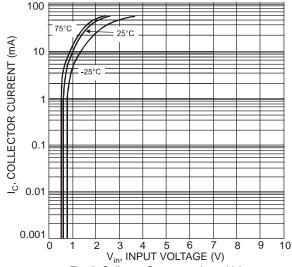


Fig. 5 Collector Current vs. Input Voltage

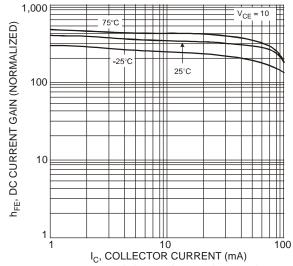


Fig. 2 Typical DC Current Gain vs. Collector Current

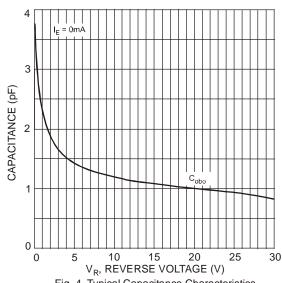


Fig. 4 Typical Capacitance Characteristics

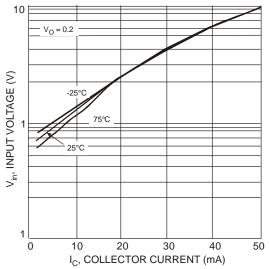
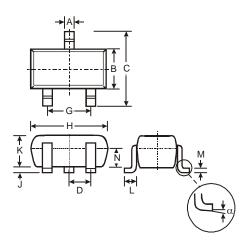


Fig. 6 Input Voltage vs. Collector Current



### **Package Outline Dimensions**

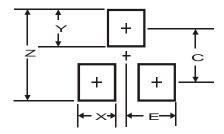
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	SOT523						
Dim	Min	Max	Тур				
Α	0.15	0.30	0.22				
В	0.75	0.85	0.80				
С	1.45	1.75	1.60				
D	_	_	0.50				
G	0.90	1.10	1.00				
Н	1.50	1.70	1.60				
J	0.00	0.10	0.05				
K	0.60	0.80	0.75				
L	0.10	0.30	0.22				
M	0.10	0.20	0.12				
N	0.45	0.65	0.50				
α	0°	8°	_				
All	All Dimensions in mm						

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.8
Х	0.4
Y	0.51
С	1.3
E	0.7





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