

TENTATIVE

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

HN3C15FU

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

Unit in mm

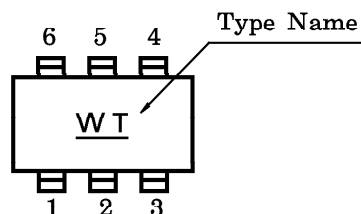
- Including Two Devices in US6 (Ultra Super Mini Type with 6 Leads)

MAXIMUM RATINGS (Ta = 25°C)

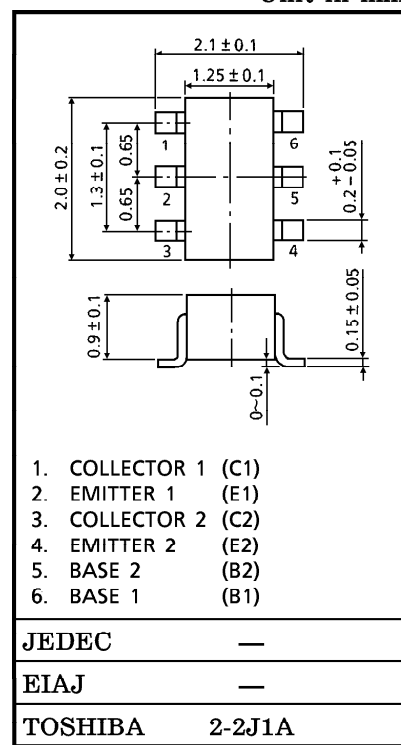
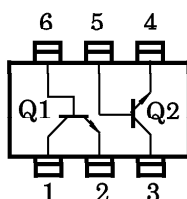
CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	V_{CB0}	15	V
Collector-Emitter Voltage	V_{CEO}	7	V
Emitter-Base Voltage	V_{EB0}	1.5	V
Collector Current	I_C	40	mA
Base Current	I_B	20	mA
Collector Power Dissipation	P_{C^*}	200	mW
Junction Temperature	T_j	125	°C
Storage Temperature Range	T_{stg}	-55~125	°C

* : Total

MARKING



PIN ASSIGNMENT (TOP VIEW)



ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	I_{CBO}	$V_{CB} = 10V, I_E = 0$	—	—	1	μA
Emitter Cut-off Current	I_{EBO}	$V_{EB} = 1V, I_C = 0$	—	—	1	μA
DC Current Gain	h_{FE}	$V_{CE} = 5V, I_C = 20mA$	50	—	160	—
Transition Frequency	f_T	$V_{CE} = 5V, I_C = 20mA$	9	12	—	GHz
Insertion Gain	$ S_{21e} ^2$	$V_{CE} = 5V, I_C = 20mA, f = 2GHz$	—	9.5	—	dB
Noise Figure	NF	$V_{CE} = 5V, I_C = 5mA, f = 2GHz$	—	1.5	3	dB
Reverse Transfer Capacitance Q1	C_{re}	$V_{CB} = 5V, I_E = 0, f = 1MHz$ (Note)	—	0.5	0.9	pF
Reverse Transfer Capacitance Q2	C_{re}		—	0.45	0.85	pF

(Note) C_{re} is measured by 3 terminal method capacitance bridge.

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