

3.3 V Single Mode 155 MBd ATM/SDH/SONET 2x9 Transceiver with Rx Monitor

V23806-A84-C5

Features

- Compliant with ATM, SONET OC-3, SDH STM-1
- Compact integrated transceiver unit with
 - MQW laser diode transmitter
 - InGaAs PIN photodiode receiver
 - Duplex SC receptacle
- Class 1 FDA and IEC laser safety compliant
- FDA Accession No. 9520890-08
- Single power supply (+3.3 V)
- Signal detect indicator
- PECL differential inputs and outputs
- Process plug included
- Wave solderable and washable with process plug inserted
- Analog Rx power monitor output
- Industry multisource 2x9 footprint
- For distances of up to 15 km on single mode fiber



Pin Configuration

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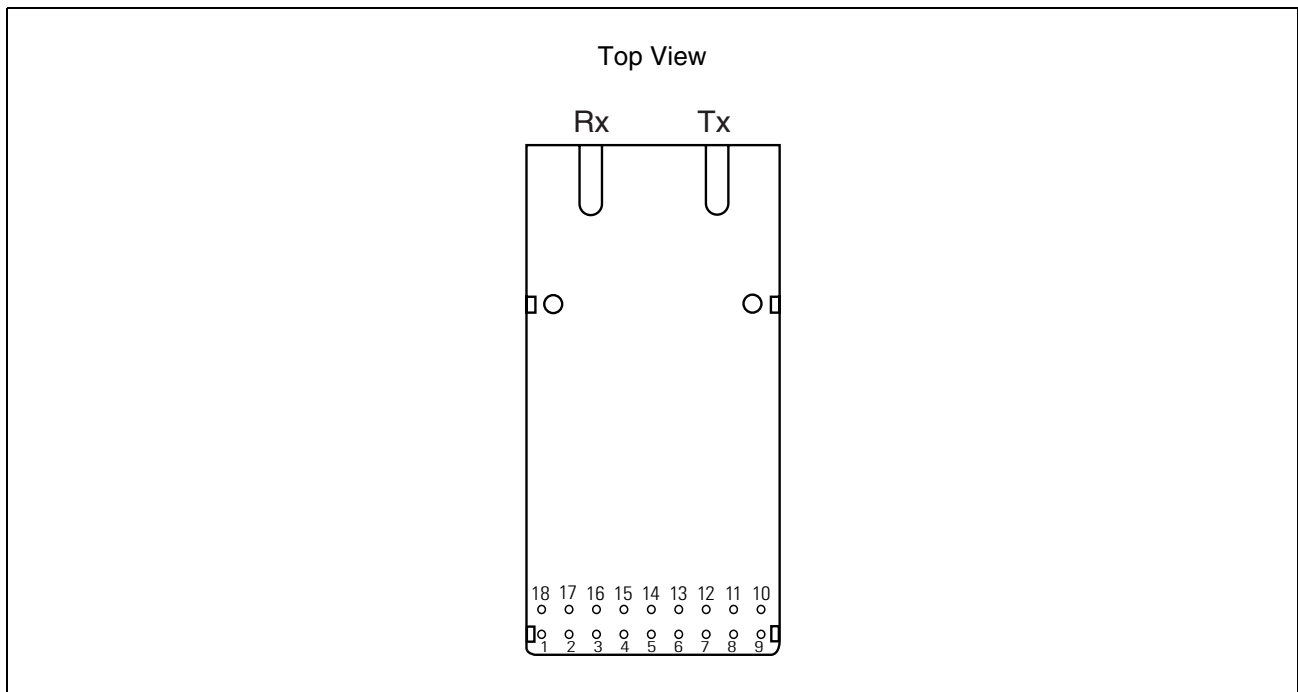
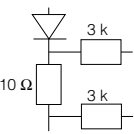


Figure 1

Pin Description

Pin No.	Symbol	Level/Logic	Function	Description
1–4	NC			Pin not connected
5 6	Bias Mon	Analog Voltage Bias Mon – Bias Mon +	Bias Monitor 	This output shows an analog voltage that is proportional to the laser bias current. Use this output to check proper laser operation and for end of life indications. Limit: Bias Current $I_{\text{BIAS}} < 60 \text{ mA}$ $I_{\text{BIAS}} = \frac{U}{10 \Omega}$
7	TxDis	TTL-Input	Tx Disable/ Enable	A low signal switches the laser on. A high signal switches the laser off.

Pin Configuration
Pin Description (cont'd)

Pin No.	Symbol	Level/Logic	Function	Description
8	RxPMon	Analog Voltage	Rx Power Monitor	This output shows an analog voltage that is proportional to the light input. Output voltage RxPMon = 2.5 V \pm 0.3 V at –20 dBm opt. input signal, and RxPMon=0.25 V \pm 0.03 V at –30 dBm opt. input signal. Source Resistance R_S = 300 k Ω .
9	TxPMon	Analog Voltage	Tx Power Monitor	This output shows an analog voltage that is proportional to the light output. This output can be used for laser safety functions. Output Voltage V_{mon} = 1.2, Source Resistance R_S = 100 k Ω
10	TxV _{EE}	Power Supply	Tx Ground	Negative power supply, normally ground
11	TxD	PECL Input	Tx Input Data	Transmitter input data
12	TxDn			Inverted transmitter input data
13	TxV _{CC}	Power Supply	Tx +3.3 V	Positive power supply, +3.3 V
14	RxV _{CC}	Power Supply	Rx +3.3 V	
15	SD	PECL Output active high	Rx Signal Detect	A high level on this output shows that optical data is applied to the optical input.
16	RxDn	PECL Output	Rx Output Data	Inverted receiver output data. If no optical input signal is present the receiver data outputs are switched to static low level (RxDn = high)
17	RxD			Receiver output data. If no optical input signal is present the receiver data outputs are switched to static low level (RxD = 0)
18	RxV _{EE}	Power Supply	Rx Ground	Negative power supply, normally ground
S1/2		Mech. Support	Stud Pin	Not connected

Description

Description

The Infineon single mode ATM transceiver complies with the ATM Forum's Network Compatible ATM for Local Network Applications document and ANSI's Broadband ISDN - Customer Installation Interfaces, Physical Media Dependent Specification, T1.646-1995, Bellcore-SONET OC-3/IR-1 and ITU-T G.957 STM-1/S.1.1.

ATM was developed to facilitate solutions in multimedia applications and real time transmission. The data rate is scalable, and the ATM protocol is the basis of the broadband public networks being standardized in the International Telecommunications Union (ITU), the former International Telegraph and Telephone Consultative Committee (CCITT). ATM can also be used in local private applications.

This Infineon single mode ATM transceiver is a single unit comprised of a transmitter, a receiver, and an SC receptacle. This design frees the customer from many alignment and PC board layout concerns. The module is designed for low cost WAN applications. It can be used as the network end device interface in workstations, servers, and storage devices, and in a broad range of network devices such as bridges, routers, and intelligent hubs, and wide area ATM switches.

This transceiver operates at 155.52 Mbit/s from a single power supply (+3.3 V). The differential data inputs, data outputs and signal detect output are PECL compatible.

Functional Description

This transceiver is designed to transmit serial data via single mode cable.

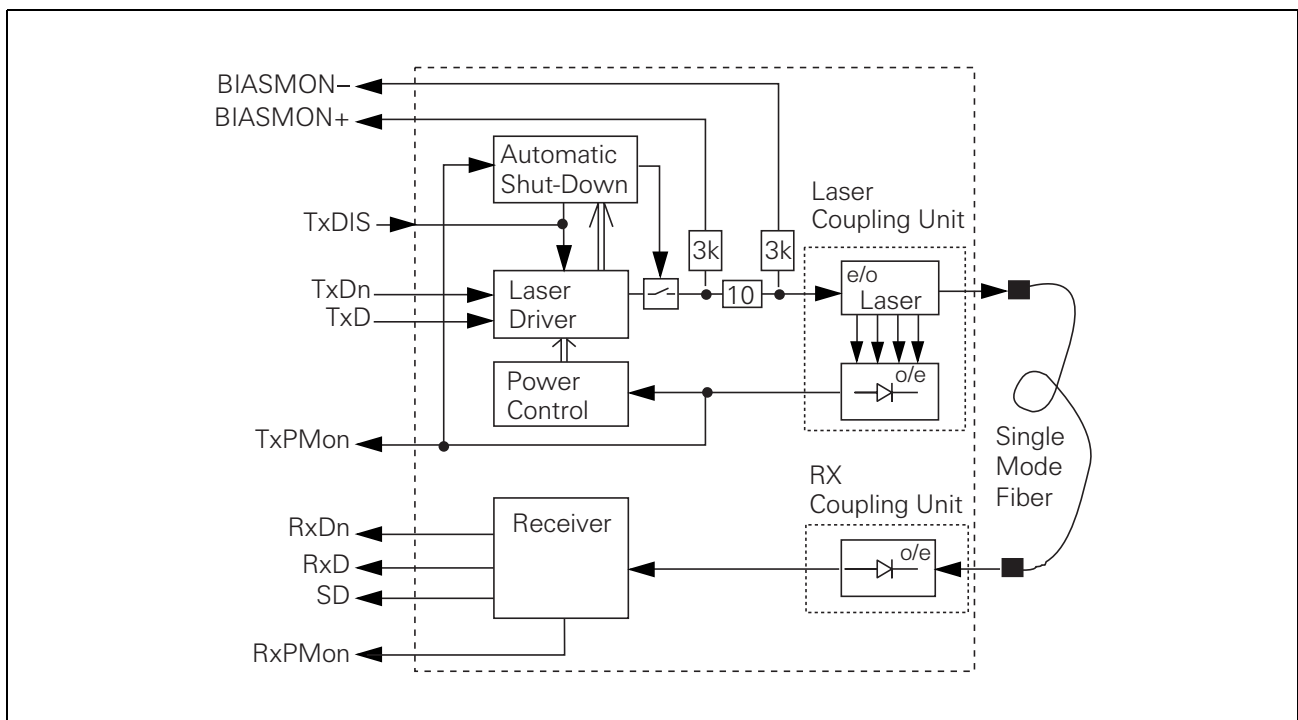


Figure 2 Functional Diagram

Description

The receiver component converts the optical serial data into PECL compatible electrical data (RxD and RxDn). The Signal Detect (SD, active high) shows whether an optical signal is present¹⁾. If no optical input signal is present the receiver data outputs are switched to static low level (RxD = 0, RxDn = high).

This transceiver offers the advantage of an analog Rx power monitor (RxPMON) that can prove and control link status, including transmission distances and security of the connection. This output shows an analog voltage that is proportional to the light input within the range of -20 dBm to -30 dBm.

The transmitter part converts electrical PECL compatible serial data (TxD and TxDn) into optical serial data. It contains a laser driver circuit that drives the modulation and bias current of the laser diode. The currents are controlled by a power control circuit to guarantee constant output power of the laser over temperature and aging.

The power control uses the output of the monitor PIN diode (mechanically built into the laser coupling unit) as a controlling signal, to prevent the laser power from exceeding the operating limits.

The laser can be switched off with a high signal on the Transmitter Disable pin (TxDis). The TxPMon pin shows a voltage reflecting the optical power output. The bias current is monitored on the BIASMON pins. Both signals can be used to supervise the function of the module.

Aging control is possible using the bias monitor output (BIASMON). The module has an integrated shutdown function that switches the laser off in the event of an internal failure.

The transceiver contains a supervisory circuit to control the power supply. This circuit generates an internal reset signal whenever the supply voltage drops below the reset threshold. It keeps the reset signal active for at least 15 milliseconds after the voltage has risen above the reset threshold. During this time the laser is inactive.

¹⁾ We recommend to switch off the transmitter supply ($V_{CC} - Tx$) if no transmitter input data is applied.

Description

Regulatory Compliance

Feature	Standard	Comments
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD 883C Method 3015.4	Class 1 (>1000 V)
Immunity: Electrostatic Discharge (ESD) to the Duplex SC Receptacle	EN 61000-4-2 IEC 61000-4-2	Discharges of ± 15 kV with an air discharge probe on the receptacle cause no damage.
Immunity: Radio Frequency Electromagnetic Field	EN 61000-4-3 IEC 61000-4-3	With a field strength of 3 V/m rms, noise frequency ranges from 10 MHz to 1 GHz. No effect on transceiver performance between the specification limits.
Emission: Electromagnetic Interference (EMI)	FCC Class B EN 55022 Class B CISPR 22	Noise frequency range: 30 MHz to 1 GHz

Technical Data

Absolute Maximum Ratings

Parameter	Symbol	Limit Values		Unit
		min.	max.	
Package Power Dissipation ¹⁾			1.5	W
Supply Voltage	$V_{CC} - V_{EE}$		6	V
Data Input Levels			$V_{CC} + 0.5$	
Differential Data Input Voltage			2.5	
Operating Case Temperature		0	70	°C
Storage Ambient Temperature		-40	85	
Soldering Conditions Temp/Time MIL-STD 883 C Method 2003)			250/5.5	°C/s

¹⁾ For $V_{CC} - V_{EE}$ (min., max.). 50% duty cycle. The supply current does not include the load drive current of the receiver output. Add max. 45 mA for the three outputs. Load is 50 Ω to $V_{CC} - 2$ V.

Exceeding any one of these values may destroy the device immediately.

Recommended Operating Conditions

Parameter	Symbol	Limit Values			Unit
		min.	typ.	max.	
Case Temperature	T_C	0		70	°C
Power Supply Voltage	$V_{CC} - V_{EE}$	3.14	3.3	3.47	V
Supply Current ¹⁾	I_{CC}		150	230	mA

Transmitter

Data Input High Voltage	$V_{IH} - V_{CC}$	-1165		-880	mV
Data Input Low Voltage	$V_{IL} - V_{CC}$	-1810		-1475	
Input Data Rise/Fall Time, 10%–90%	t_R, t_F	0.4		1.3	ns
TxDis Input High Voltage	V_{TIH}	2.0			V
TxDis Input Low Voltage	V_{TIL}			0.8	

Receiver

Output Current	I_O			25	mA
Input Center Wavelength	λ_C	1260		1360	nm

¹⁾ For $V_{CC} - V_{EE}$ (min., max.). 50% duty cycle. The supply current does not include the load drive current of the receiver output. Add max. 45 mA for the three outputs. Load is 50 Ω to $V_{CC} - 2$ V.

Technical Data

The electro-optical characteristics described in the following tables are valid only for use under the recommended operating conditions.

Transmitter Electro-Optical Characteristics

Parameter	Symbol	Limit Values			Unit
		min.	typ.	max.	
Output Power (Average)	P_O	-15.0	-11.0	-8.0	dBm
Center Wavelength	λ_C	1274		1356	nm
Spectral Width, RMS	σ_I			2.5	
Output Rise/Fall Time, 20%–80%	t_R, t_F			0.4	ns
Extinction Ratio (Dynamic)	ER	8.2	12		dB
Eye Diagram ¹⁾	ED				
Reset Threshold for Tx V_{CC} ²⁾	V_{TH}	2.85	2.93	3.0	V
Reset Active Time Out ²⁾	t_{RES}	15	22	35	ms

¹⁾ Transmitter meets ANSI T1E1.2, SONET OC-3, and ITU-T G.957 mask patterns.

²⁾ Laser power is shut down if power supply is below V_{TH} and switched on if power supply is above V_{TH} after t_{RES} . The supervisory circuit can be reset by switching TxDis from high to low.

Receiver Electro-Optical Characteristics

Parameter	Symbol	Limit Values			Unit
		min.	typ.	max.	
Sensitivity (Average Power) ¹⁾	P_{IN}		-34	-31	dBm
Saturation (Average Power)	P_{SAT}	-8.0	-3.0		
Signal Detect Assert Level ²⁾	P_{SDA}		-38	-34	
Signal Detect Deassert Level ³⁾	P_{SDD}	-44	-40		
Signal Detect Hysteresis	$P_{SDA} - P_{SDD}$		2		dB
Signal Detect Assert Time	t_{ASS}		1		ms
Signal Detect Deassert Time	t_{DAS}		5		
PECL Output Low Voltage ⁴⁾	$V_{OL} - V_{CC}$	-1950		-1630	mV
PECL Output High Voltage ⁴⁾	$V_{OH} - V_{CC}$	-1025		-735	
Output Data, Rise/Fall Time, 20%–80%	t_R, t_F		0.7	1	ns
Output SD, Rise/Fall Time				40	

¹⁾ Minimum average optical power at which the BER is less than 1×10^{-10} . Measured with a $2^{23} - 1$ NRZ PRBS as recommended by ANSI T1E1.2, SONET OC-3, and ITU-T G.957.

²⁾ An increase in optical power of data signal above the specified level will cause the SIGNAL DETECT to switch from a Low state to a High state.

³⁾ A decrease in optical power of data signal below the specified level will cause the SIGNAL DETECT to switch from a High state to a Low state.

⁴⁾ PECL compatible. Load is 50Ω into $V_{CC} - 2 \text{ V}$. Measured under DC conditions at 25°C . For dynamic measurements a tolerance of 50 mV should be added. $V_{CC} = +3.3 \text{ V}$.

Eye Safety

Eye Safety

This laser based single mode transceiver is a Class 1 product. It complies with IEC 60825-1 and FDA 21 CFR 1040.10 and 1040.11.

To meet laser safety requirements the transceiver shall be operated within the Absolute Maximum Ratings.

Attention: All adjustments have been made at the factory prior to shipment of the devices. No maintenance or alteration to the device is required. Tampering with or modifying the performance of the device will result in voided product warranty.

Note: Failure to adhere to the above restrictions could result in a modification that is considered an act of “manufacturing,” and will require, under law, recertification of the modified product with the U.S. Food and Drug Administration (ref. 21 CFR 1040.10 (i)).

Laser Data

Wavelength	1300 nm
Total output power (as defined by IEC: 50 mm aperture at 10 cm distance)	less than 2 mW
Total output power (as defined by FDA: 7 mm aperture at 20 cm distance)	less than 180 μ W
Beam divergence	4°

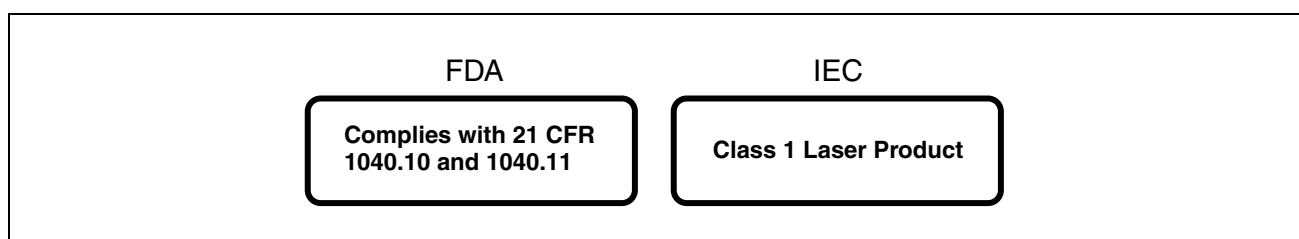


Figure 3 Required Labels

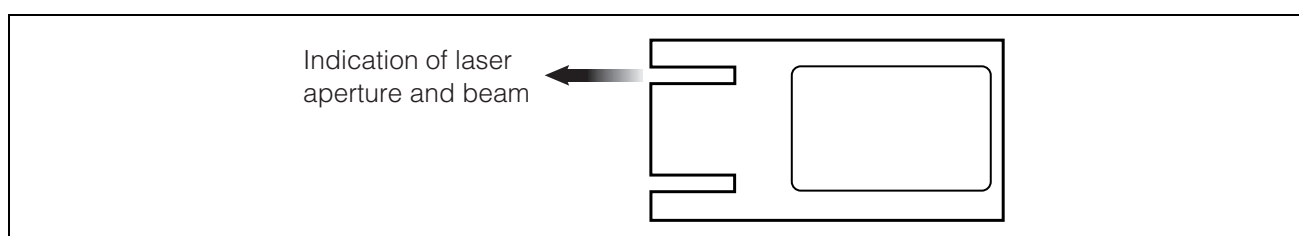


Figure 4 Laser Emission

Application Notes

Single Mode 155 MBd ATM 2x9 Transceiver

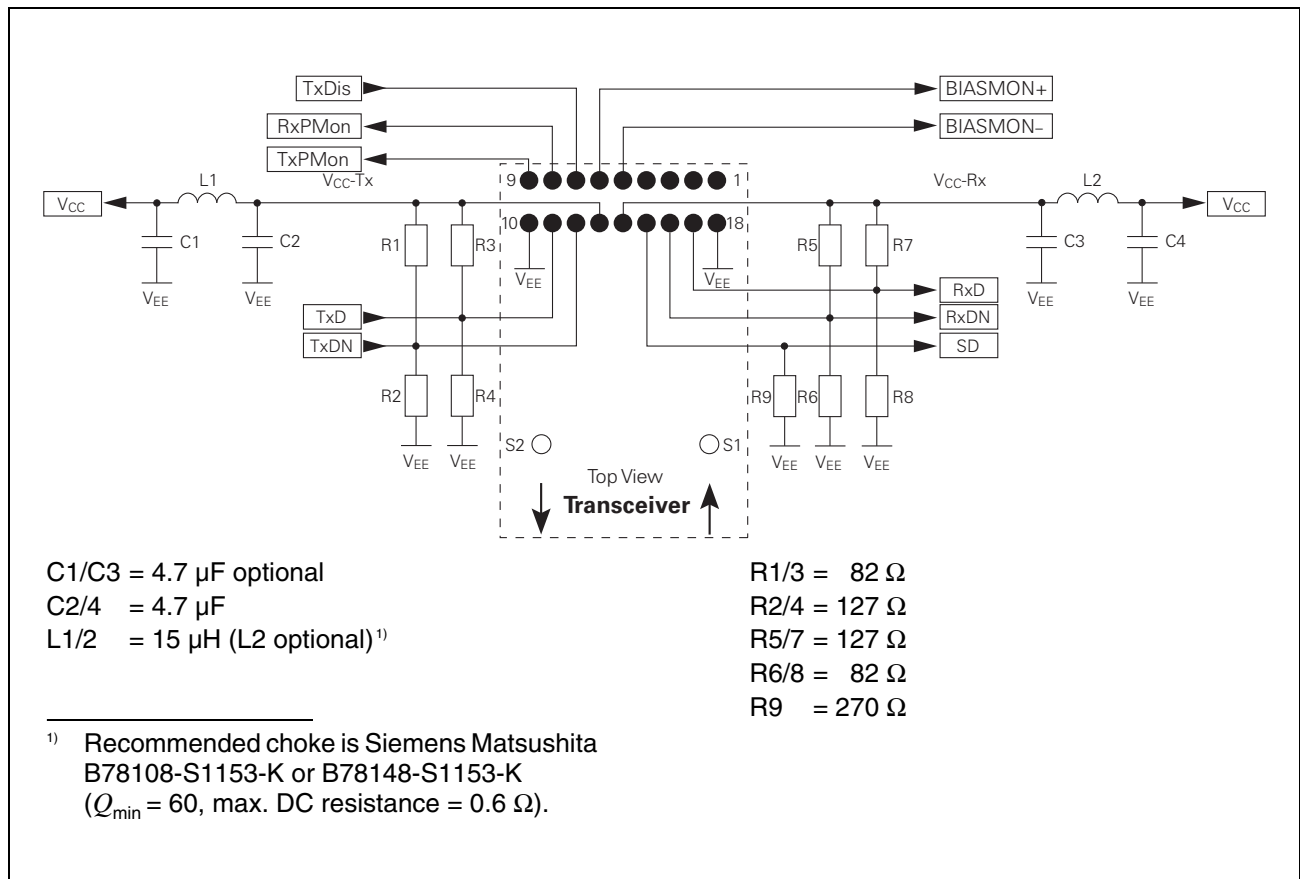


Figure 5

The power supply filtering is recommended for good EMI performance. Use short tracks from the inductor L1/L2 to the module V_{CC} -Rx/ V_{CC} -Tx. We recommend a V_{EE} plane under the module for getting good EMI performance. The transceiver contains an automatic shutdown circuit. Reset is only possible when the power is turned off, and then on again (V_{CC} Tx = 0 V). The receiver data signals should be terminated with 50 Ω at the far end. Terminations should be placed very close to the receiver data pins in combination with AC coupling.

Package Outlines

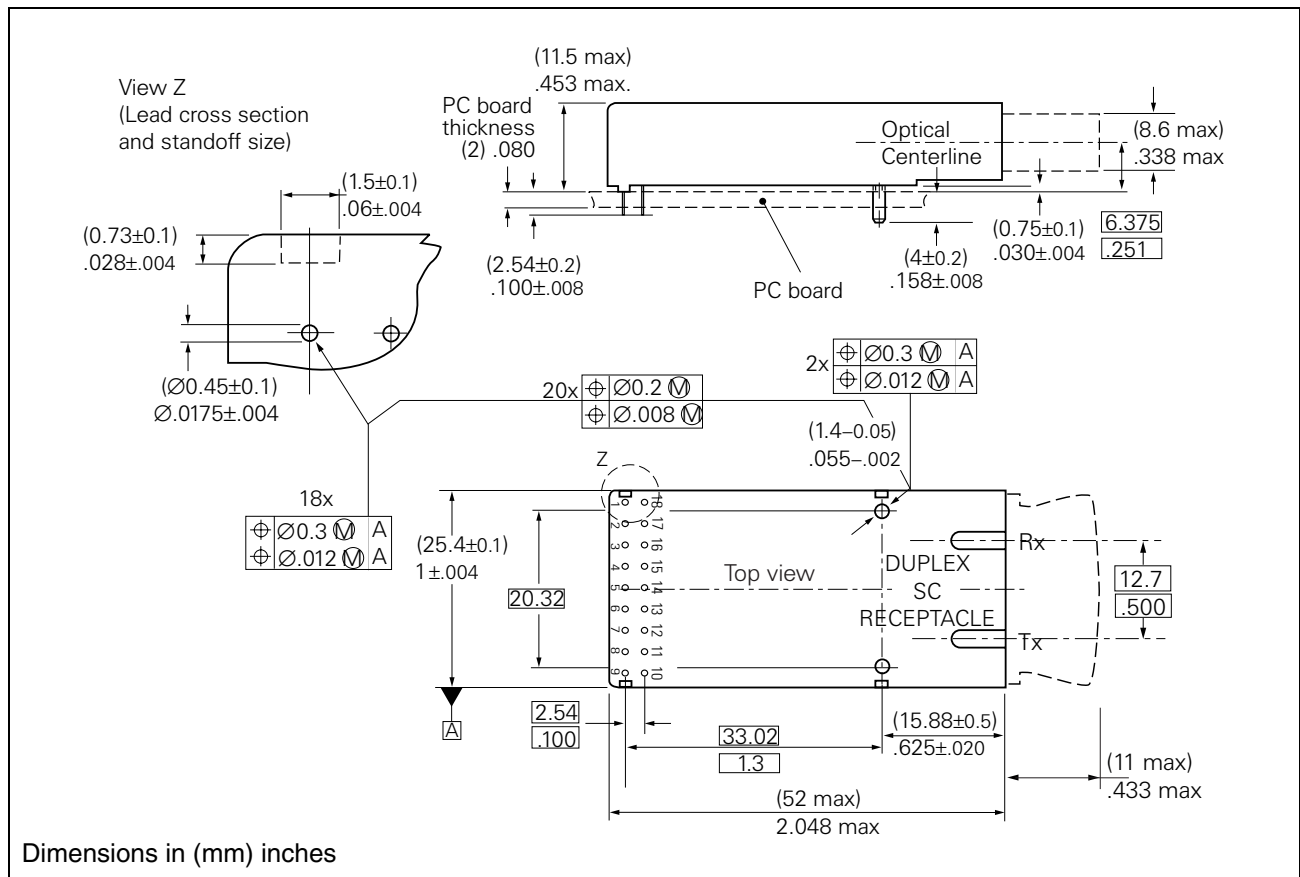


Figure 6

Previous Version:

Page	Subjects (major changes since last revision)
	Document's layout has been changed: 2002-Aug.

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