



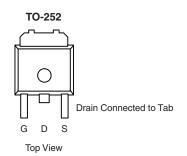
# P-Channel 60-V (D-S), 175 °C MOSFET, Logic Level

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
- 60	0.170 at V <sub>GS</sub> = - 10 V	- 10		
	0.280 at V <sub>GS</sub> = - 4.5 V	- 8		

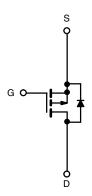
#### **FEATURES**

- TrenchFET® Power MOSFETs
- 175 °C Rated Maximum Junction Temperature





Ordering Information: SUD10P06-280L-E3 (Lead (Pb)-free)



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_C = 25  ^{\circ}C$ , unle	ess otherwise no	ted		
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Drain Current (T, = 150 °C)	T <sub>C</sub> = 25 °C		- 10		
Continuous Diam Current (1) = 130 C)	T <sub>C</sub> = 100 °C	I <sub>D</sub>	- 7		
Pulsed Drain Current		I <sub>DM</sub>	- 20	A	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	- 10		
Avalanche Current		I <sub>AS</sub>	- 10		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	5	mJ	
Maximum Davier Dissination	T <sub>C</sub> = 25 °C	В	37	10/	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2 <sup>a</sup>	W	
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Junction-to-Ambient <sup>a</sup>	FR4 Board Mount	R <sub>thJA</sub>	60	70		
Junction-to-Ambient*	Free Air		120	140	°C/W	
Junction-to-Case		R <sub>thJC</sub>	3.7	4.0	]	

#### Notes:

a. Surface Mounted on FR4 board.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm.

# SUD10P06-280L

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Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static	,						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 60				
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0	- 2.0	- 3.0	- V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			- 50	μΑ	
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			- 150		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 10			Α	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A		0.130 0.1			
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 125 °C			0.31	Ω	
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 175 °C			0.375		
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2 A		0.210	0.280		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A		6		S	
Dynamic	•						
Input Capacitance	C <sub>iss</sub>			635		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		100			
Reverse Transfer Capacitance	C <sub>rss</sub>			30			
Total Gate Charge	$Q_g$			11.5	25		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -10 \text{ A}$		3.5		nC	
Gate-Drain Charge	$Q_{gd}$			2			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			9	20		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = - 30 V, $R_L$ = 3 $\Omega$		16	20	- ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 2.5 \Omega$		17	30		
Fall Time <sup>c</sup>	t <sub>f</sub>	1		19	35		
Source-Drain Diode Ratings and Cha	racteristics	T <sub>C</sub> = 25 °C <sup>a</sup>			<u>.                                    </u>		
Pulsed Current	I <sub>SM</sub>				- 20	Α	
Forward Voltage <sup>b</sup>	$V_{SD}$	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0 V			- 1.3	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 10 A, dl/dt = 100 A/μs		50	80	ns	

#### Notes:

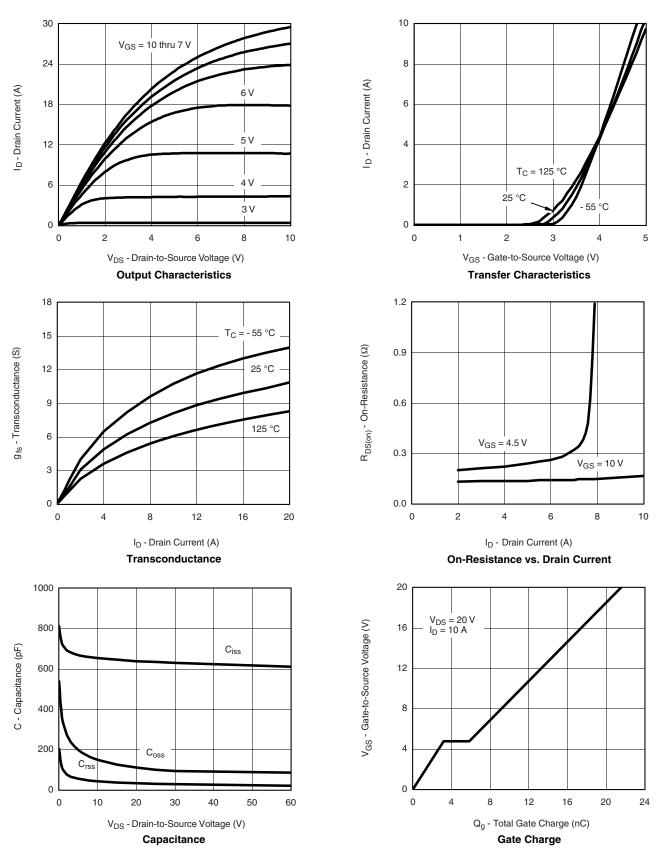
- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





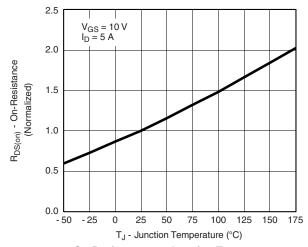
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



T<sub>J</sub> = 150 °C

T<sub>J</sub> = 150 °C

T<sub>J</sub> = 25 °C

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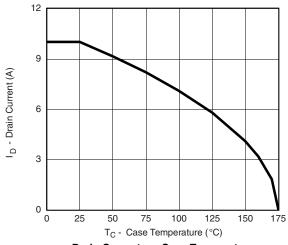
V<sub>SD</sub> - Source-to-Drain Voltage (V)

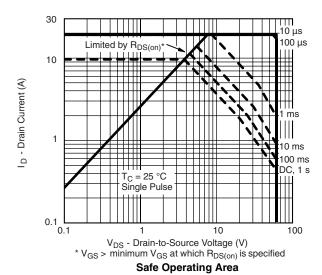
100

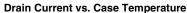
On-Resistance vs. Junction Temperature

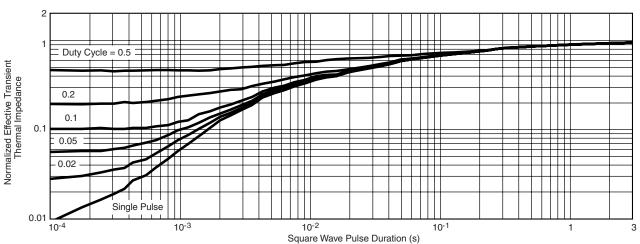
Source-Drain Diode Forward Voltage

#### THERMAL RATINGS



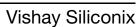






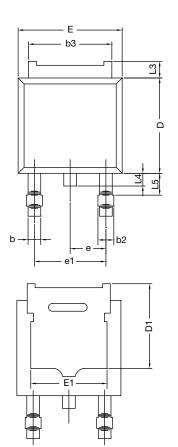
Normalized Thermal Transient Impedance, Junction-to-Case

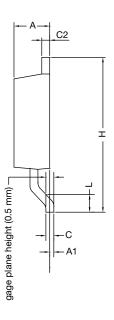
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## **TO-252AA Case Outline**



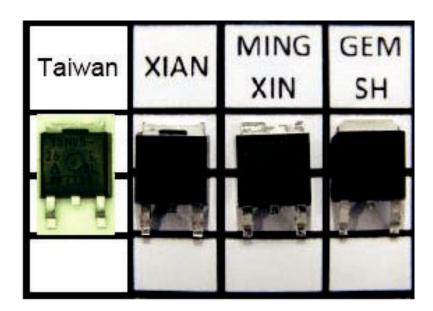


	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	4.10	-	0.161	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	=	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.01	1.52	0.040	0.060	
ECN: T13-0359-Rev. O, 03-Jun-13					

DWG: 5347

#### Notes

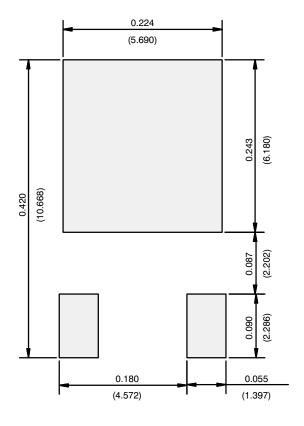
- Dimension L3 is for reference only.
- Xi'an, Mingxin, and GEM SH actual photo.



Revision: 03-Jun-13 Document Number: 71197



#### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

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Revision: 02-Oct-12 Document Number: 91000

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